## **Calorimeter Upgrade Meeting**

Analog Electronics COTS design

14/oct/2011

## **COTS** status

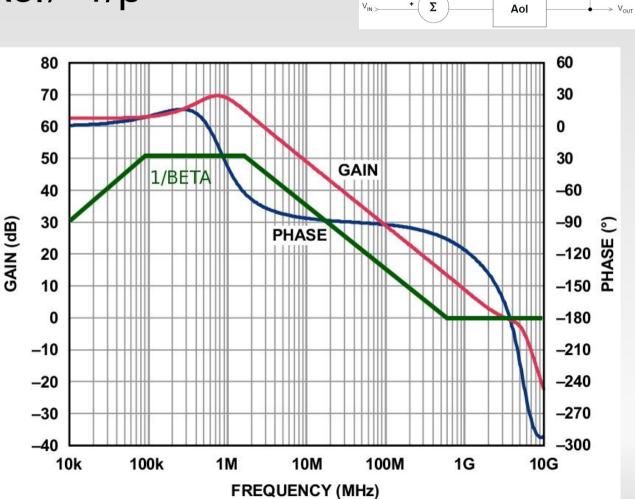
- Integrator Oscillations
- Measurements
  - Input Stage
  - Clipping Stage
  - Integrating Stage
- Conclusions/Ideas

## Integrator Oscillations

#### Integrator Oscillations

- Aol· $\beta \neq -1 \rightarrow Aol \neq -1/\beta$
- ADA4930
- Calculate  $1/\beta$  with all parasitic effects

Nearly No Phase Margin



β

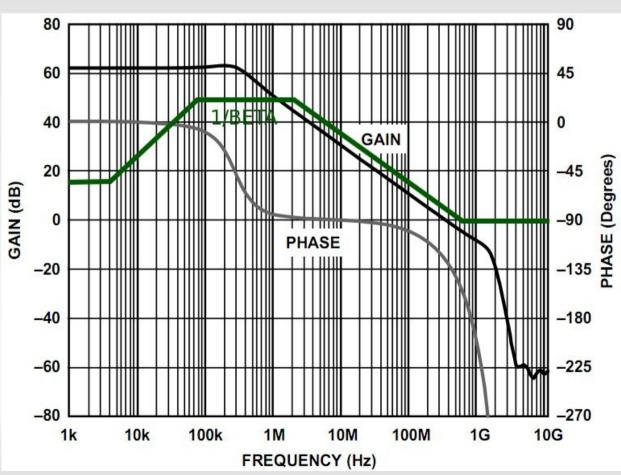
Aol

Σ

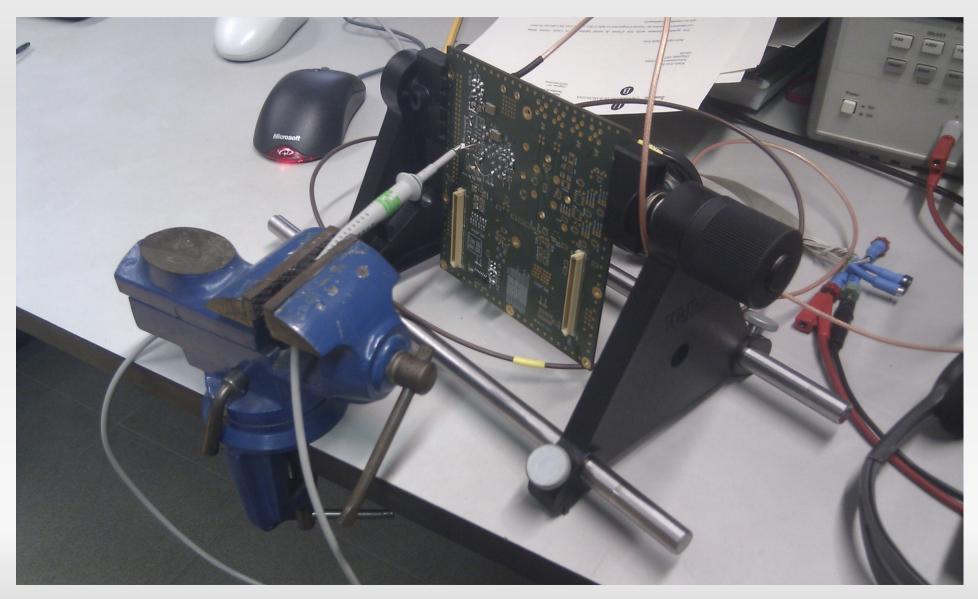
## **Integrator Oscillations**

Chage for other model looking for stability...

- ADA4932
- First Order Calculation OK!!
- Real Life Measure OK too (later shown)
- No more oscillations

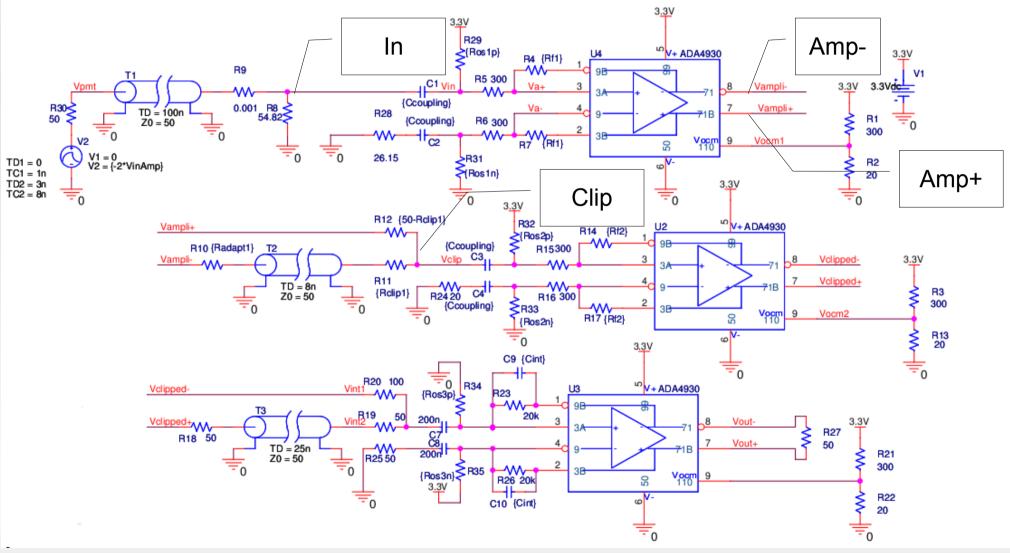


No oscillations → Continue work → Measurements



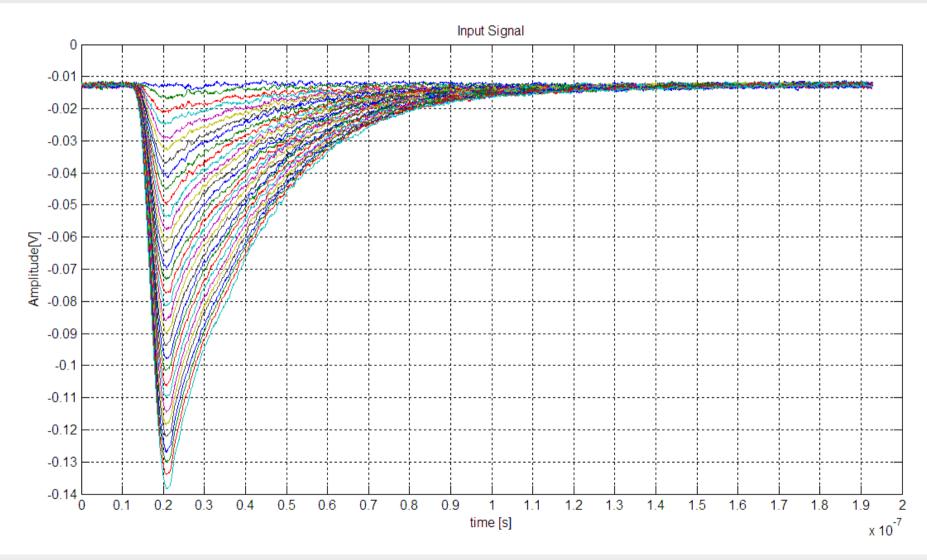
## INPUT STAGE

Input Stage Measurements



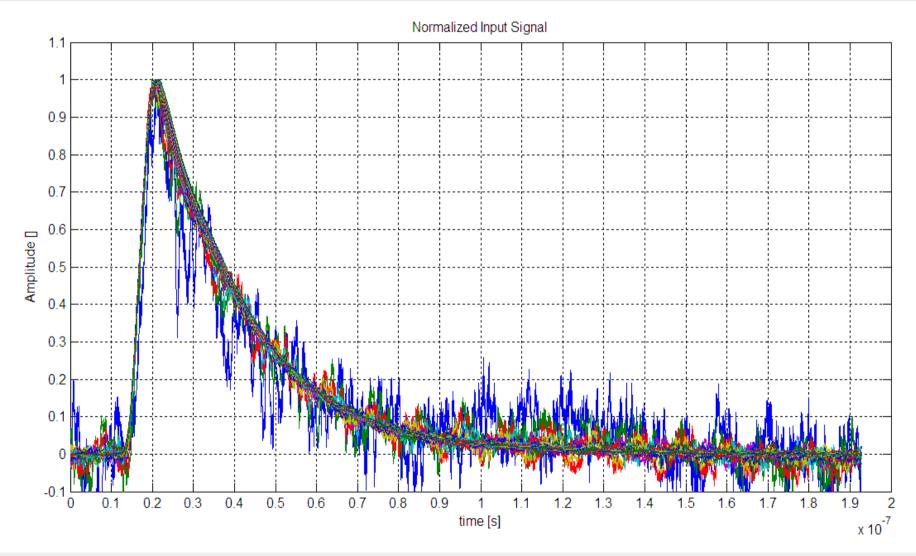
## **INPUT STAGE**

#### Input RAW DATA



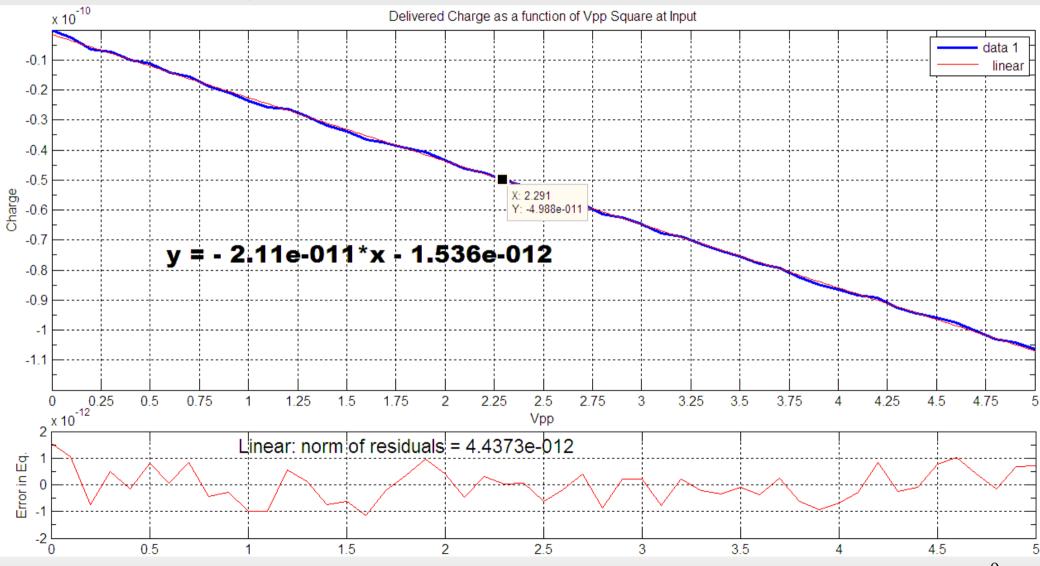
## **INPUT STAGE**

#### Input Normalized (SHAPE)



## INPUT STAGE

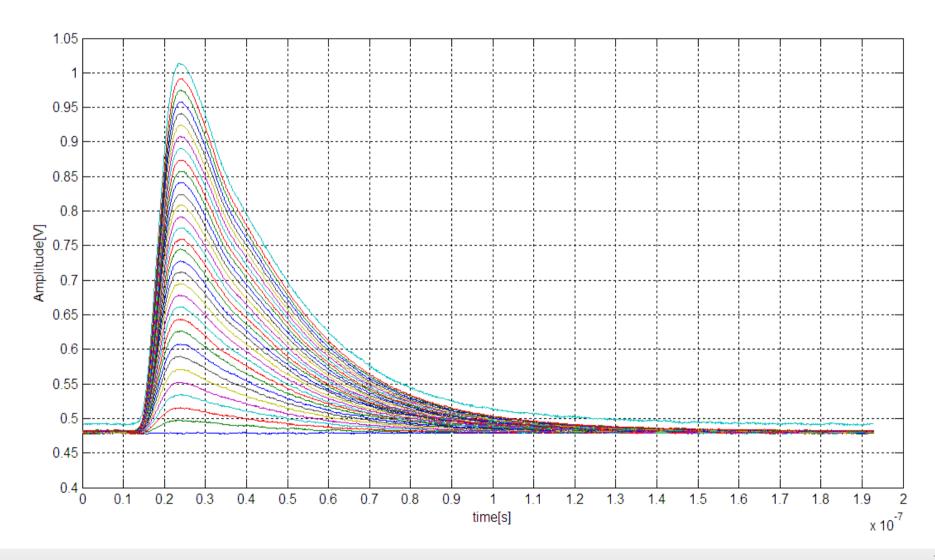
Input Linearity



9

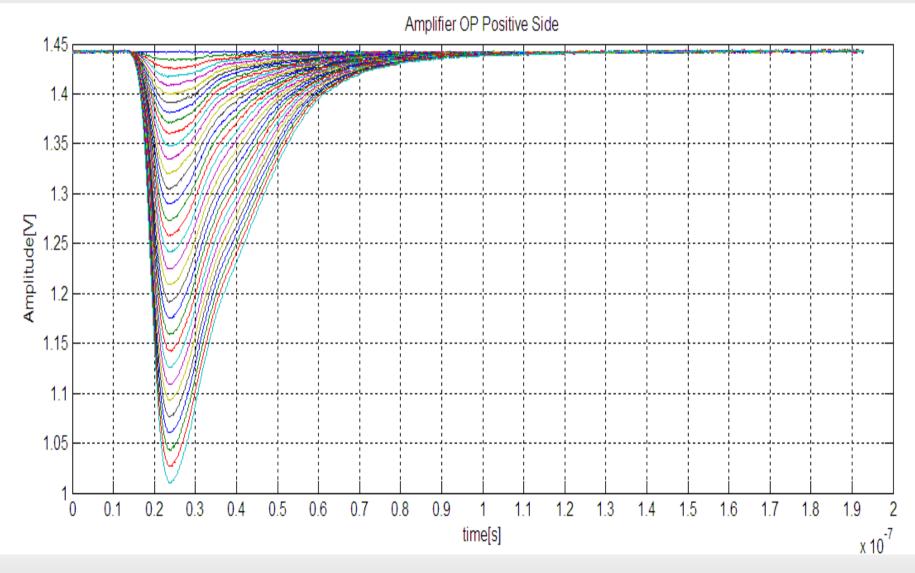
## **INPUT STAGE**

#### AMP- RAW DATA



## INPUT STAGE

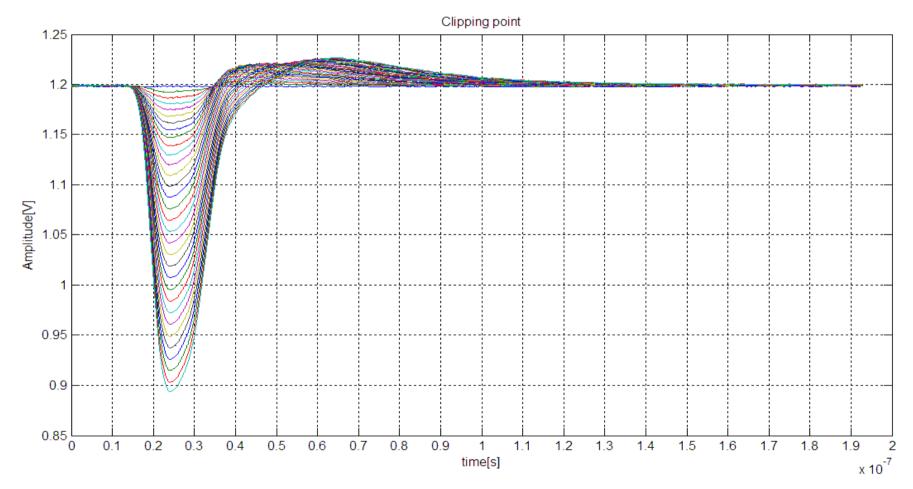
AMP+ RAW DATA



11

## **INPUT STAGE**

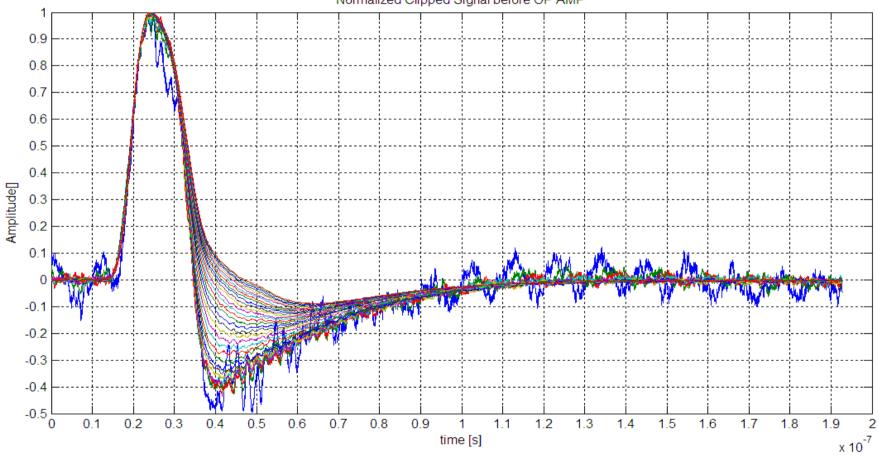
CLIP RAW DATA



¿? Why Undershoot? (RC=50\*200n=1us)

## **INPUT STAGE**

CLIP Normalized (SHAPE)

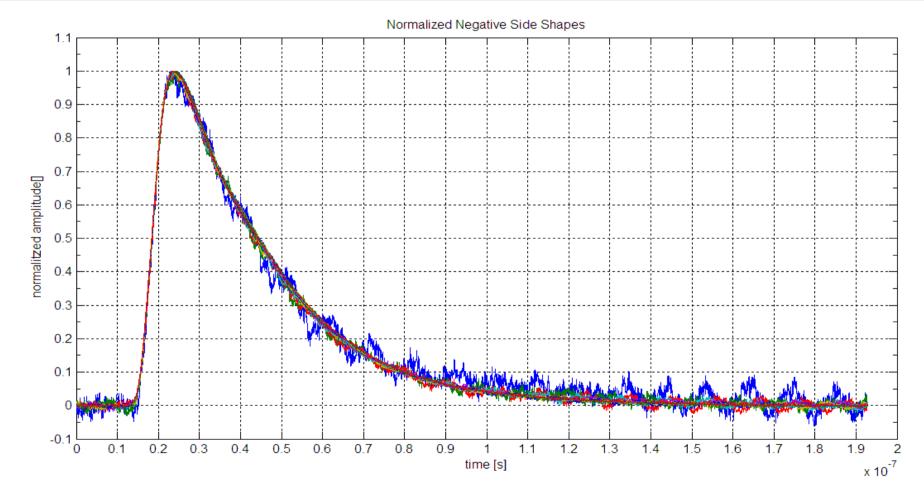


Normalized Clipped Signal before OP AMP

- Undershoot increases when amplitude decreases  $\rightarrow$ ;?
- Let's have a closer look to AMP+ and AMP- at 0.4E-7s

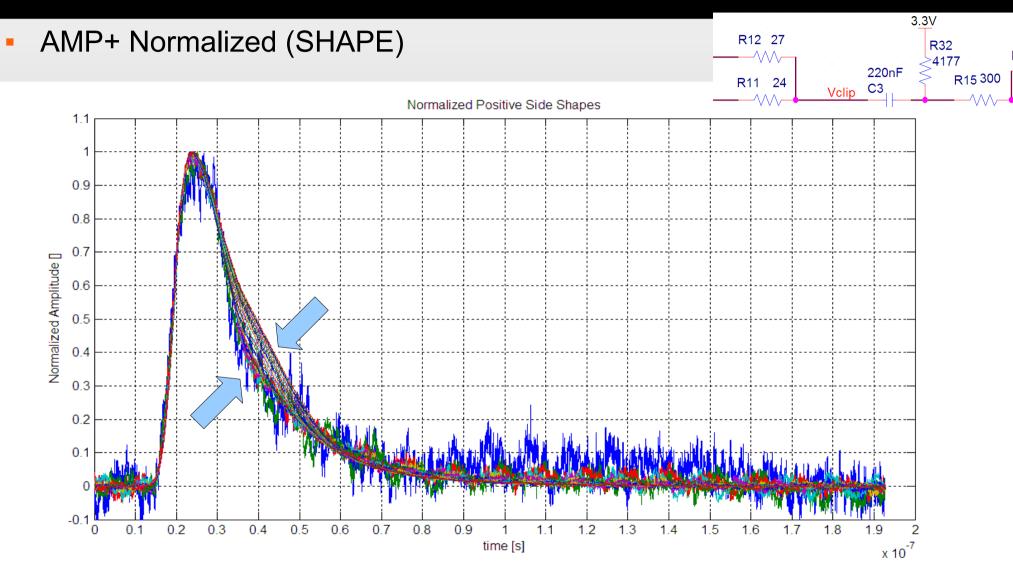
## **INPUT STAGE**

AMP- Normalized (SHAPE)



Looks good

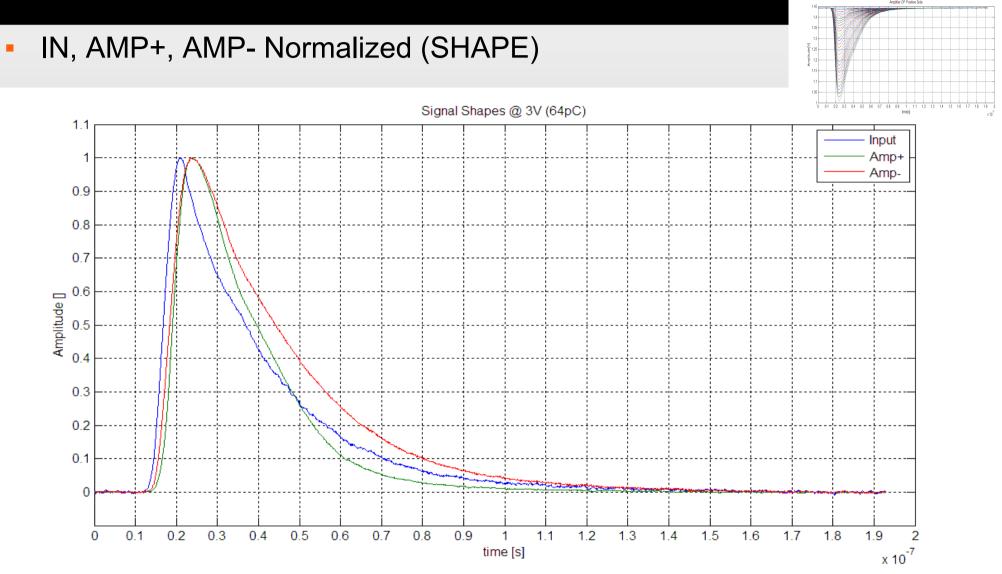
## **INPUT STAGE**



- Variations at 0.4E-7s depending on amplitude
- Maybe because of RC differences?

## **INPUT STAGE**

16

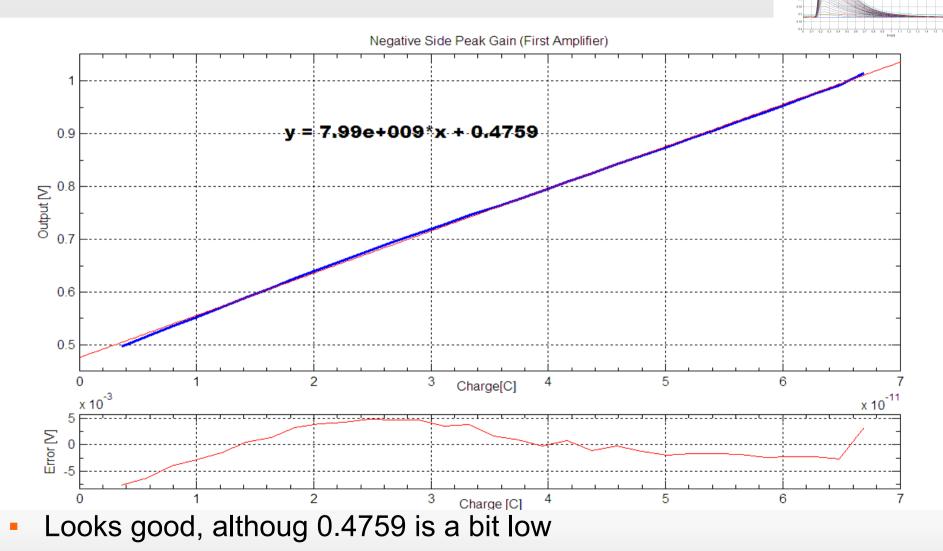


Different shapes of AMP+ and AMP-

• Ideal (Simulated) clipping with these shapes  $\rightarrow$  measured result

# INPUT STAGE

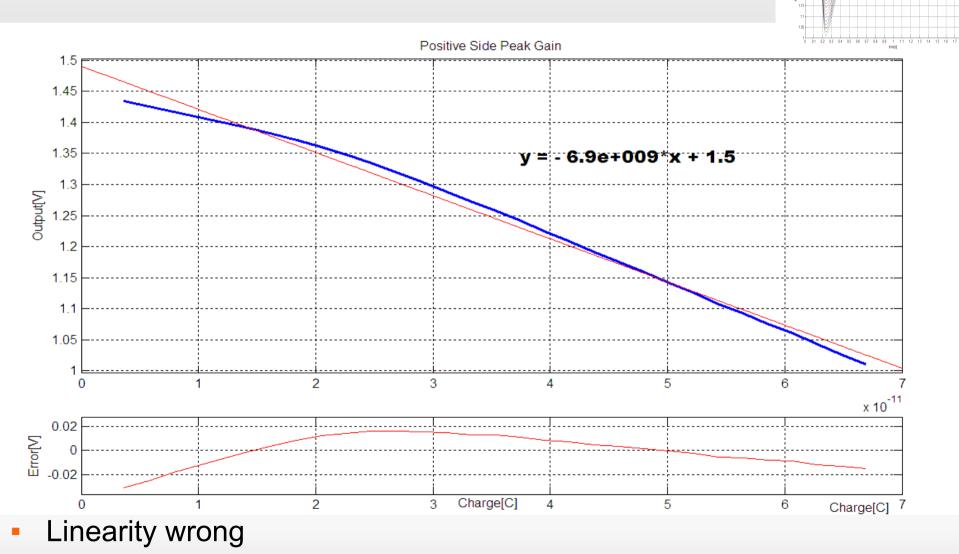
AMP- Transfer Function MAX\_Out(Charge\_in)



Error about 5E-3/5E-1\*100=1% (Compatible with IN precision)

## **INPUT STAGE**

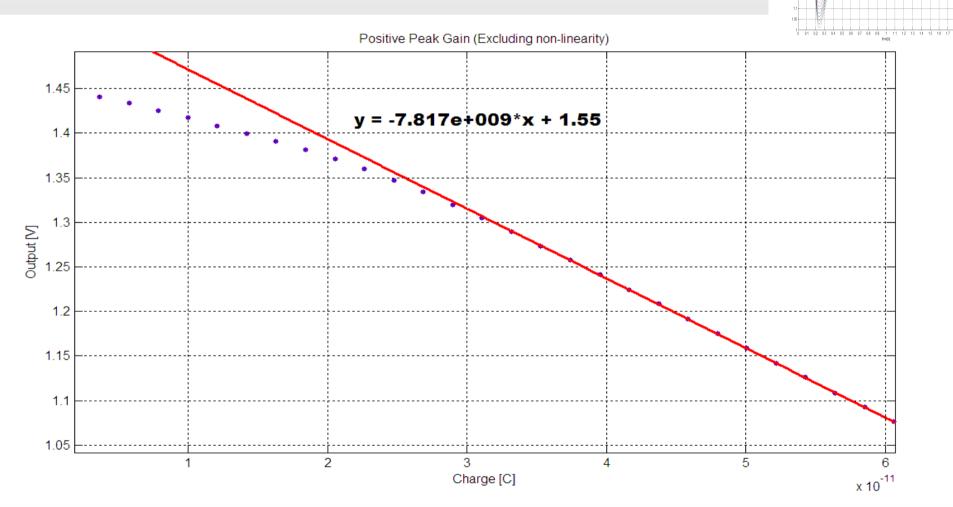
AMP+ Transfer Function MAX\_Out(Charge\_in)



Linear regression useless (need to exclude conflictive zone)

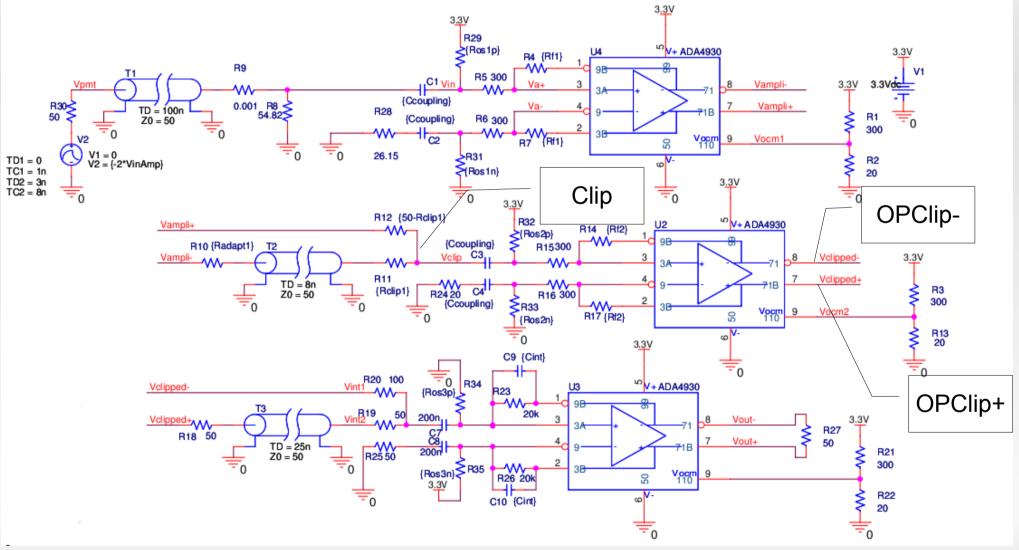
## INPUT STAGE

AMP+ Transfer Function MAX\_Out(Charge\_in)

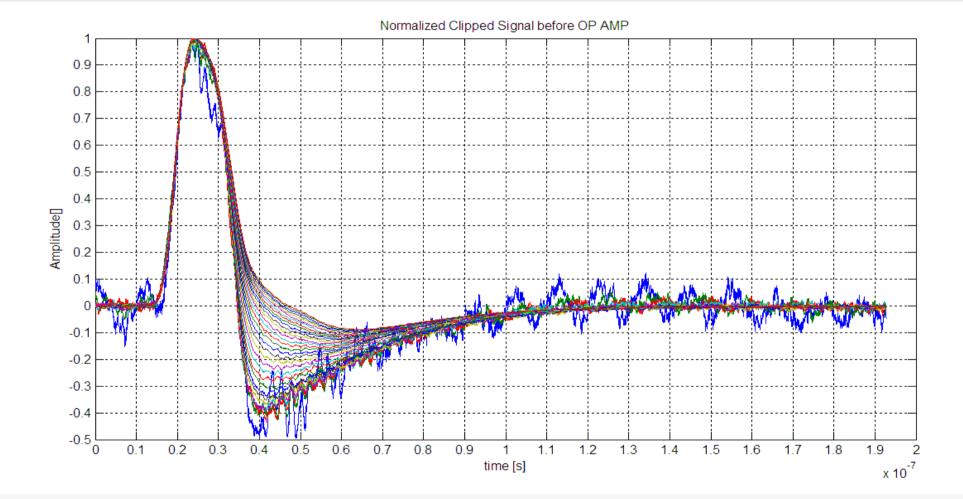


- It would like to have a 1.55V at 0 input  $\rightarrow$  too high! (Max=1.45)
- Related to different shapes? Two different errors?

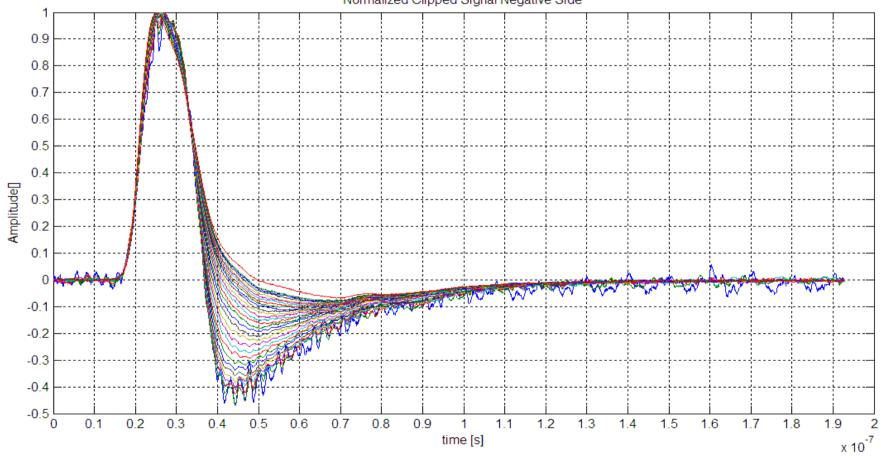
Input Stage Measurements



Clip Signal Normalized (SHAPE) ← RECALLING



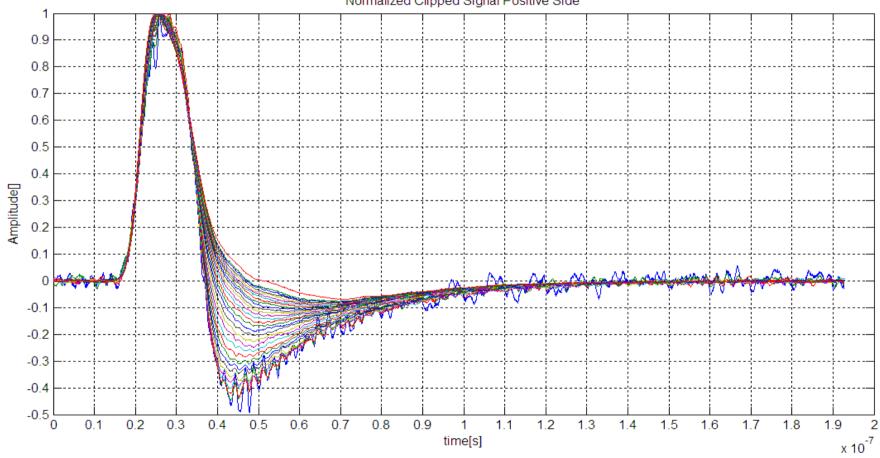
OPClip- Normalized (SHAPE)



Normalized Clipped Signal Negative Side

- Same Shape approx
- Linear to the eye

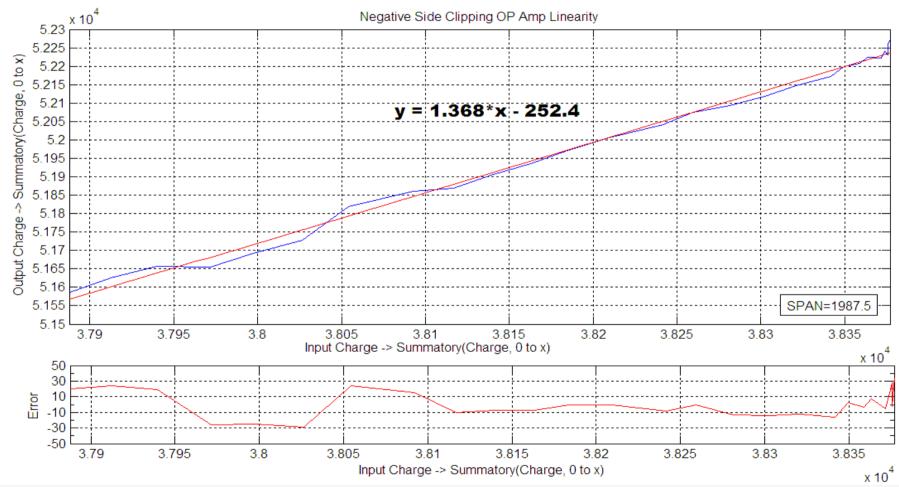
OPClip+ Normalized (SHAPE)



Normalized Clipped Signal Positive Side

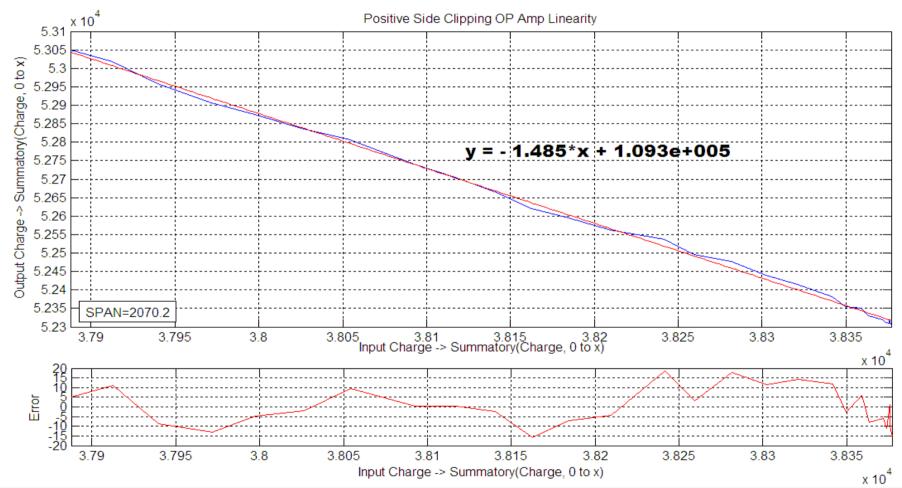
- Same Shape approx
- Linear to the eye NEED A BETTER WAY TO MEASURE

OPClip- Gain



Reasonable Linearity taking noise into account

OPClip+ Gain

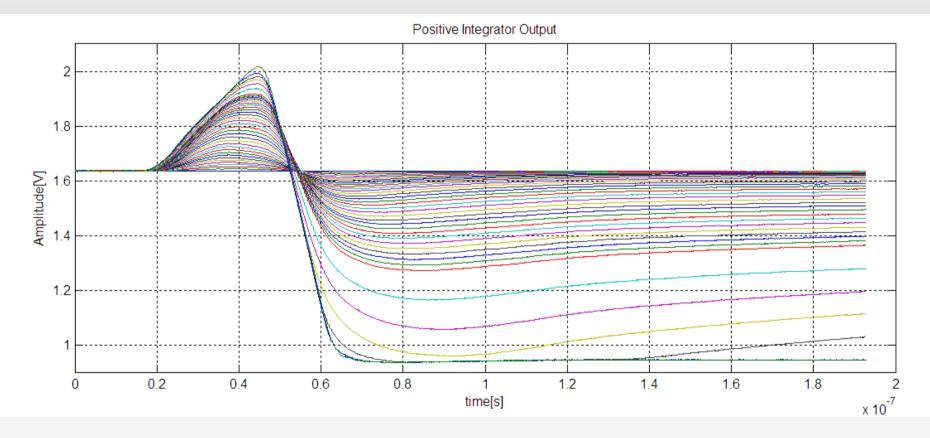


Reasonable Linearity taking noise into account

But different gain? 1.368 vs. 1.485?

## INT. STAGE

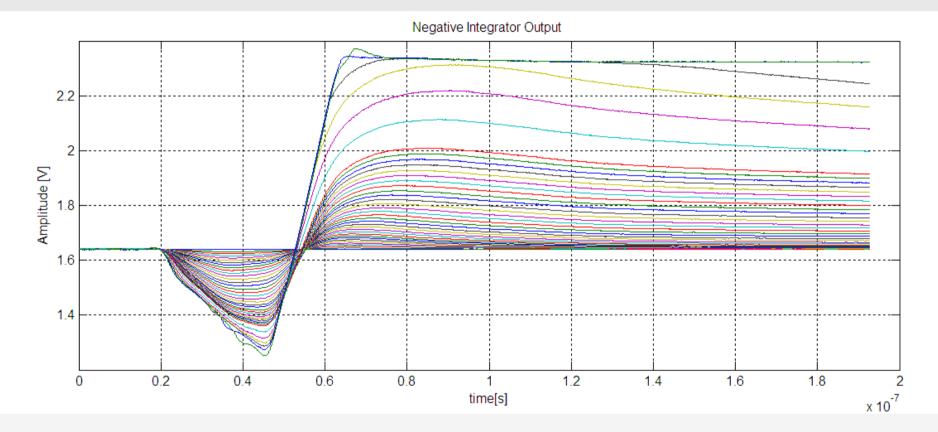
Integrator Positive Output



Need to understand why

## INT. STAGE

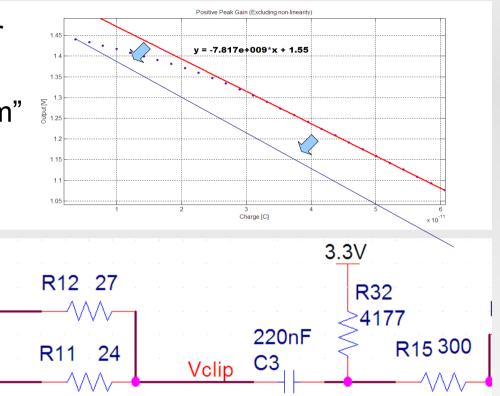
Integrator Negative Output



Need to understand why

## **Conclusions/Ideas**

- INPUT STAGE:
  - Need to tune input to have lower offset
  - Need to understand "RC problem"
- Will offset solve it all?
  - Test and see
- If not:
  - Cut DC path between outputs
  - Reduce capacitor
  - Increase next stage impedance



## **Conclusions/Ideas**

#### CLIPPING STAGE:

- Performs reasonably from linearity point of view
- INTEGRATING STAGE:
  - Oscillation solved
  - Integrator Output not understood
    - Need to simulate with captured data
      - (Numerical Integration)

- GENERAL:
  - Need to use a more precise source
  - Perform noise tests with ADC
  - Possibly change the ADA4930 with ADA4932 → Bipolar supply with no offsets. +3V and -3V → much more dynamic range