

Micro-strip based detection systems: advances and new technological developments

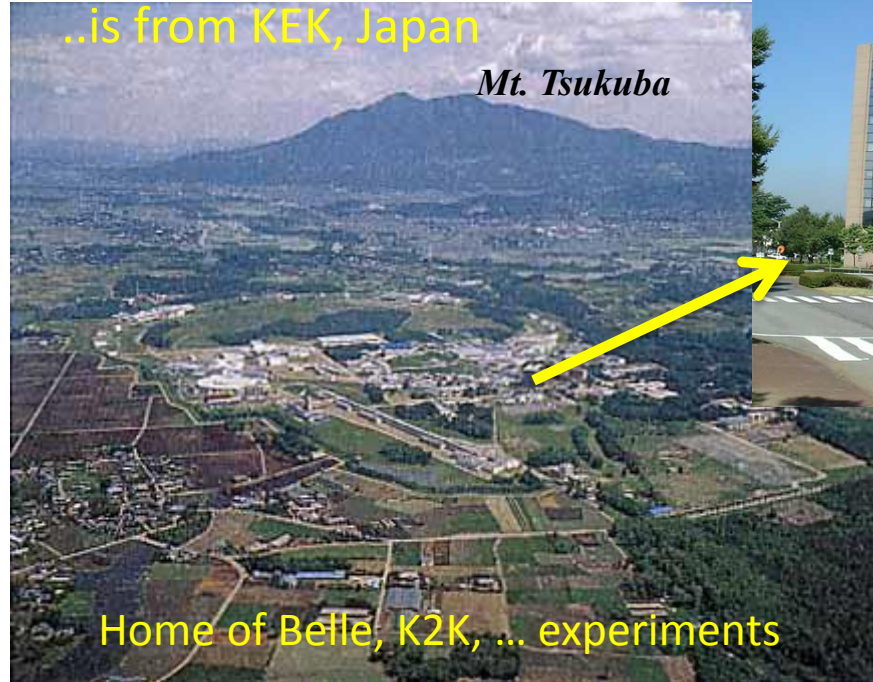
Yoshinobu Unno
KEK

Your Lecturer



Yoshinobu UNNO

Your Lecturer



..is from KEK, Japan

Mt. Tsukuba

Home of Belle, K2K, ... experiments



.. his office

Yoshinobu UNNO
Professor of KEK

Your Lecturer



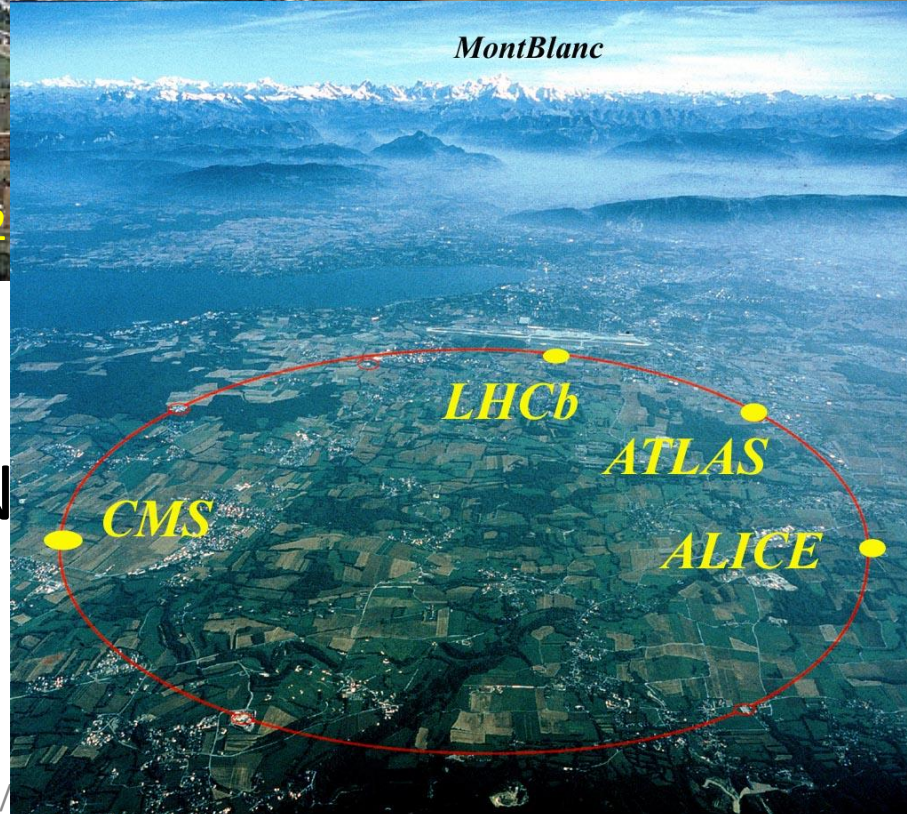
..is from KEK, Japan

Mt. Tsukuba

.. his office



Home of Belle, K2



MontBlanc

LHCb

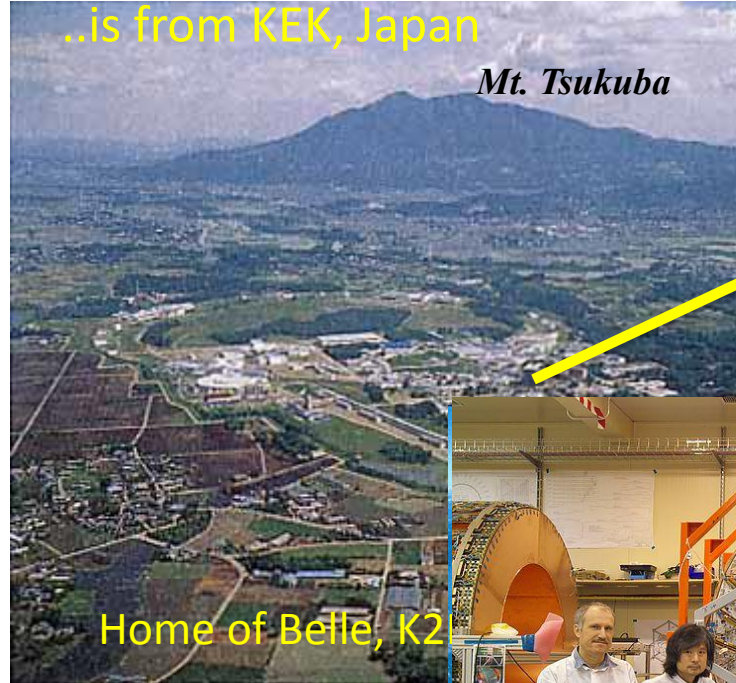
ATLAS

ALICE

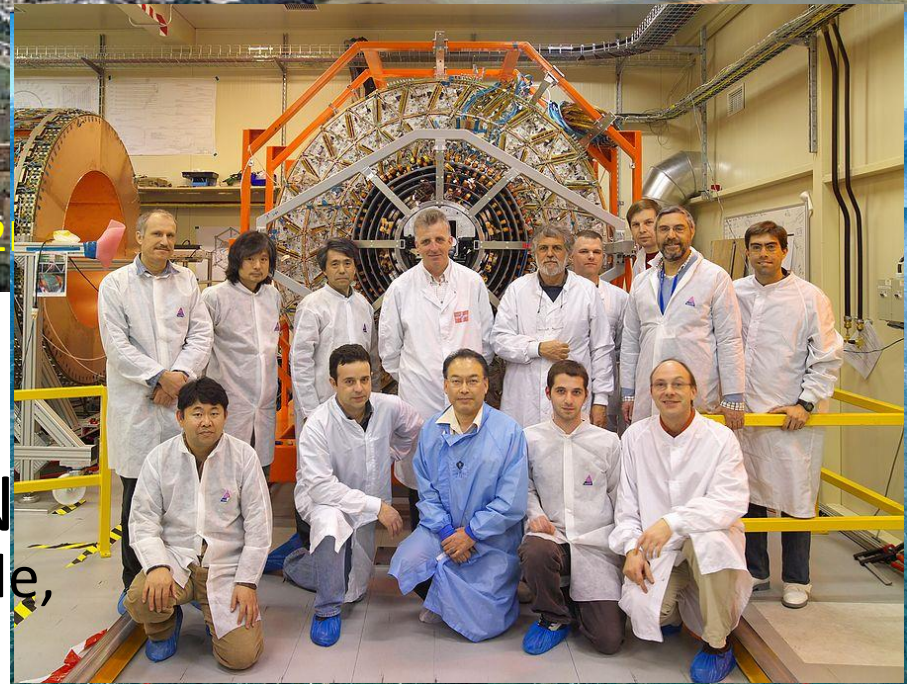
CMS

Yoshinobu UNNO
Professor of KEK
ATLAS experiment at CERN

Your Lecturer



Yoshinobu UNNO
Professor of KEK
ATLAS experiment at CERN
Design, construction, ... and upgrade,
of the silicon tracker
- together with many colleagues
- for the great discovery



Japan - Wonderland



One of the advantage is the industry, specially electronics

Japan - Wonderland



Big electronics giants like HITACHI, ...

Japan - Wonderland



Big electronics giants like HITACHI, SONY, ...

Japan - Wonderland

AKIhabara

HAMAMATSU
Photon is our business

SOLID STATE DIVISIONS ELECTRON TUBE DIVISIONS LASER GROUP SYSTEM DIVISIONS

The advertisement features a blue background with a glowing blue line and a small globe. Below the main text are four circular images showing various electronic components and devices, each labeled with its respective division name.



SONY
make.believe
TELEVISION AND A BLU-RAY PLAYER

The advertisement shows a black Sony television set on a white background.

HITACHI
Inspire the Next

私たちが日立グループは拍レイソルを応援しています。
「日立の樹」 www.hitachinoki.net

The advertisement features a large green tree in a field under a blue sky. A small orange character is visible at the base of the tree.

But, we have benefitted from a smaller company ...

Japan - Wonderland



An advertisement for HAMAMATSU. The word 'HAMAMATSU' is written in large, bold, red letters at the top. Below it, the word 'business' is written in a white, cursive font. The background is dark blue with a glowing globe and a circuit board. In the center, there is a large, glowing, spherical object, possibly a light bulb or a piece of scientific equipment, with a red coiled cable attached to it. The words 'SOLID STATE' are visible at the bottom left of the advertisement.

Nobel prize for opening the
Neutrino astronomy

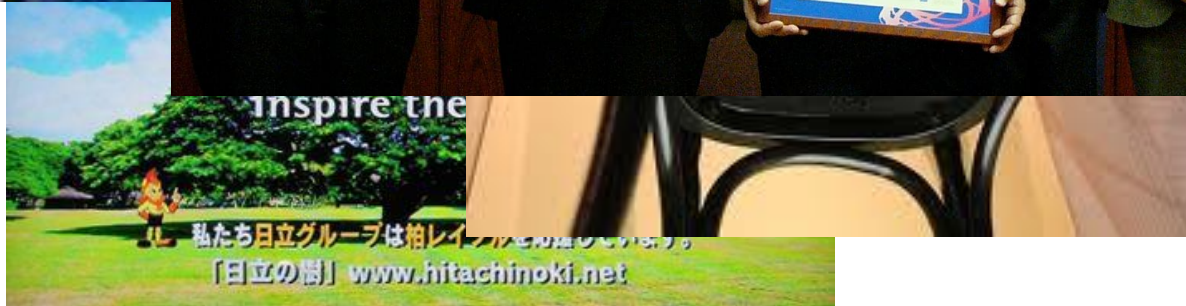
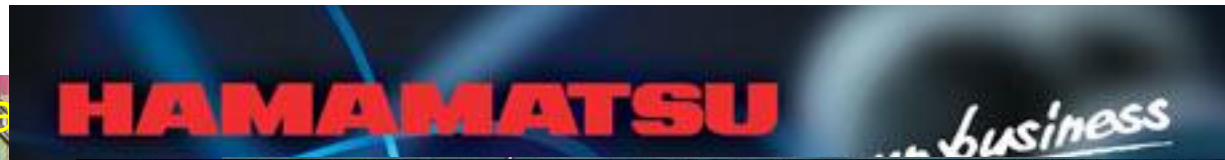


An advertisement for Hitachi. The word 'HITACHI' is written in large, bold, white letters at the top. Below it, the phrase 'Inspire the' is written in a smaller font. The background is a lush green landscape with a large, leafy tree. At the bottom, there is a small cartoon character and some Japanese text. The text at the bottom reads: '私たち日立グループは柏レイノルを応援しています。【日立の樹】www.hitachinoki.net'.

An advertisement for Sony. The word 'SONY' is written in large, bold, black letters at the top. Below it, the phrase 'make.believe' is written in a smaller font. At the bottom, the text 'TELEVISION AND A BLU-RAY PLAYER' is written in a smaller font. The background is a plain, light-colored surface.

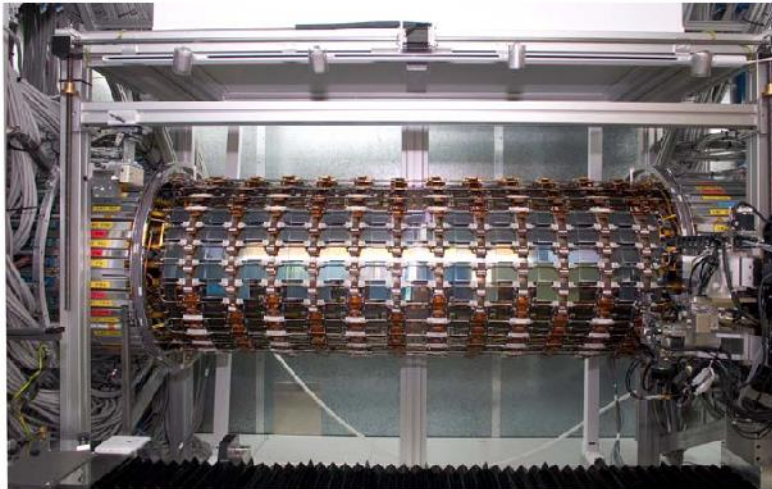
who has a long history of collaboration with our fields ...

Japan - Wonderland



Now, we are benefitting from the company...

Hamamatsu Photonics



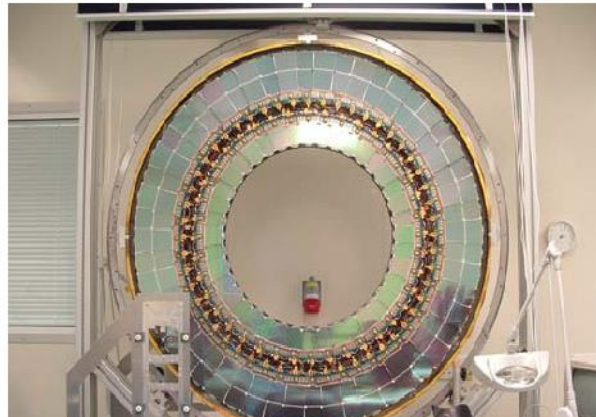
A cylindrical SCT barrel structure, radius 299 mm, length 1492 mm, tiled with rectangular modules constructed using silicon sensors supplied by Hamamatsu Photonics

Supply of Silicon Microstrip Sensors for the ATLAS SemiConductor Tracker

Hamamatsu Photonics has supplied 17,028 of the p-in-n single-sided silicon microstrip sensors that make up the detecting element of the ATLAS Semiconductor Tracker. The sensors are of six different shapes, each having 768 ac-coupled readout strips at a pitch close to 80 μm .

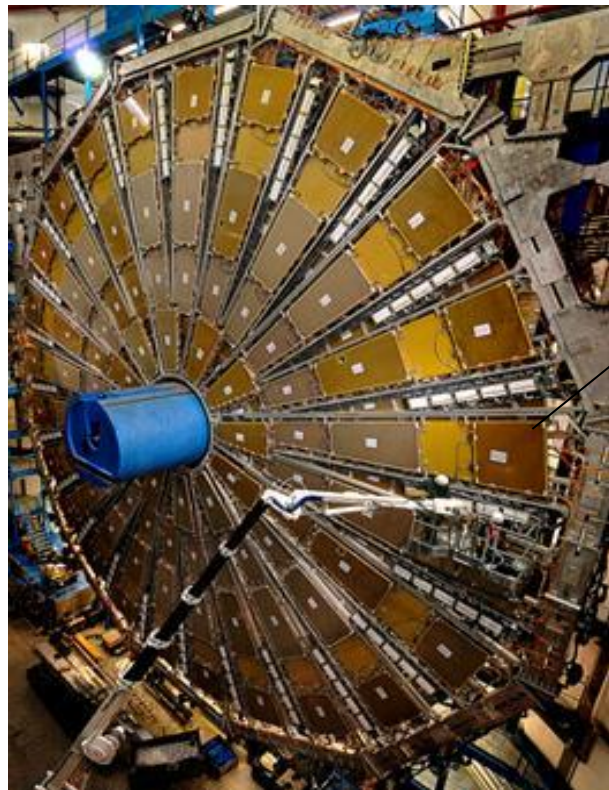
The final design details and specifications were developed during several years of collaborative R&D between Hamamatsu Photonics and ATLAS Institutes. The challenge was to produce sensors with high strip quality and efficiency that could withstand the high radiation levels to be experienced in ATLAS, operating at high bias voltages after type-inversion.

The sensors supplied were of uniformly excellent quality, well in excess of the requirements of the technical specification. They were delivered over a three-year period to the agreed schedule and cost. The ATLAS Collaboration greatly appreciates the help, the flexible attitude and the enormous contribution of Hamamatsu Photonics to the experiment.

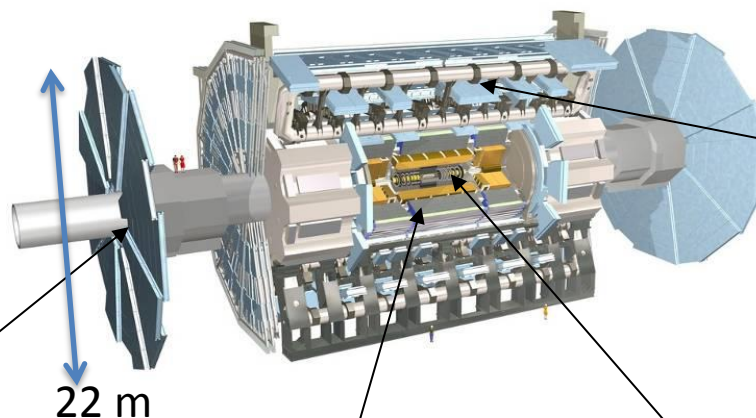


for the silicon detectors: ATLAS strips $\sim 92\%$, CMS strips $\sim 97\%$

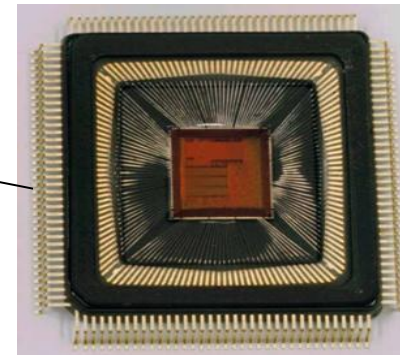
Contributions by Japanese teams in the ATLAS construction



1200 TGC chambers and 320K ch. L1 Electronics of endcap muon trigger system
KEK, Tokyo, Kobe, Nagoya...



Superconducting Solenoid, KEK



400k ch. of TDC chips for MDT system, KEK



6000 sensors and 980 modules of barrel SCT system, KEK, Tsukuba, Okayama, Hiroshima ...

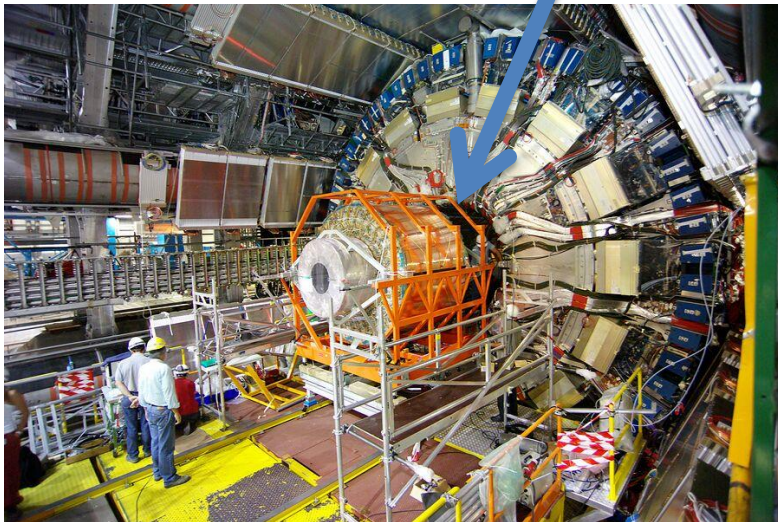
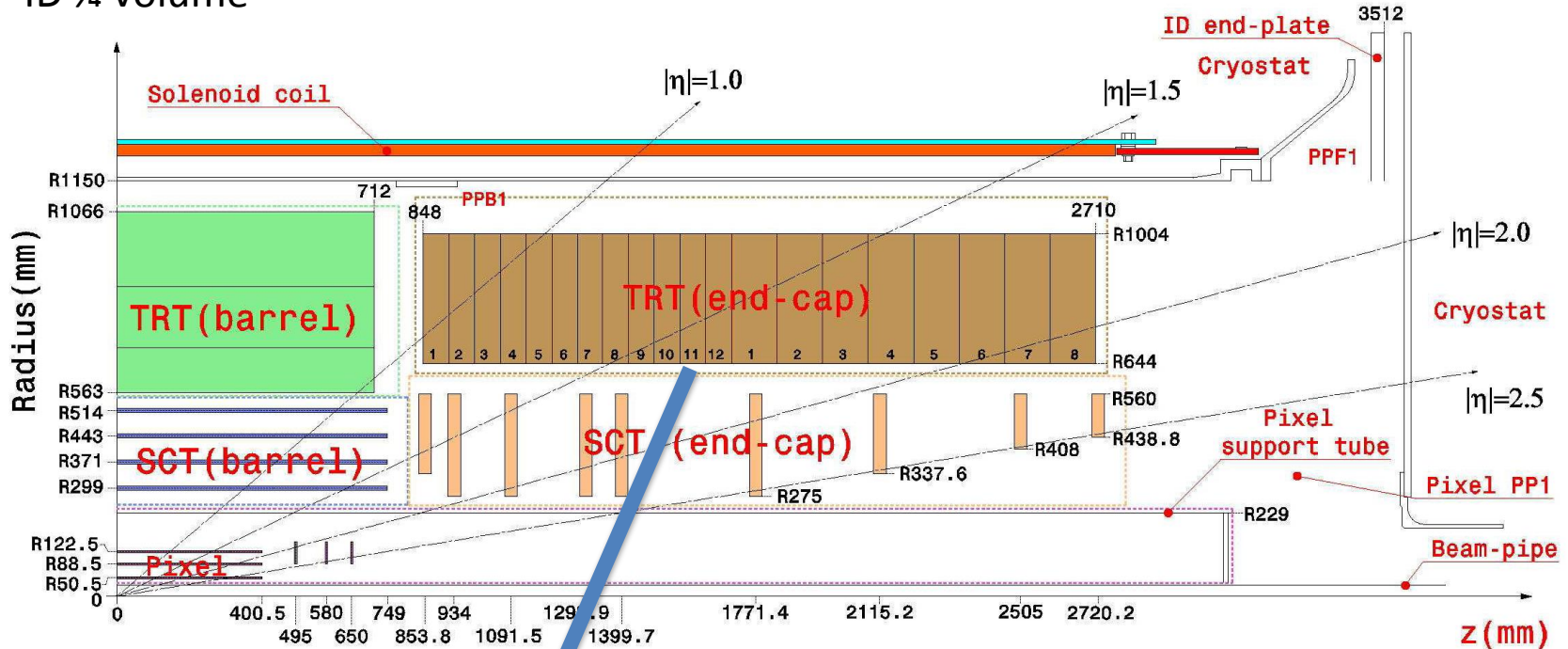
- In addition, many Japanese industries provided high quality detector components: Hamamatsu Photonics, Kawasaki Heavy Industries, Toshiba, Kuraray, Arisawa, Fujikura, etc

This lecture

- Micro-strip based detection systems
 - advances and new technological development
- It is not a monopoly of ATLAS nor HPK nor Japan. It is an example of our fields.
 - Other experiments: CMS, LHCb, ...
 - Other industries: Microns, CiS, ...
- Content
 - Brief overview of the current ATLAS silicon microstrip tracker (SCT)
 - Issues and achievement of the LHC tracker
 - New technological development for the high-luminosity LHC tracker
 - Understanding the underlying physics – TCAD simulation

ATLAS Inner Detector

ID ¼ volume

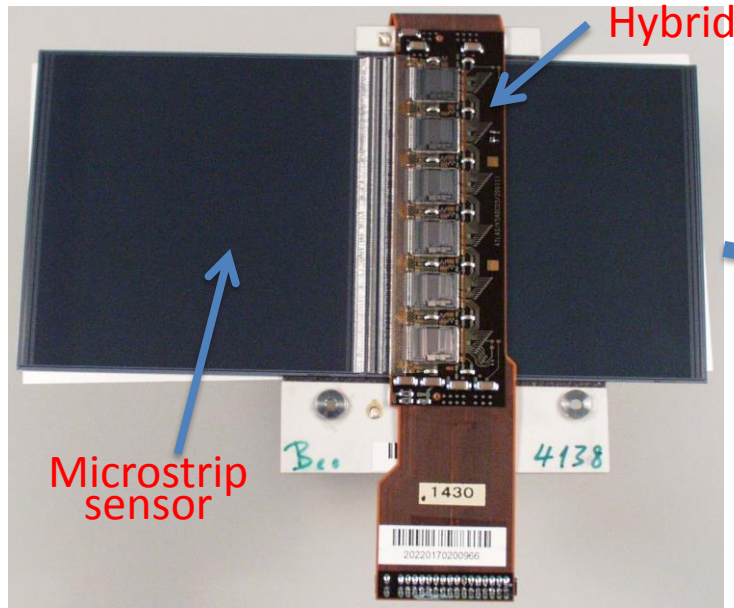


Installation into ATLAS

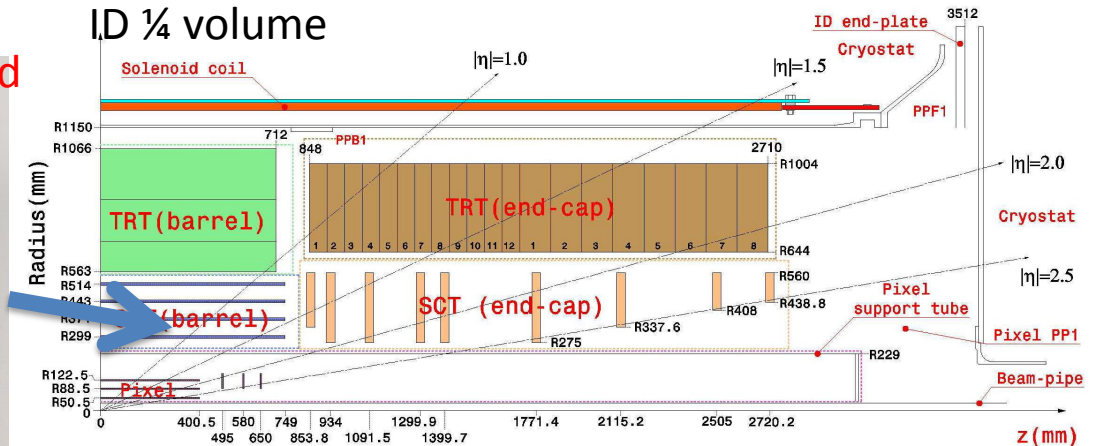
- ID=Silicon (Pixel + Strip) + TRT
 - Radius~ 1 m, L=5.4 m
- Position (ϕ) resolutions
 - Pixel: ~15 μm (50 μm pitch)
 - Strip (axial-stereo pair): ~17 μm (80 μm pitch)
 - TRT: ~ 22 μm (130 μm drift reso., 36 sampling)
- Why Silicon?

$$dp/p \propto \mu_e \int ds / (0.3BL^2) \propto p$$

ATLAS Inner Detector



SCT barrel module

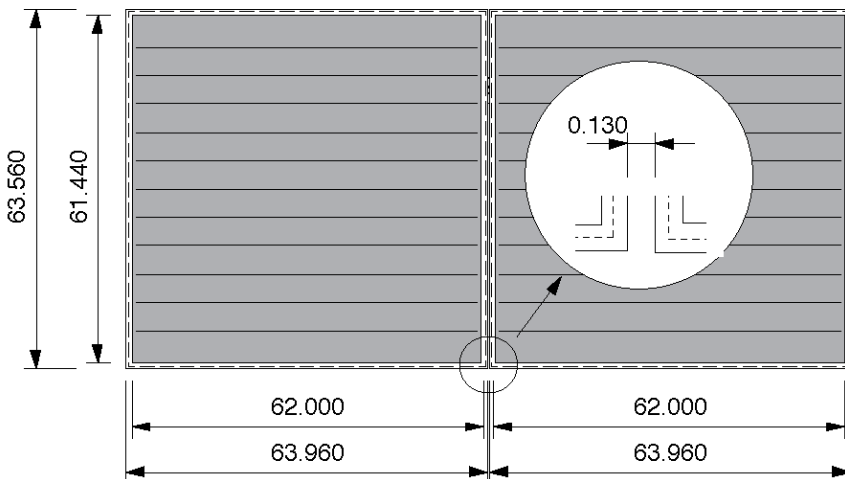


- Barrel modules

- Double-side, stereo readout (40 mrad)
- **Sensors**: 2 x (6.4 x 6.4 cm²) /side x 2 side (top and bottom)
 - 4 in. FZ crystal wafer, <111> and some <100>
 - 80 μm pitch, ~12.6 cm strip length, 768 strips
- **Hybrids**: 6 ABCD chips (128 ch)/side
 - Cu/Polyimide flex circuit + Carbon-carbon substrate

Red letters: Contributions of Japan

- Japan: ~980/2112+spares in **barrel modules**



Silicon Detector (LHC)

Assembly station: aligning two sensors in $<5 \mu\text{m}$



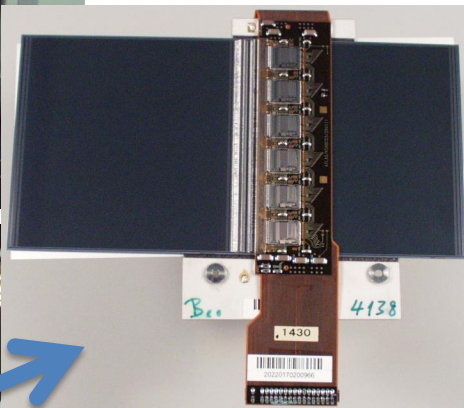
in Japan, ...



in Japan, ...

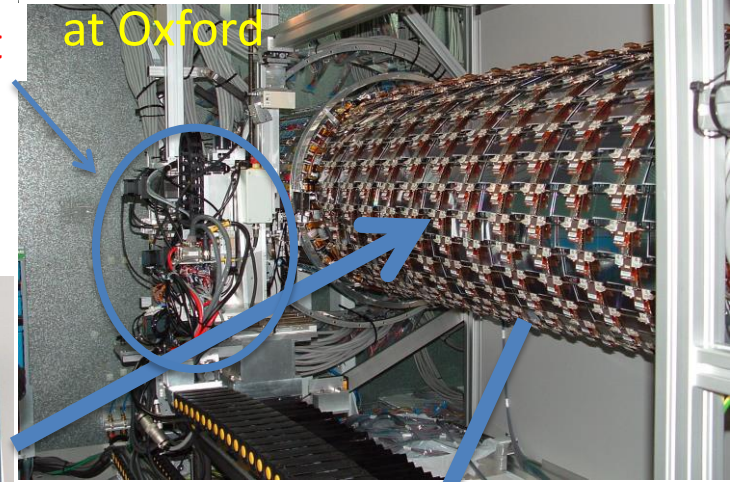
Wire bonding of hybrid-sensor and sensor-sensor: ~ 3200 bonds

Placement robot

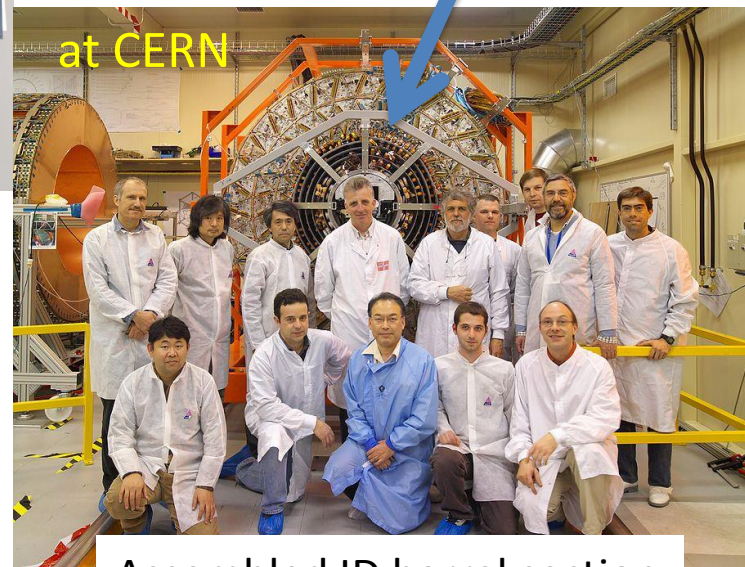


Completed modules

Mounted on the barrel cylinder



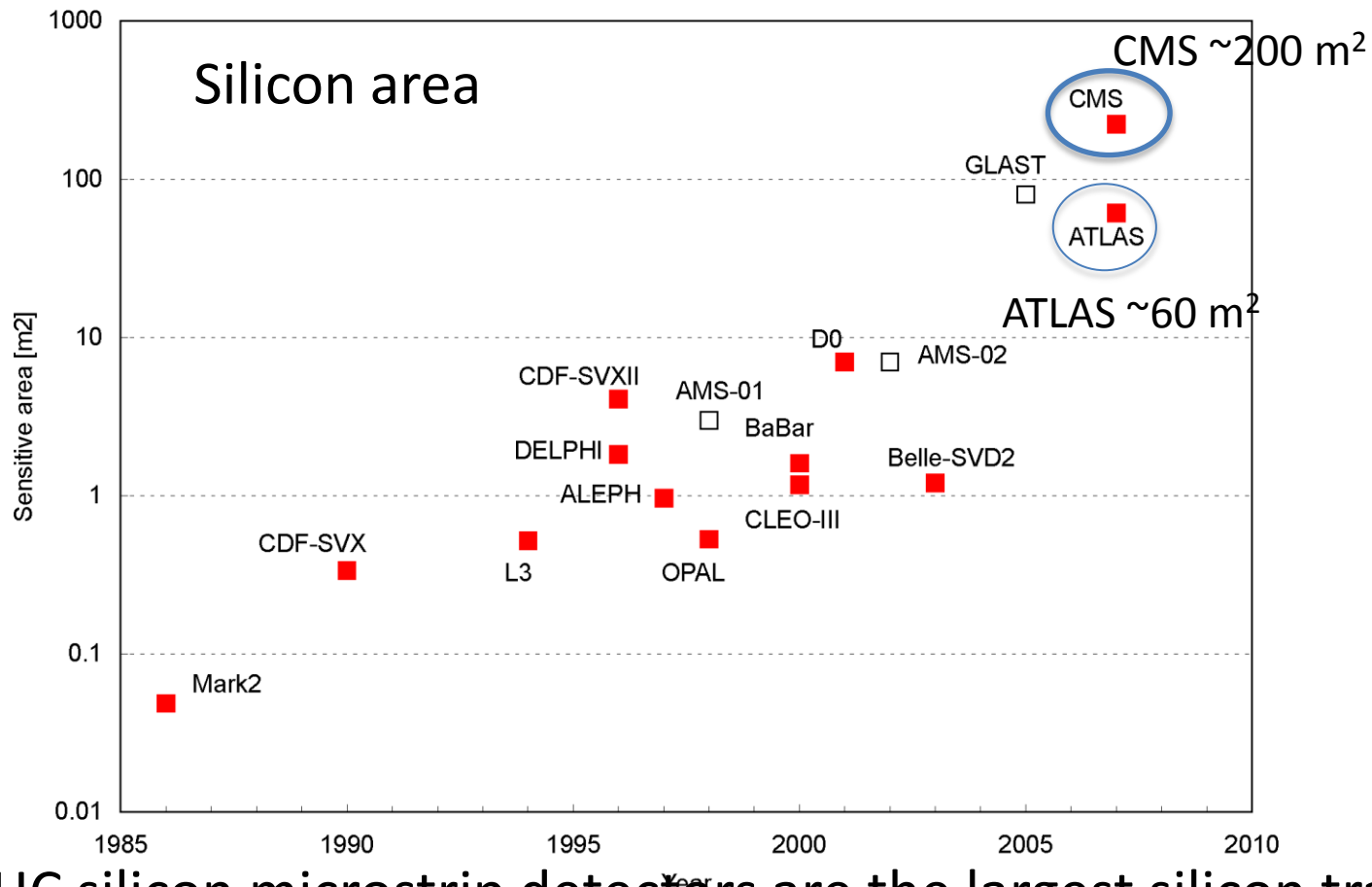
at Oxford



at CERN

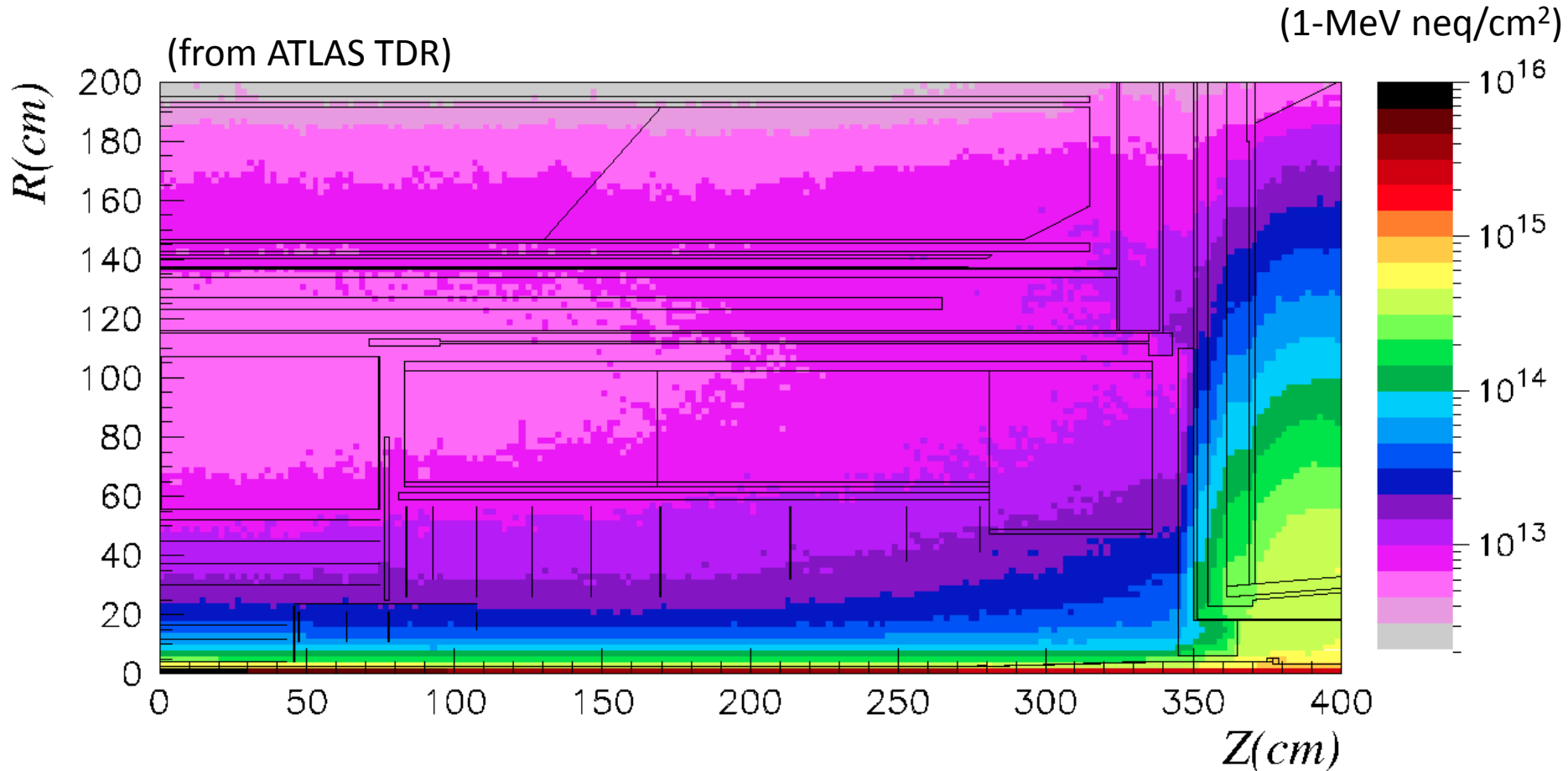
Assembled ID barrel section
Then, installed into ATLAS

Silicon Detector (LHC)



- LHC silicon microstrip detectors are the largest silicon trackers ever built.
 - A scale: cost of the sensor ~ 1 million Euro/m²
- Silicon microstrip detector is the “must” for large area coverage.

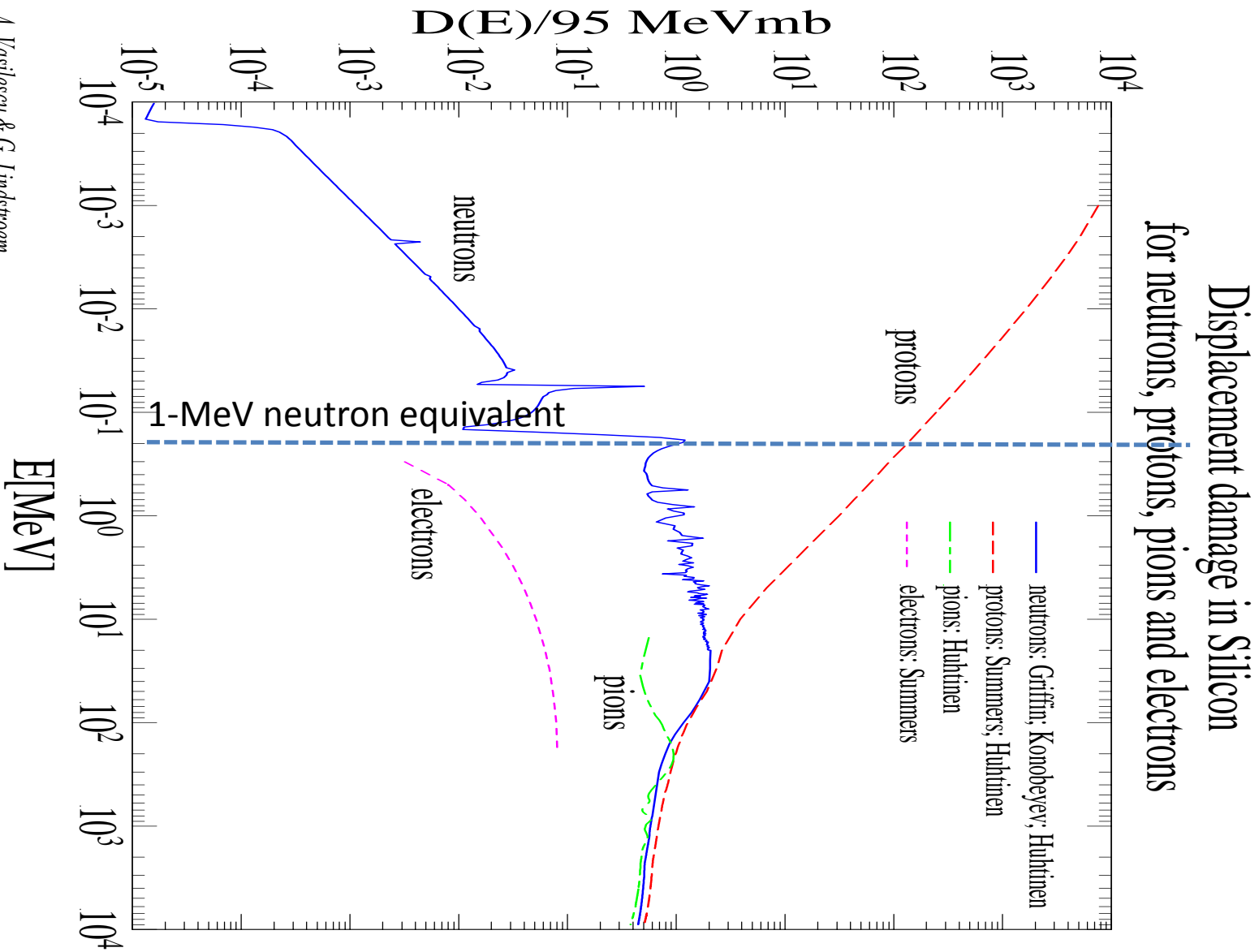
Particle fluence



- Yearly fluence of particles
 - in the unit of 1-MeV neutrons equivalent per cm²
 - Luminosity: 10^{34} cm⁻²s⁻¹, integrated: 100 fb⁻¹/yr
- End of life fluence at SCT ($r=30$ cm)
 - 2×10^{14} 1-MeV neq/cm² (including 50% uncertainty in pp cross section)

Non Ionizing Energy Loss (NIEL)

A. Vasilescu & G. Lindstroem



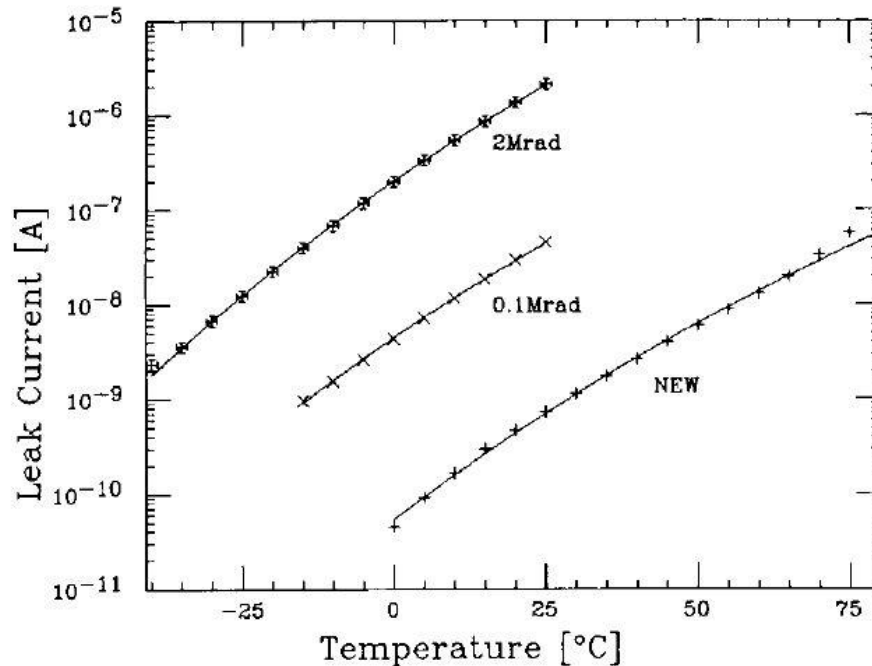
Radiation Damage Studies

- Radiation damage of silicon
 - the 1st, in our field... 30 yrs ago
 - T. Kondo et al, Radiation Damage Test of Silicon Microstrip Detectors
 - Proc. of the 1984 Summer Study on the Design and Utilization of SSC, June 23-July 13, 1984, Snowmass, Colorado, pp. 612-614
- The messages were
 - The prevailing opinion was that silicon vertex detectors were not possible at 10^{33} luminosity, but...
 - It was shown that silicon is rad-hard, little pulse-height change, cooling needed.

Radiation Damage Studies

- Since then, radiation damage studies are continued in Japan, Europe, US., and elsewhere
 - Two papers were already published in 1988, (25 yrs ago)
 - T. Ohsugi, ... T. Kondo, ... K. Yamamoto ..., "Radiation Damage in Silicon Microstrip Detectors", Nucl. Instr. Meth. A265(1988)105
 - M. Nakamura, ... T. Kondo, "Radiation Damage Test of Silicon Multistrip Detectors", Nucl. Instr. Meth. A270(1988)42, using the irradiated sensor by 800 GeV protons

Increase of leakage current



Also, temperature dependence of bulk leakage current

$$J_g(T) \propto T^2 \exp\left(-\frac{E_{ef}}{2k_B T}\right)$$

$$E_{ef} = 1.20 \text{ eV}$$

Fig. 5. Temperature dependence of the leakage current. The solid lines are the best fits using the formula given in the text.

- Radiation Damage in Silicon Microstrip Detectors
 - T. Ohsugi, ... T. Kondo, ... K. Yamamoto .., Nucl. Instr. Meth. A265(1988)105

Type inversion of the silicon

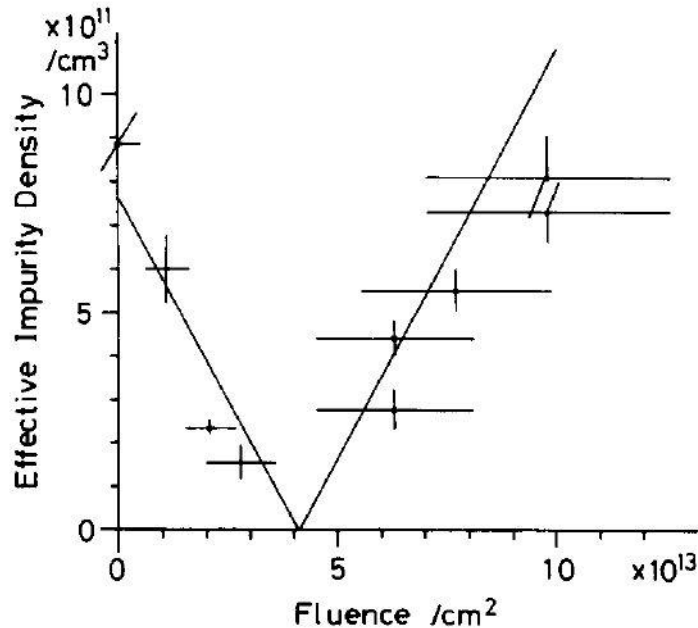


Fig. 25. Estimated effective impurity density as a function of proton fluence.

Abstract:

..... The effective impurity density decreases with fluence up to $\sim 4 \times 10^{13} / \text{cm}^2$, but for greater fluences, it increases. **This may indicate the type conversion** of the bulk silicon

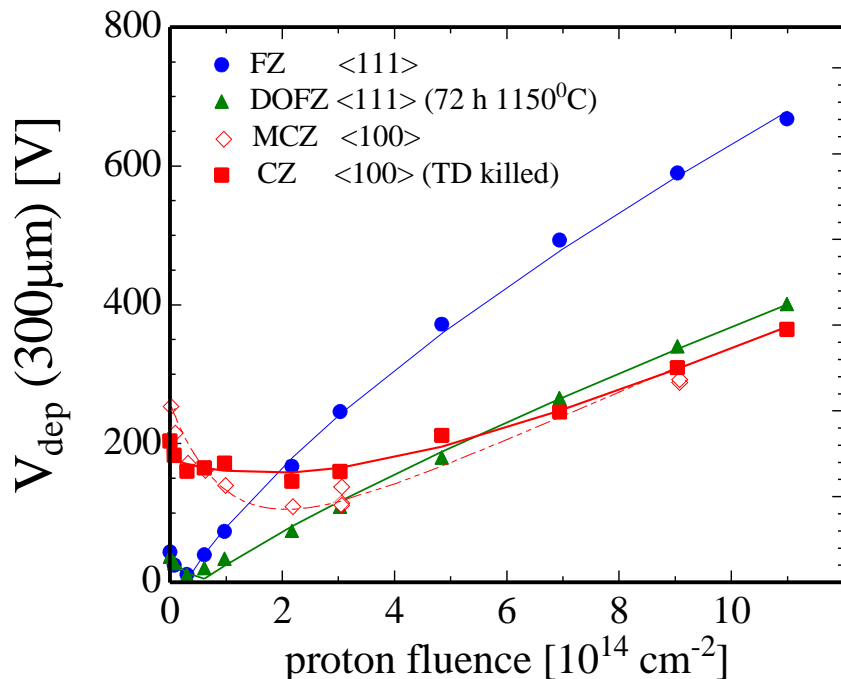
- M. Nakamura, ... T. Kondo, "Radiation Damage Test of Silicon Multistrip Detectors", Nucl. Instr. Meth. A270(1988)42, using the irradiated sensor by 800 GeV protons

Evolution of depletion voltage

- A thorough study of the radiation damages has been made by RD50 collaboration. But, also done elsewhere...
 - E.g. Michael Moll, Ph.D Thesis, 1999.

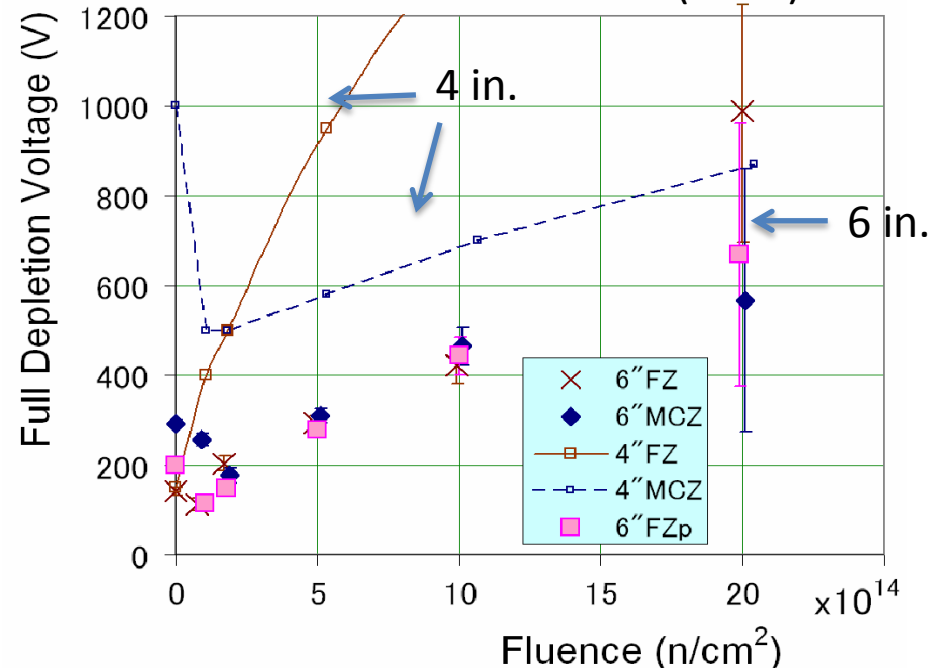


24 GeV/c proton irradiation
(n-type silicon)



70 MeV proton irradiation
(p-type silicon)

K. Hara et al.,
IEEE Trans. Nucl. Sci. 56 (2009) 468



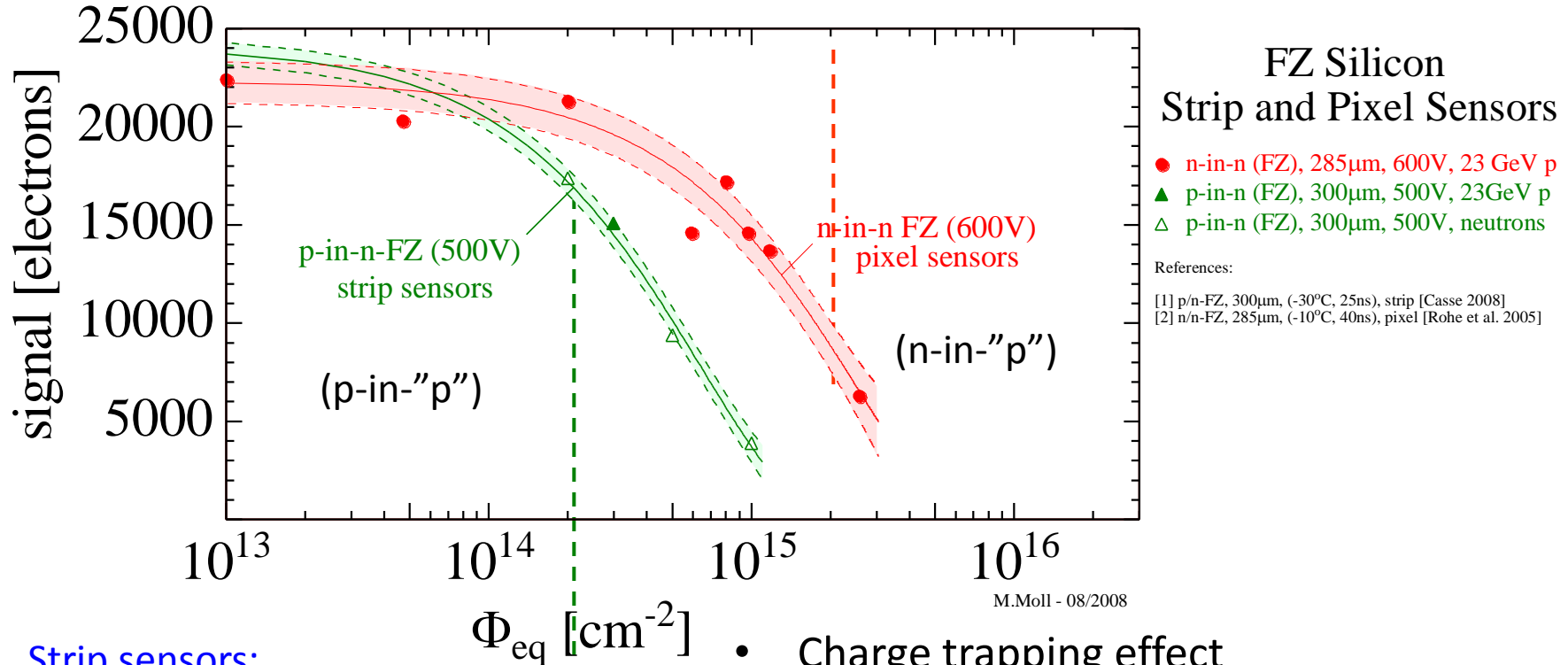
6 in. FZ is as same as DOFZ or MCZ

Signal degradation in LHC Silicon Sensors

Pixel sensors:

max. cumulated fluence for LHC

RD50 collaboration



Strip sensors:

max. cumulated fluence for LHC

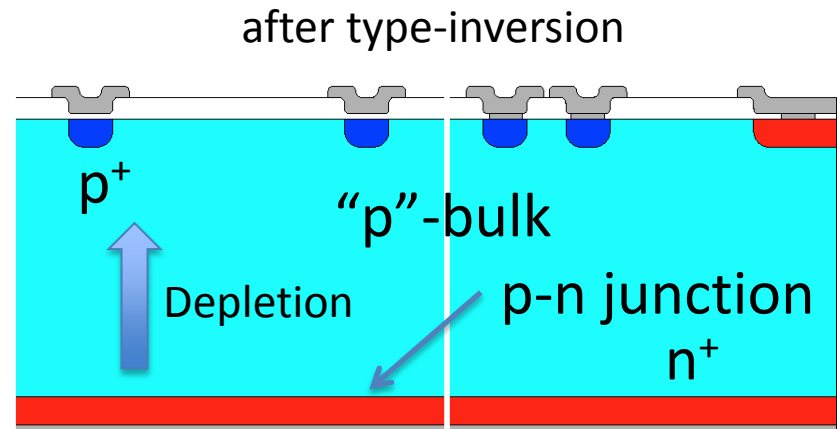
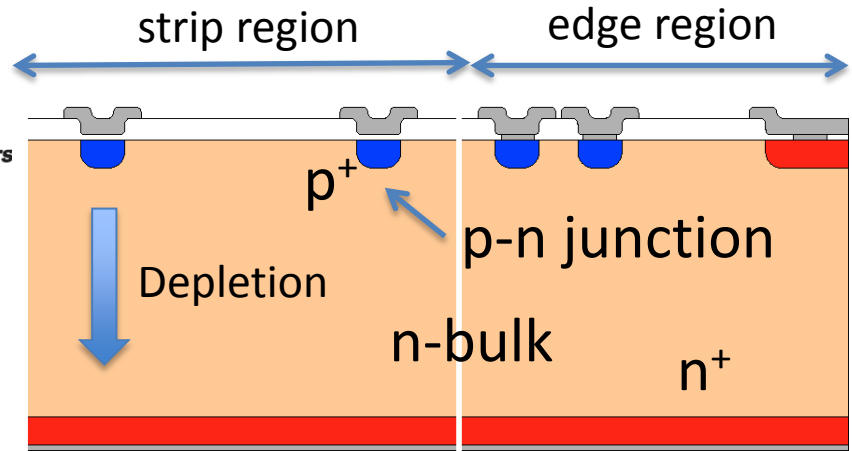
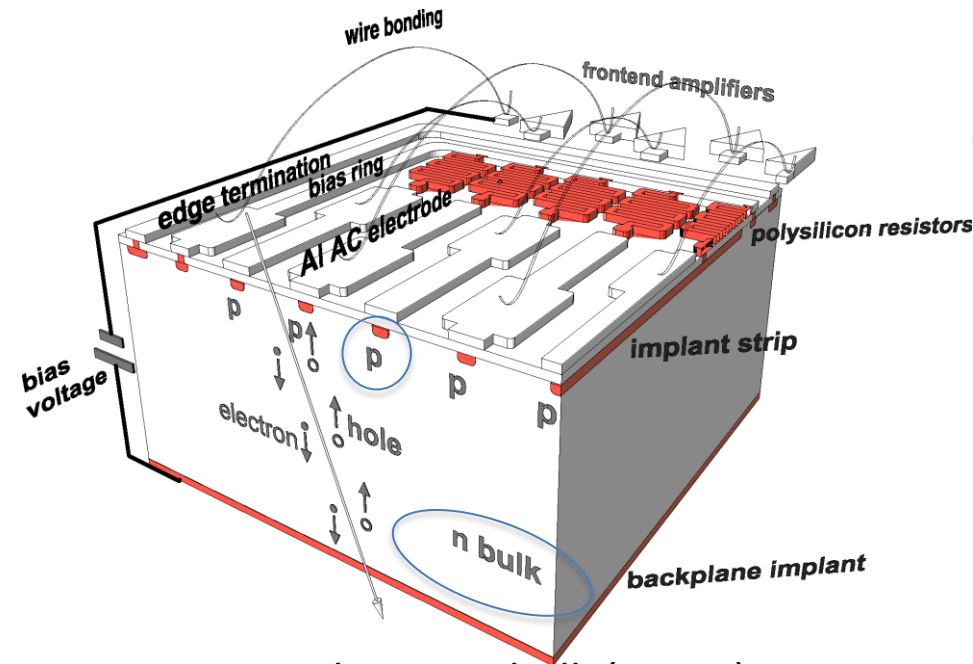
- Charge trapping effect
 - ← Most of signals at around the strips (see Appendix)
- Depleted region
 - p-in-n \rightarrow p-in-p (requires "full depletion")
 - n-in-n \rightarrow n-in-p (works under "partial depletion")

Choice of LHC Experiments

Experiment	Type	Wafer
ALICE pixel	p-in-n	standard FZ
ATLAS pixel	n-in-n	oxygenated
ATLAS strips	p-in-n	standard FZ <111> (some <100>)
CMS pixel	n-in-n	standard FZ
CMS strips	p-in-n	standard FZ <100>
LHCb VELO	n-in-n	standard FZ

- Cost consideration and compromises
- p-in-n:
 - single-side process (lower cost)
 - requires full depletion, high voltage operation
- n-in-n
 - double-side process (higher cost)
 - works under partial depletion, less requirement for high voltage

Silicon Detector (LHC ATLAS)

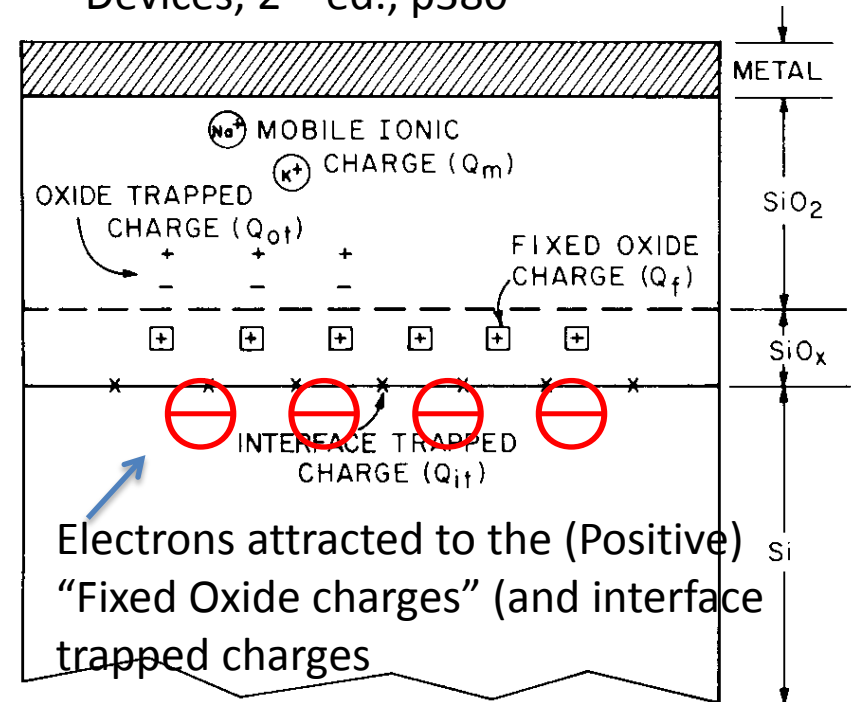
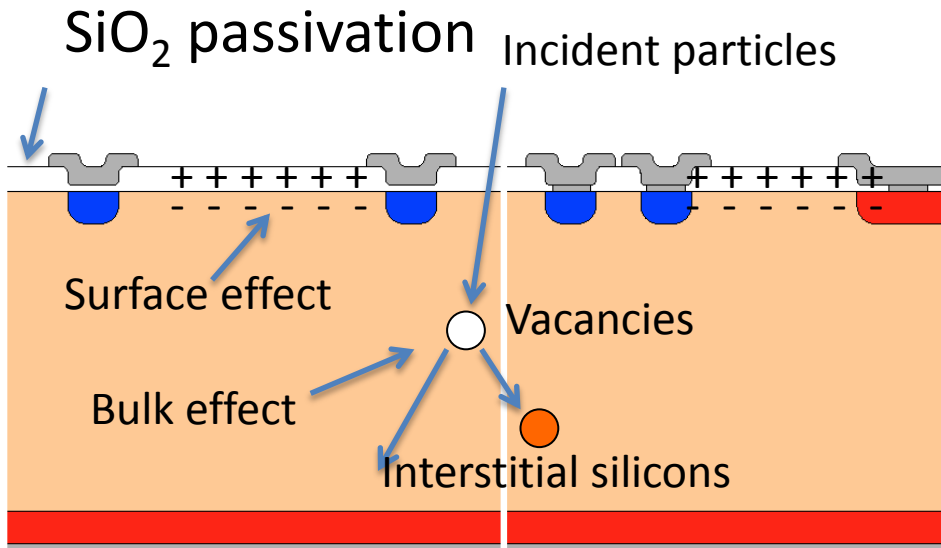


LHC: p-readout in n-bulk (p-in-n)

- Silicon sensor principle
 - Deplete the bulk by holding the bias voltage at p-n junction
- LHC ATLAS: p-in-n strip sensor
 - N-bulk: conventional
 - Cheaper than other options
 - Need full depletion
 - 500 V max operation/specification

Radiation damage – Surface effect

S.M. Sze, Physics of Semiconductor Devices, 2nd ed., p380



- The interfacial region is a single-crystal silicon followed by a monolayer of SiO_x, incompletely oxidized silicon, then a strained region of SiO₂ roughly 10-40 Å deep.
- Interface traps (Q_{if}) and fixed oxide charges (Q_f) exist, (as a consequence of thermal oxidation)
- Oxide trapped charges (Q_{ot}) can be created by radiation.
- Q_f and Q_{ot} are "positive" and attract electrons in the Si-SiO₂ interface.

1st Visualization of Microdischarge

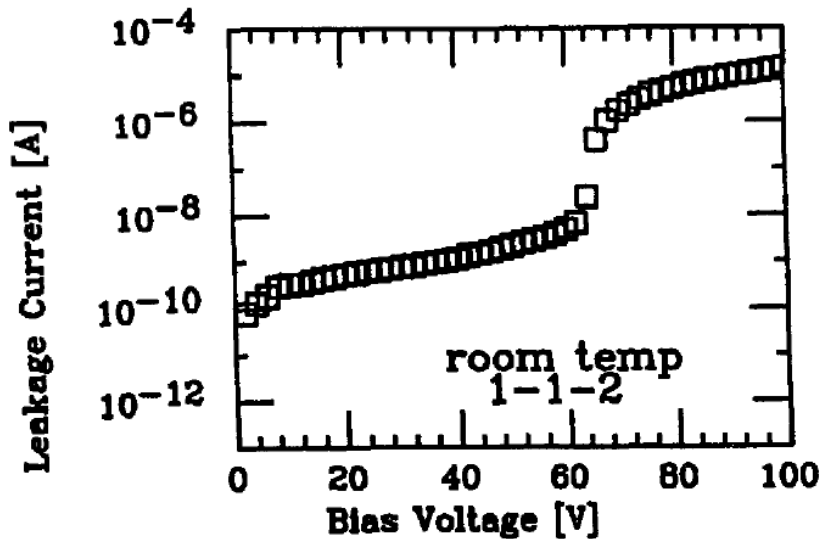
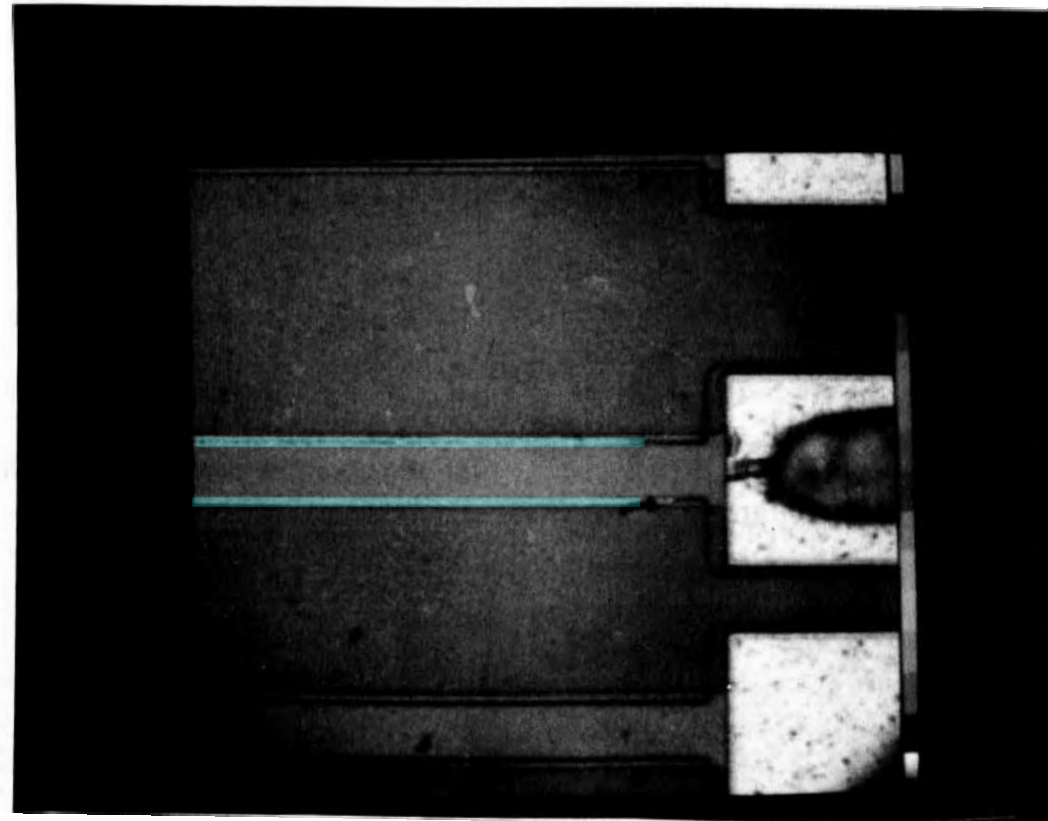
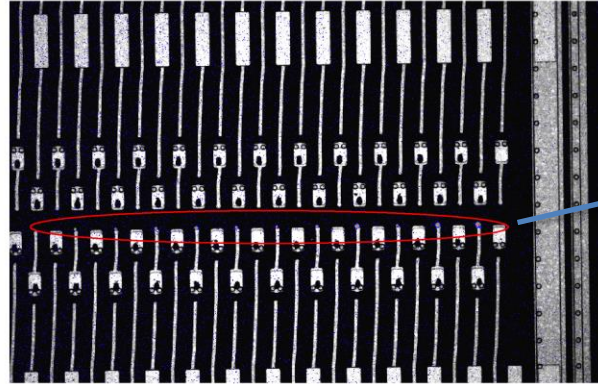
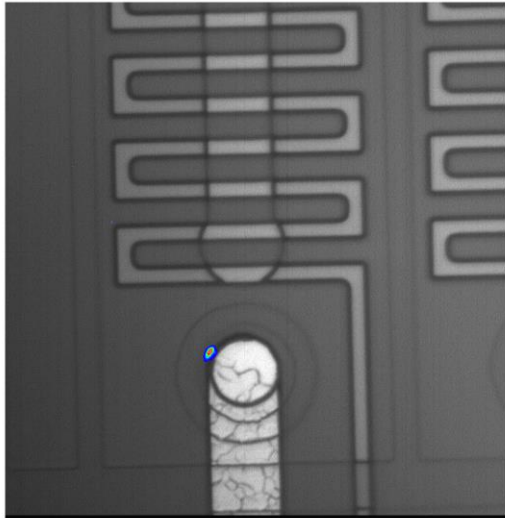


Fig. 1. Leakage current as a function of the bias voltage when the potential is across the integrated capacitor on the p-strip.

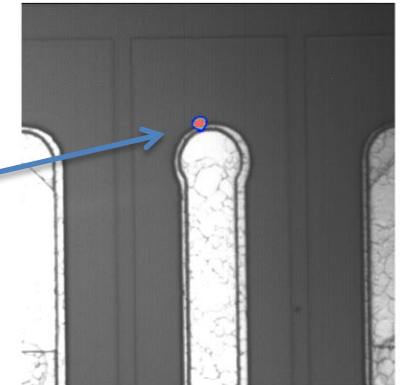


- High bias voltage \rightarrow High electric field \rightarrow avalanche breakdown
 - Breakdown field ~ 30 V/ μ m in silicon
- Visualization with an infra-red sensitive camera
- T. Ohsugi et al., Nucl. Instr. Meth. A432 (1994) 22

Other examples of hot spots



Seg4 の DC PAD ストライプ 先端で発光 (×5)



Seg4 の DC PAD ストライプ 先端で発光 (×100)

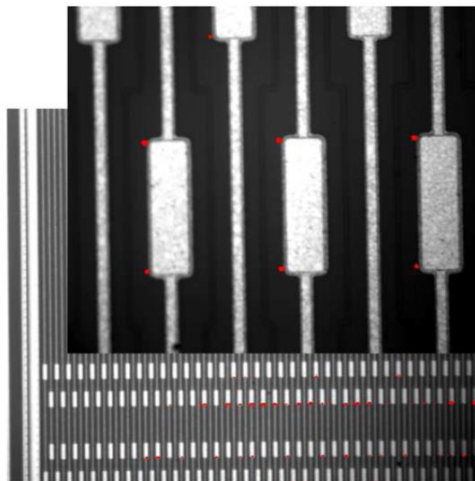
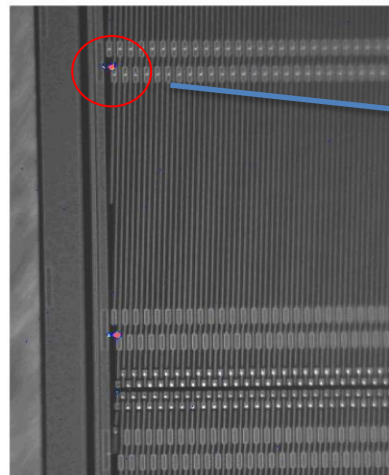
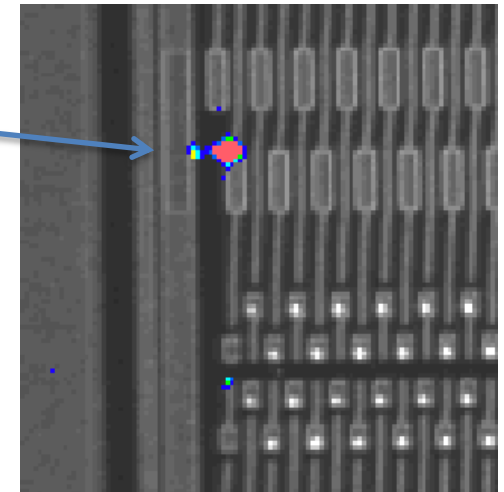


Fig. 9. Hot spots observed at AC pad corners. The AC pad is 60 μm wide and 200 μm long.



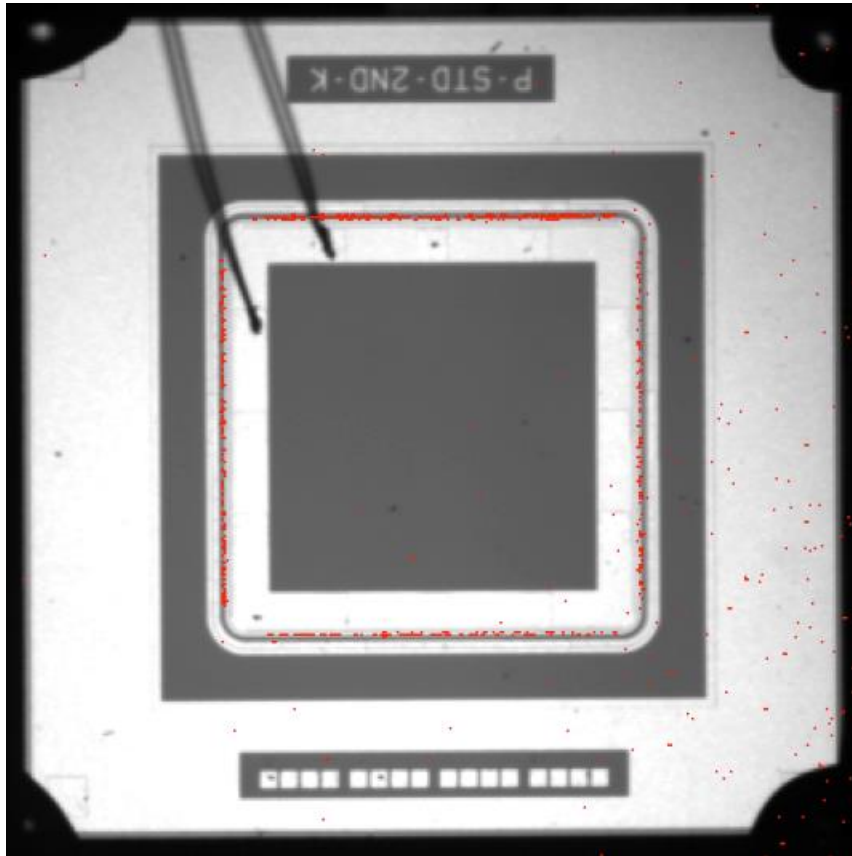
の AC PAD 角と Seg4 の DC PAD ストライプ 先端で発光 (×0.8)



Y. Unno et al., Nucl. Instr. Meth. A Supplement 636 (2011) S24

Y. Takahasi et al., <http://dx.doi.org/10.1016/j.nima.2012.04.031>

Microdischarge after Irradiation

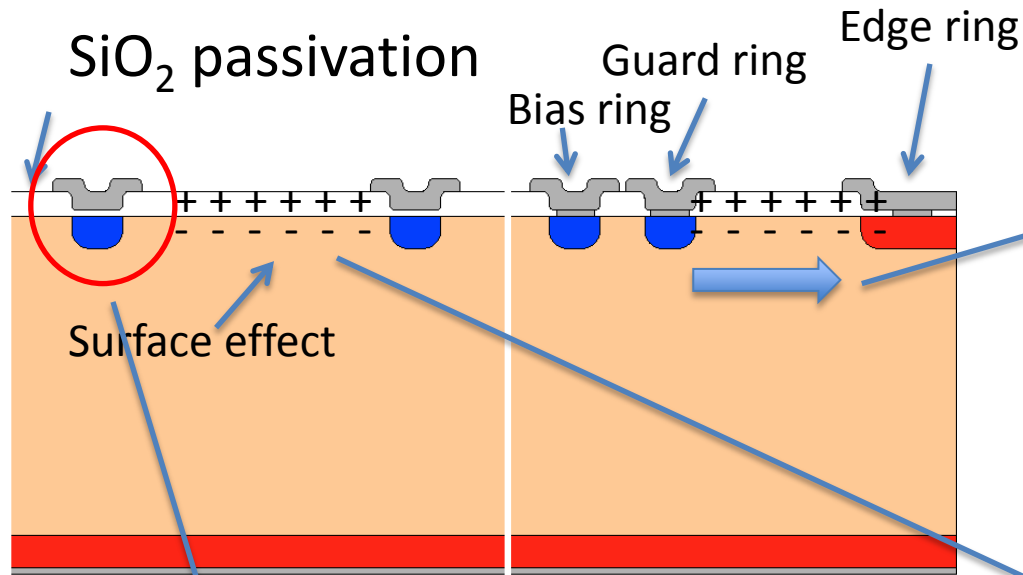


CYRIC proton irradiated
 $1 \times 10^{14} n_{eq}/\text{cm}^2$
10 μA at 2000 V
-15 ° C

S. Mitsui et al.,
Nucl. Instr. Meth. A699 (2013) 36-40

- Hot electron images confirm that
 - hot spots were observed first at the edge of the bias ring, and then at the inside of the edge metal.
 - the highest electric field is at the bias ring (n^+ implant), not at the edge ring (p^+ implant).

Design of the sensor to high bias voltage



Surface effect

②

Optimize the edge structure
 → Width, guard ring
 between the bias and the dicing
 edge
 HPK design:
 1) width ~ 1 mm
 2) minimum # guard rings, i.e. 1

③

Reduce the oxide fixed/trap charges
 → <100> crystal orientation
 Less “dangling” bonds in interface
 <100>: $\sim 10^{10}$ ions/cm²
 <111>: $\sim 10^{11}$ ions/cm²

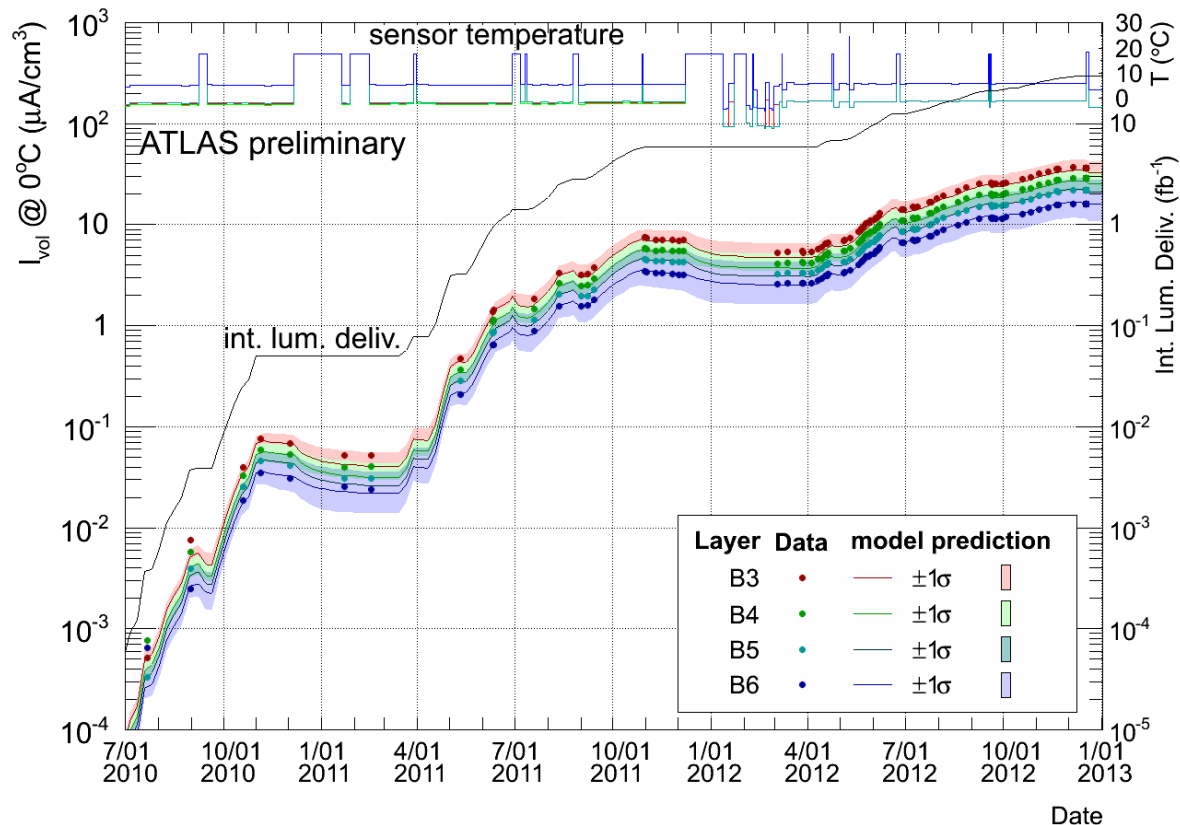
①

Move the highest electric
 field from Si (to SiO₂)
 → Extended electrode
 Under the condition of
 1) Same potential in
 implant and metal

④

Clean and high quality process
 → little irregularity at high field
 where HPK is appreciated.

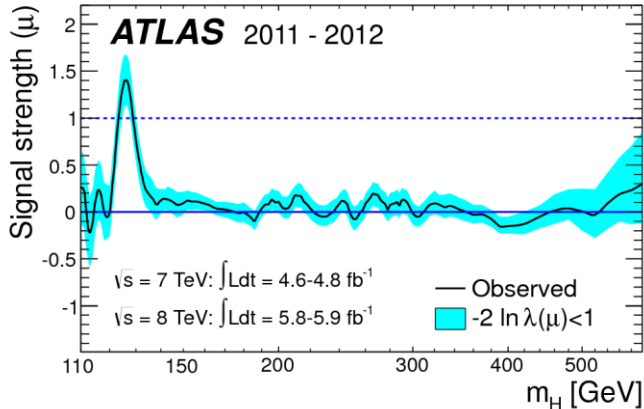
ATLAS SCT in operation



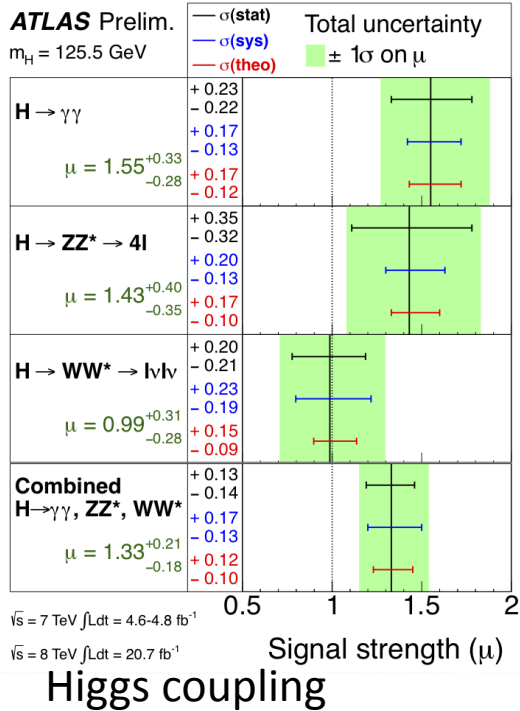
- Radiation damage monitoring
 - Leakage currents are well consistent with the expectation
- 99.3% modules are working
 - 30/4088 modules were disabled due to LV, HV, Cooling problems, ...

LHC Upgrade (HL-LHC)

Higgs search



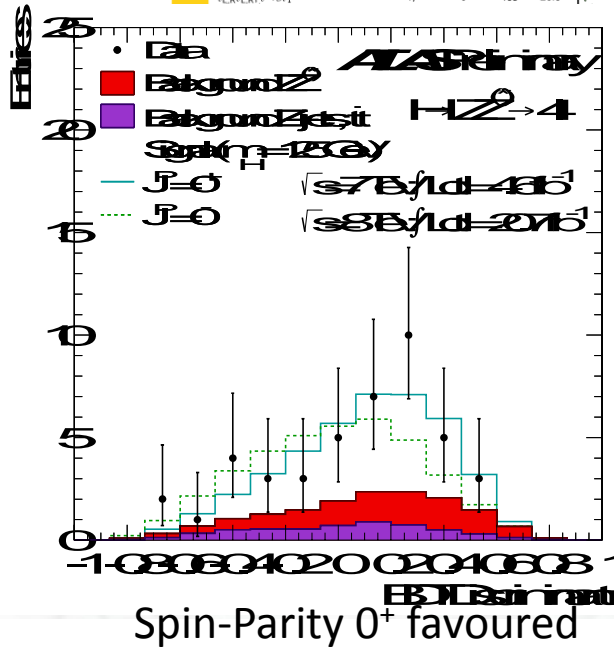
Phys. Lett. B 716 (2012) 1-29 Fig.7c



ATLAS SUSY Searches* - 95% CL Lower Limits

Status: LP 2013

Model	e, μ, τ, γ	Jets	E_{miss}	$\int L dt [\text{fb}^{-1}]$	Mass limit	Reference		
Inclusive Searches	MSUGRA/CMSSM	1 e, μ	3-6 jets	Yes	20.3	1.2 TeV	ATLAS-CONF-2013-062	
	MSUGRA/CMSSM	0	7-10 jets	Yes	20.3	1.1 TeV	ATLAS-CONF-2013-054	
	$\tilde{g}\tilde{g}, \tilde{q}\tilde{q} \rightarrow \tilde{q}\tilde{q}^*$	0	2-6 jets	Yes	20.3	740 GeV	ATLAS-CONF-2013-047	
	$\tilde{g}\tilde{g}, \tilde{q}\tilde{q} \rightarrow \tilde{q}\tilde{q}^*$	0	2-6 jets	Yes	20.3	1.3 TeV	ATLAS-CONF-2013-047	
	$\tilde{g}\tilde{g}, \tilde{q}\tilde{q} \rightarrow \tilde{q}\tilde{q}^*$	1 e, μ	3-6 jets	Yes	20.3	1.18 TeV	ATLAS-CONF-2013-062	
	$\tilde{g}\tilde{g} \rightarrow \tilde{q}\tilde{q}^* l(l) \tilde{l} \tilde{l}^*$	2 e, μ (SS)	3 jets	Yes	20.7	1.1 TeV	ATLAS-CONF-2013-007	
	GMSB (\tilde{l} NLSP)	2 e, μ	2-4 jets	Yes	4.7	1.24 TeV	1208.4688	
	GMSB (\tilde{l} NLSP)	1-2 τ	0-2 jets	Yes	20.7	1.4 TeV	ATLAS-CONF-2013-026	
	GGM (bino NLSP)	2 γ	0	Yes	4.8	1.07 TeV	1209.0753	
	GGM (wino NLSP)	1 $e, \mu + \gamma$	0	Yes	4.8	619 GeV	ATLAS-CONF-2012-144	
3rd gen. & med.	GGM (higgsino-bino NLSP)	γ	1 b	Yes	4.8	900 GeV	1211.1167	
	GGM (higgsino NLSP)	2 e, μ (Z)	0-3 jets	Yes	5.8	690 GeV	ATLAS-CONF-2012-152	
	Gravitino LSP	0	mono-jet	Yes	10.5	645 GeV	ATLAS-CONF-2012-147	
	$\tilde{g} \rightarrow b\tilde{b}^*$	0	3 b	Yes	20.1	1.2 TeV	ATLAS-CONF-2013-061	
	$\tilde{g} \rightarrow t\tilde{t}^*$	0	7-10 jets	Yes	20.3	1.14 TeV	ATLAS-CONF-2013-054	
	$\tilde{g} \rightarrow t\tilde{t}^*$	0-1 e, μ	3 b	Yes	20.1	1.34 TeV	ATLAS-CONF-2013-061	
	$\tilde{g} \rightarrow b\tilde{t}^*$	0-1 e, μ	3 b	Yes	20.1	1.3 TeV	ATLAS-CONF-2013-061	
	3rd gen. squarks direct production	$\tilde{t}_1, \tilde{t}_2, \tilde{b}_1, \tilde{b}_2 \rightarrow b\tilde{t}_1^*$	0	2 b	Yes	20.1	100-630 GeV	ATLAS-CONF-2013-053
		$\tilde{t}_1, \tilde{t}_2, \tilde{b}_1, \tilde{b}_2 \rightarrow t\tilde{t}_1^*$	2 e, μ (SS)	0-3 b	Yes	20.7	430 GeV	ATLAS-CONF-2013-007
		\tilde{t}_1, \tilde{t}_2 (light), $\tilde{t}_1 \rightarrow b\tilde{t}_1^*$	1-2 e, μ	1-2 b	Yes	4.7	167 GeV	1208.4305, 1209.2102
\tilde{t}_1, \tilde{t}_2 (light), $\tilde{t}_1 \rightarrow Wb\tilde{t}_1^*$		2 e, μ	0-2 jets	Yes	20.3	220 GeV	ATLAS-CONF-2013-048	
\tilde{t}_1, \tilde{t}_2 (medium), $\tilde{t}_1 \rightarrow b\tilde{t}_1^*$		2 e, μ	0-2 jets	Yes	20.3	150-440 GeV	ATLAS-CONF-2013-048	
\tilde{t}_1, \tilde{t}_2 (medium), $\tilde{t}_1 \rightarrow b\tilde{t}_1^*$		0	2 b	Yes	20.1	150-580 GeV	ATLAS-CONF-2013-053	
\tilde{t}_1, \tilde{t}_2 (heavy), $\tilde{t}_1 \rightarrow t\tilde{t}_1^*$		1 e, μ	1 b	Yes	20.1	200-610 GeV	ATLAS-CONF-2013-037	
\tilde{t}_1, \tilde{t}_2 (heavy), $\tilde{t}_1 \rightarrow t\tilde{t}_1^*$		0	2 b	Yes	20.5	300-560 GeV	ATLAS-CONF-2013-024	
\tilde{t}_1, \tilde{t}_2 (natural GMSB)		2 e, μ (Z)	1 b	Yes	20.7	500 GeV	ATLAS-CONF-2013-025	
$\tilde{t}_1, \tilde{t}_2, \tilde{b}_1, \tilde{b}_2 \rightarrow \tilde{t}_1 + Z$		2 e, μ (Z)	1 b	Yes	20.7	520 GeV	ATLAS-CONF-2013-025	
$\tilde{t}_1, \tilde{t}_2, \tilde{b}_1, \tilde{b}_2 \rightarrow \tilde{t}_1^*$	2 e, μ	0	Yes	20.3	7	ATLAS-CONF-2013-049		
Mass scale	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	85-315 GeV	ATLAS-CONF-2013-049	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	125-450 GeV	ATLAS-CONF-2013-049	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	180-330 GeV	ATLAS-CONF-2013-028	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	600 GeV	ATLAS-CONF-2013-035	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	315 GeV	ATLAS-CONF-2013-035	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	857 GeV	1210.2852	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	385 GeV	ATLAS-CONF-2013-057	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	395 GeV	ATLAS-CONF-2013-058	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	230 GeV	ATLAS-CONF-2013-058	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	700 GeV	1304.6310	
Reference	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	1.61 TeV	1210.2852	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	1.1 TeV	ATLAS-CONF-2013-057	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	1.2 TeV	ATLAS-CONF-2013-058	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	760 GeV	ATLAS-CONF-2013-058	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	350 GeV	1212.1272	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	666 GeV	1212.1272	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	880 GeV	ATLAS-CONF-2012-140	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	760 GeV	ATLAS-CONF-2013-036	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	350 GeV	1210.4813	
	$\tilde{g} \rightarrow \tilde{g} + \tilde{g}$	0	0	Yes	20.3	100-287 GeV	ATLAS-CONF-2013-007	



shown. All limits quoted are observed minus 1 for the nominal signal cross section uncertainty.

- Higgs – Great discovery of the century
- Need high statistics
 - Study of properties
 - Search for “something” in TeV mass region

Schedule for HL-LHC

2009

Start of LHC

Run 1: 7 and 8 TeV centre of mass energy, luminosity ramping up to few $10^{33} \text{ cm}^{-2} \text{ s}^{-1}$, few fb^{-1} delivered

2013/14

LHC shut-down to prepare machine for design energy and nominal luminosity

IBL installation

Run 2: Ramp up luminosity to nominal ($10^{34} \text{ cm}^{-2} \text{ s}^{-1}$), ~50 to 100 fb^{-1}

2018

Injector and LHC Phase-I upgrades to go to ultimate luminosity

LHC

Run 3: Ramp up luminosity to 2.2 x nominal, reaching ~100 fb^{-1} / year accumulate **few hundred fb^{-1}**

~2022

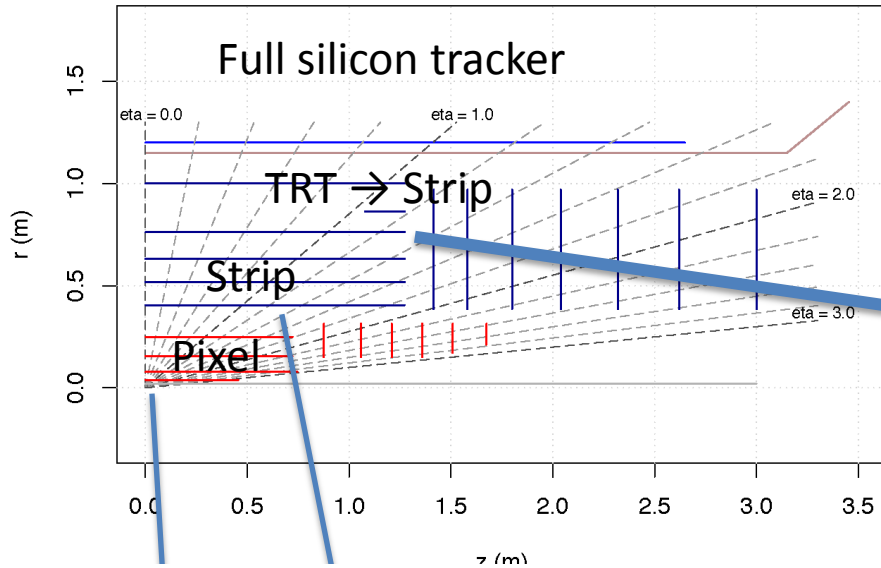
Phase-II: High-luminosity LHC. New focussing magnets for very high luminosity with levelling

HL-LHC

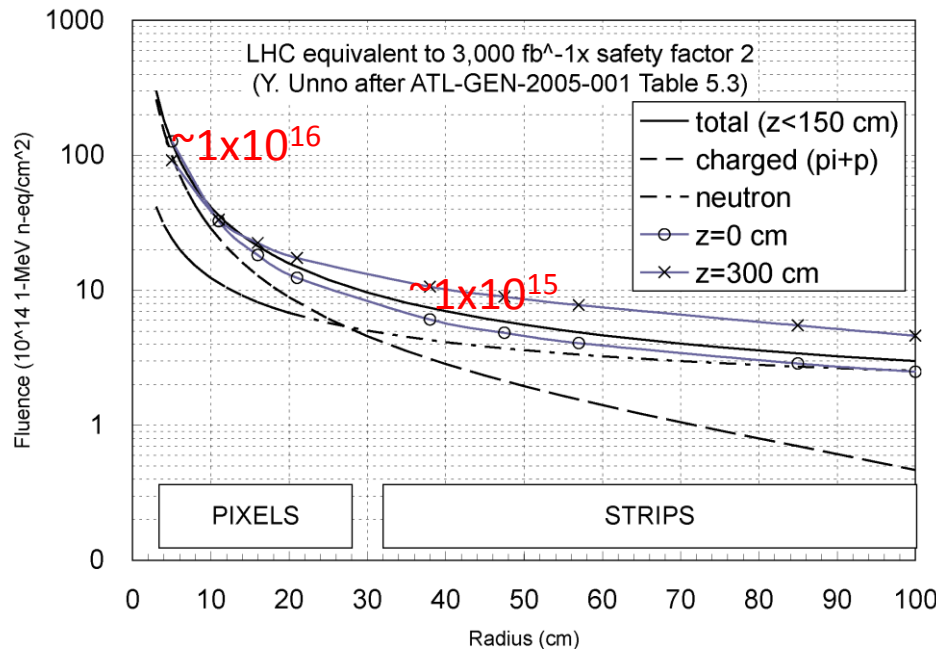
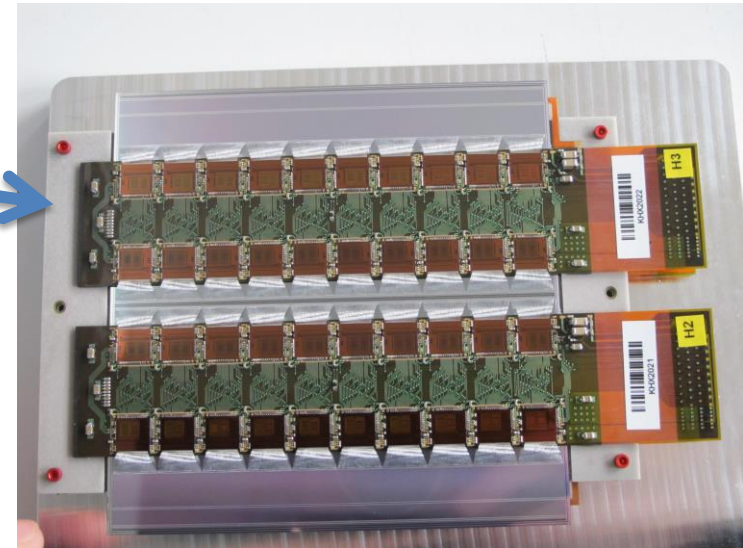
Run 4: Collect data until > **3000 fb^{-1}**

2030

Inner Detector Upgrade (HL-LHC)

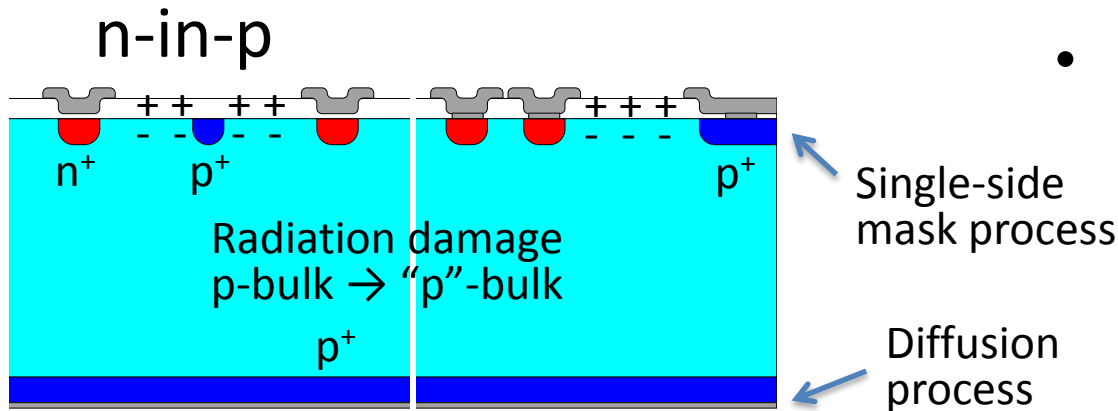
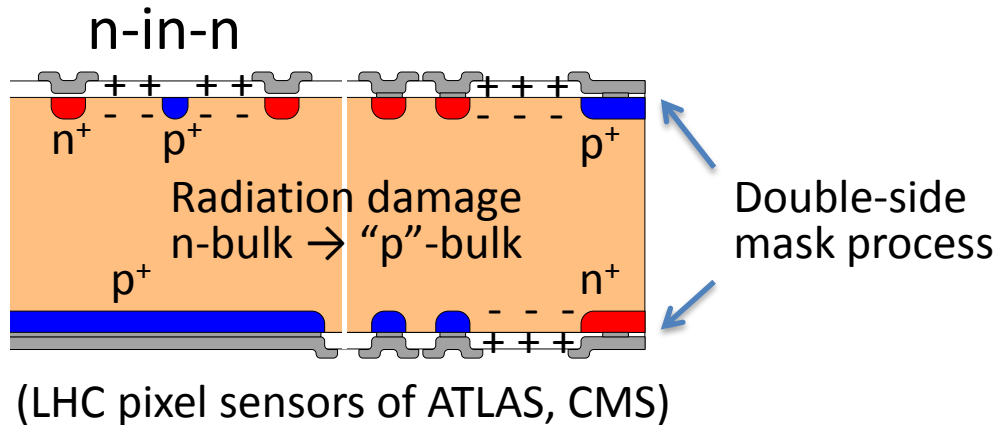


Strip sensor module, example



- 2022 – Full tracker replacement
 - Area: ~200 m²
 - Silicon strips: ~1 × 10¹⁵ neq/cm²
- Silicon sensors
 - Max. 1000 V operation
 - Full depletion might not be possible...

Cost-effective n-in-p planar sensor

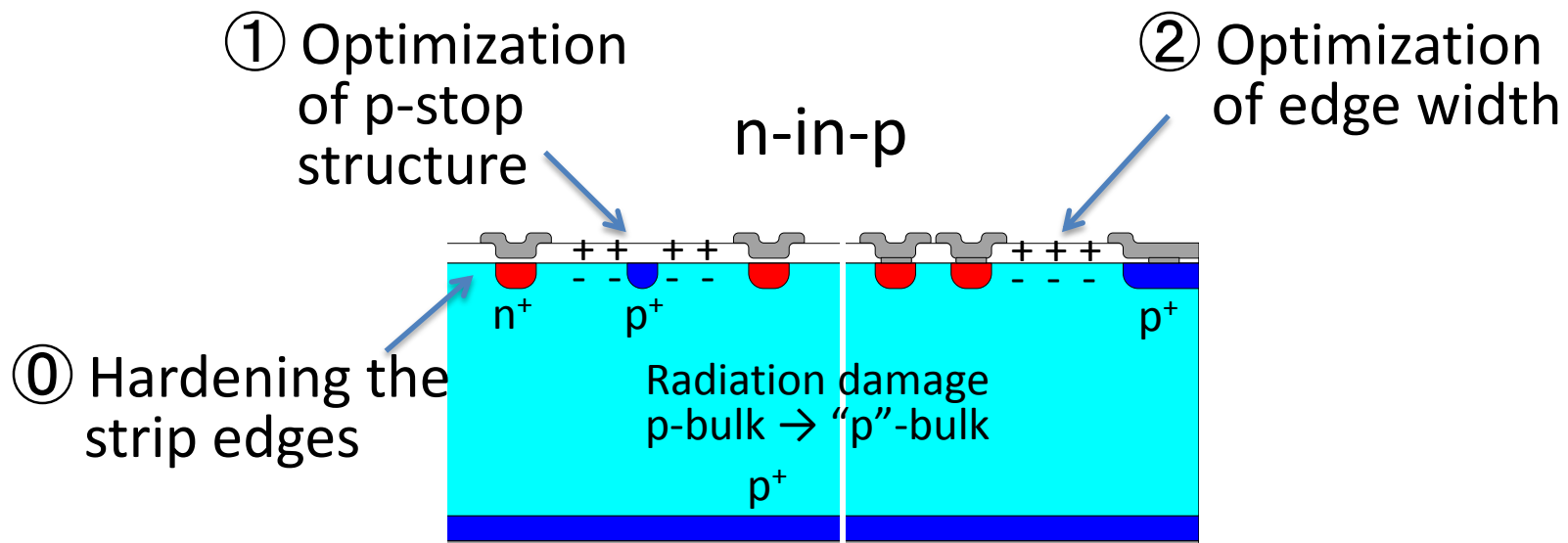


ATLAS choice for strip sensors for HL-LHC

- for heavy radiation environments
- Bulk radiation damage
 - one way to be "p" type
- n^+ readout
 - p-n junction to allow getting signals from "partially" depleted sensor
- Special in n^+ readout
 - conductive layer in the surface
 - $\sim M\Omega/\text{square}$
 - due to the electrons attracted to the oxide trap/fixated charges
 - no junction effect at the n^+ implant
 - the electron layer must be
 - interrupted (p-stop), or
 - cancelled (p-spray)

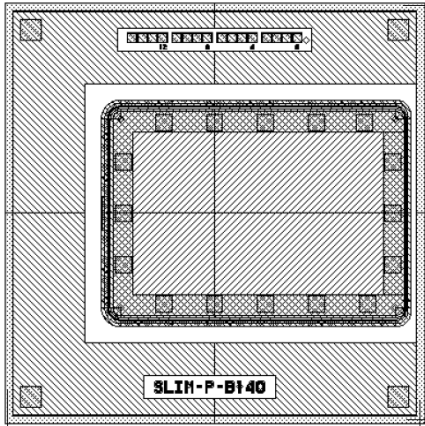
n-in-p sensors for HL-LHC

- Sensors with the p-stop isolation
- Operable to 1000 V bias voltage.
 - Equivalently, suppressing “microdischarge” breakdown up to ~ 1000 V
- How?
 - Those 0, 1, 2, backed by 3
 - In addition, protection against beam splash: punch-through-protection (PTP) structure



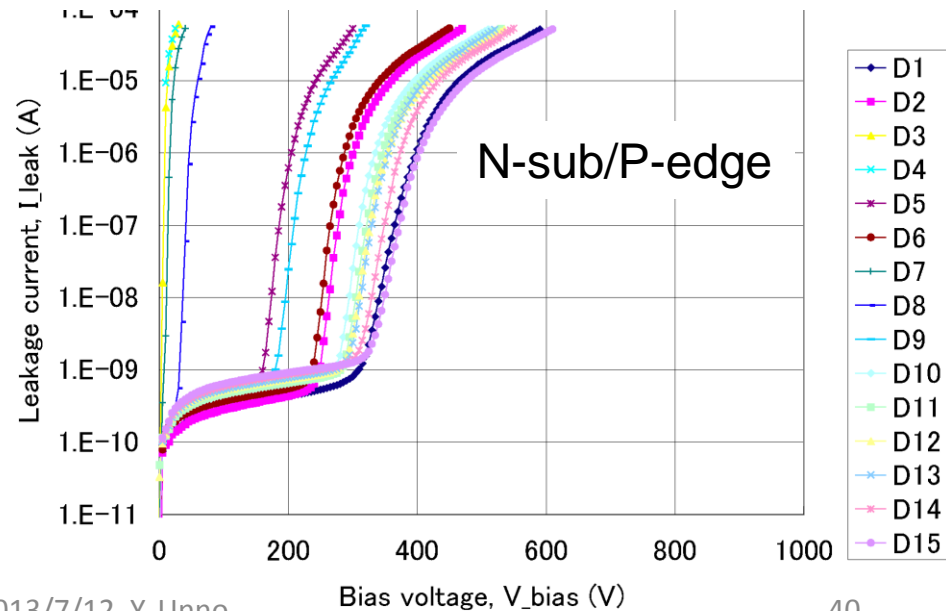
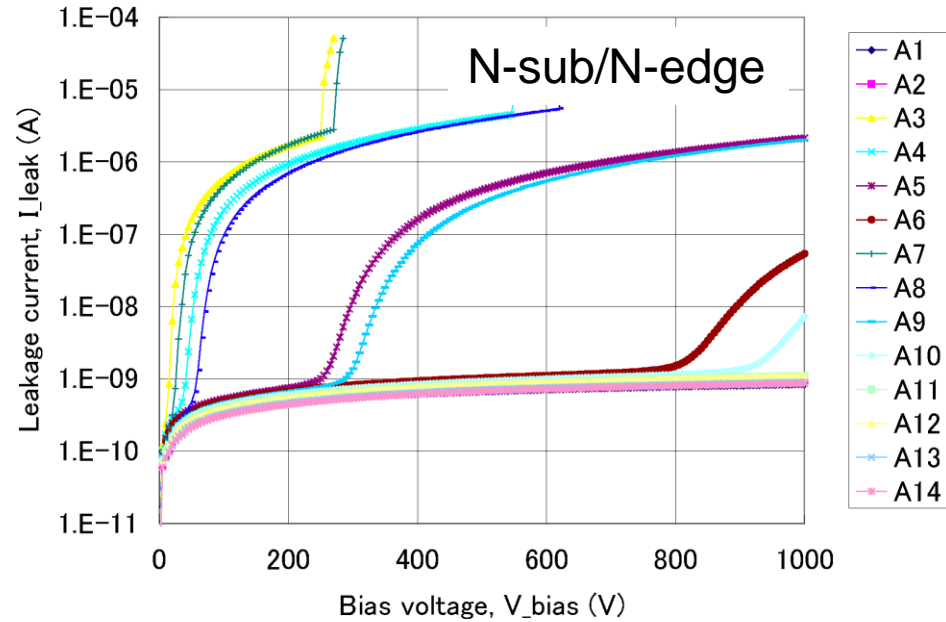
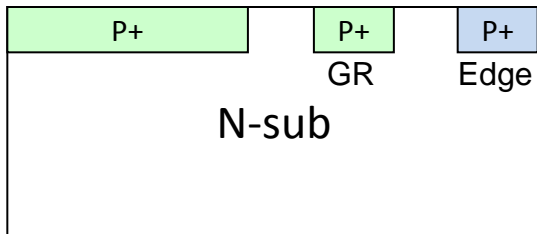
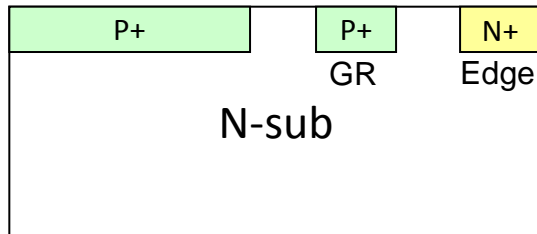
- ③ Understanding the physics
← Technology CAD (TCAD) simulation

Study of required edge width

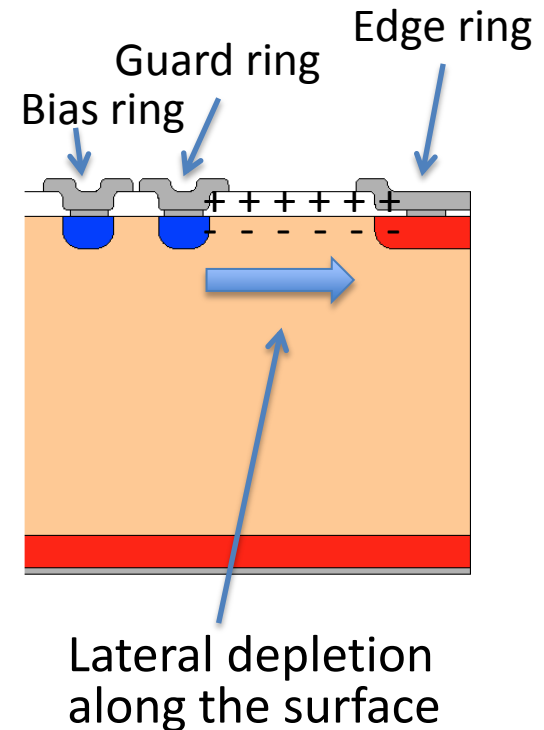
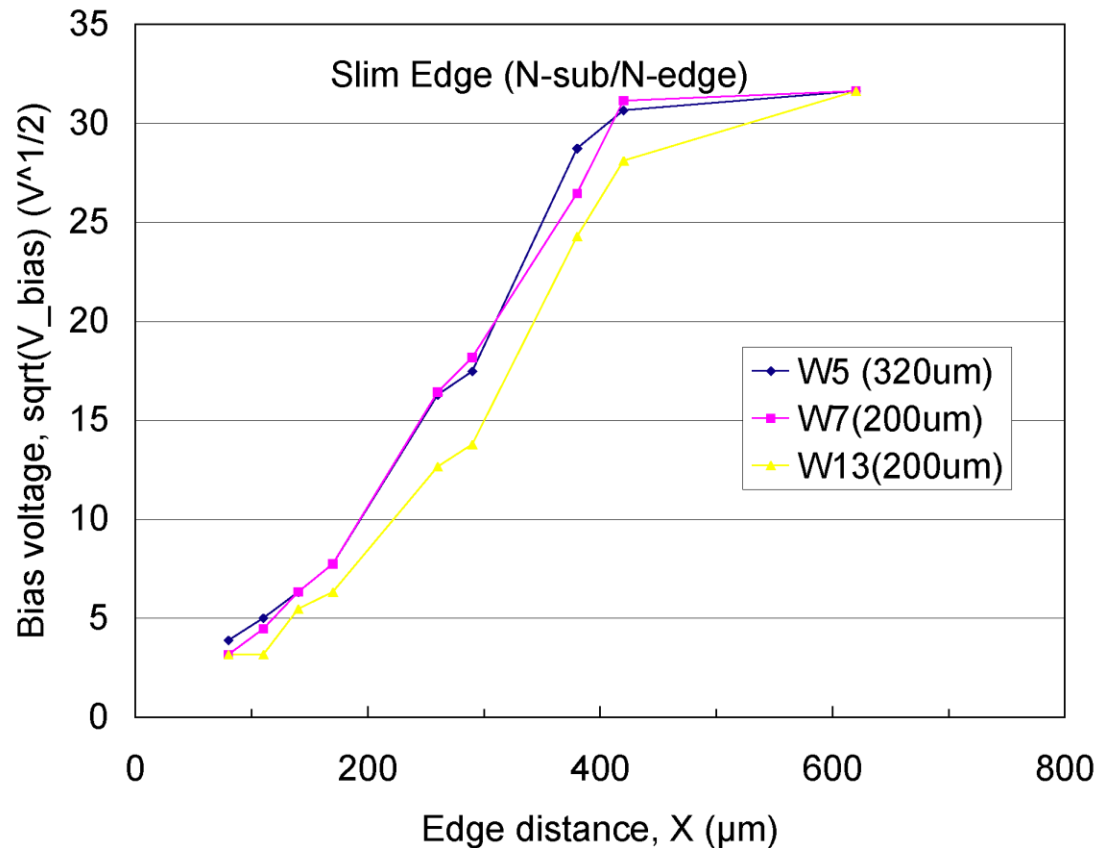


Width varied at one edge

- Results are from N-type wafer
- Thickness (as is, thinned)
 - 320 (W5), 200 (W7,13) μm
- Edge implantation
 - N+ or P+



Underlying physics of the edge width



- Square root of V_{bias} is linearly dependent on the edge distance
 - Reflecting the depletion along the surface
- Distance can be $\leq 500 \mu\text{m}$ for the bias voltage up to 1 kV
- ... Different story if the side wall is implanted e.g., - active edge

Required width after Irradiation

S. Mitsui et al, NIMA 699 (2013) 36-40

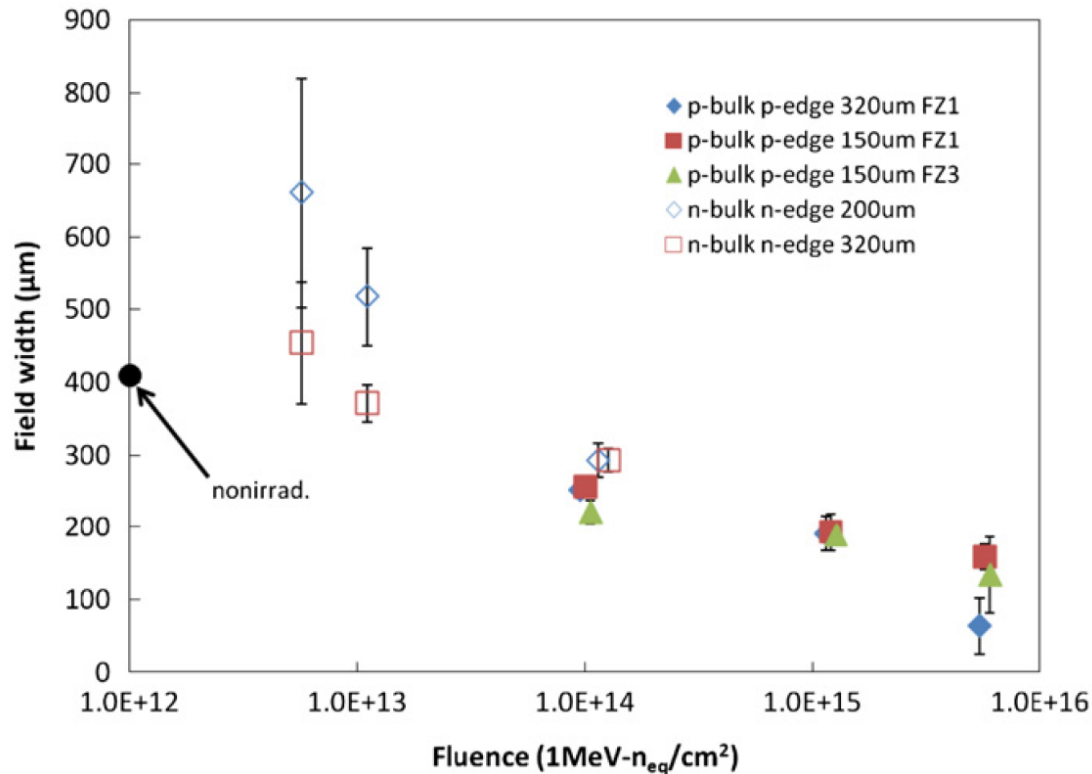
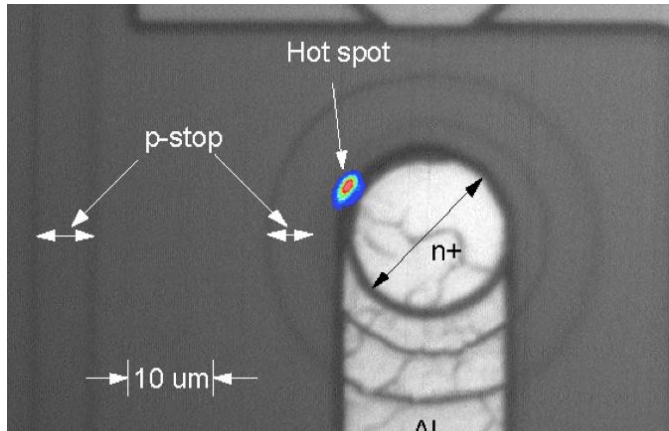


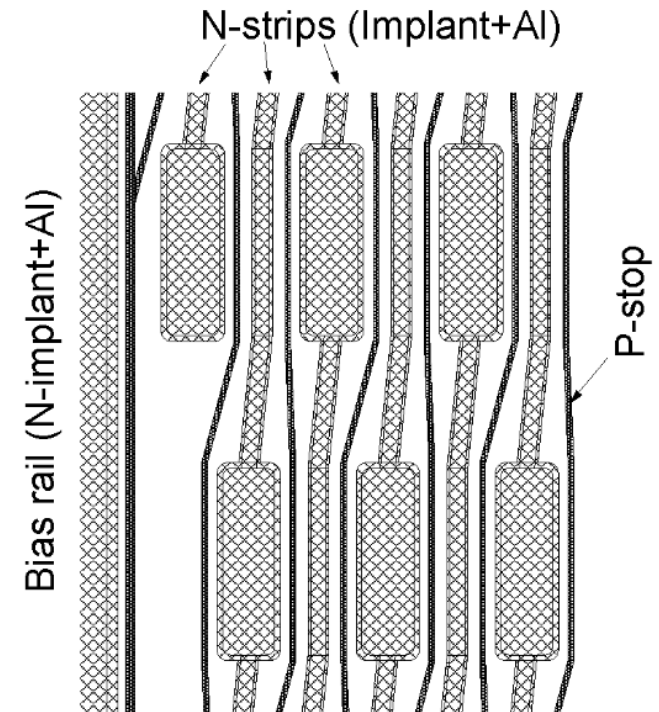
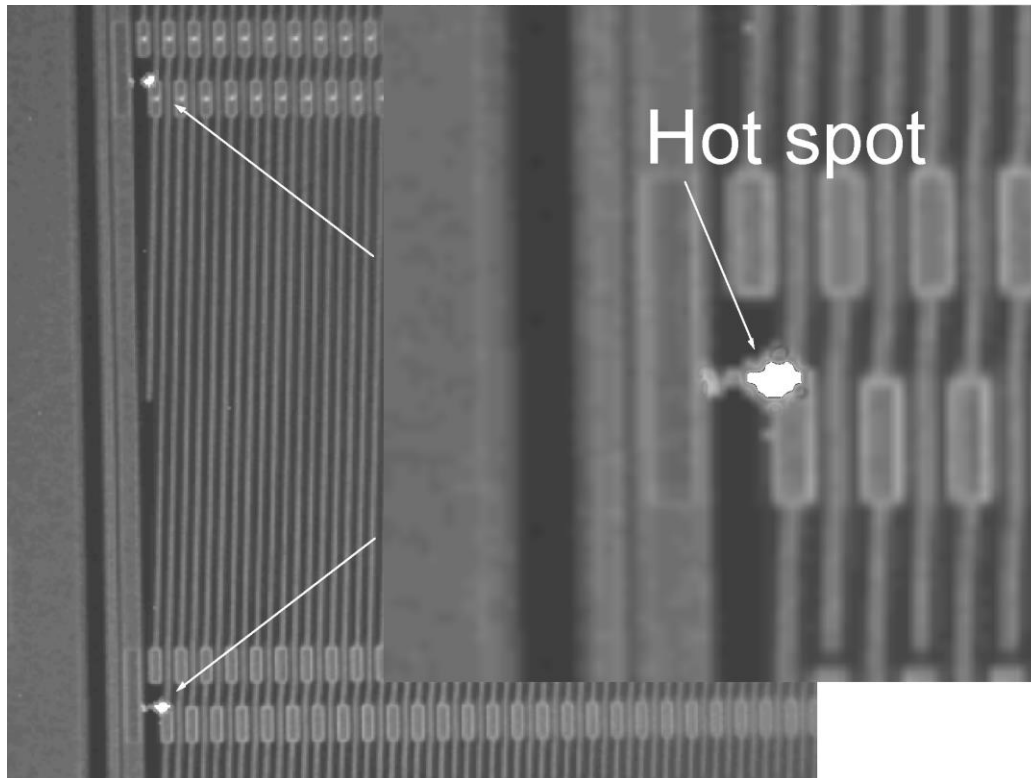
Fig. 5. Fluence dependence of field width hold up to 1000 V.

- Required width is ~450 μm to hold 1000 V.
 - At around 1×10^{13} , the required edge space is more than 450 μm, but also the depletion voltage is decreased less than that of non-irrad. and anyway it is much less than 1000 V.
 - At higher fluences, the required width is less than that of the non-irrad.

P-stops between N-implants

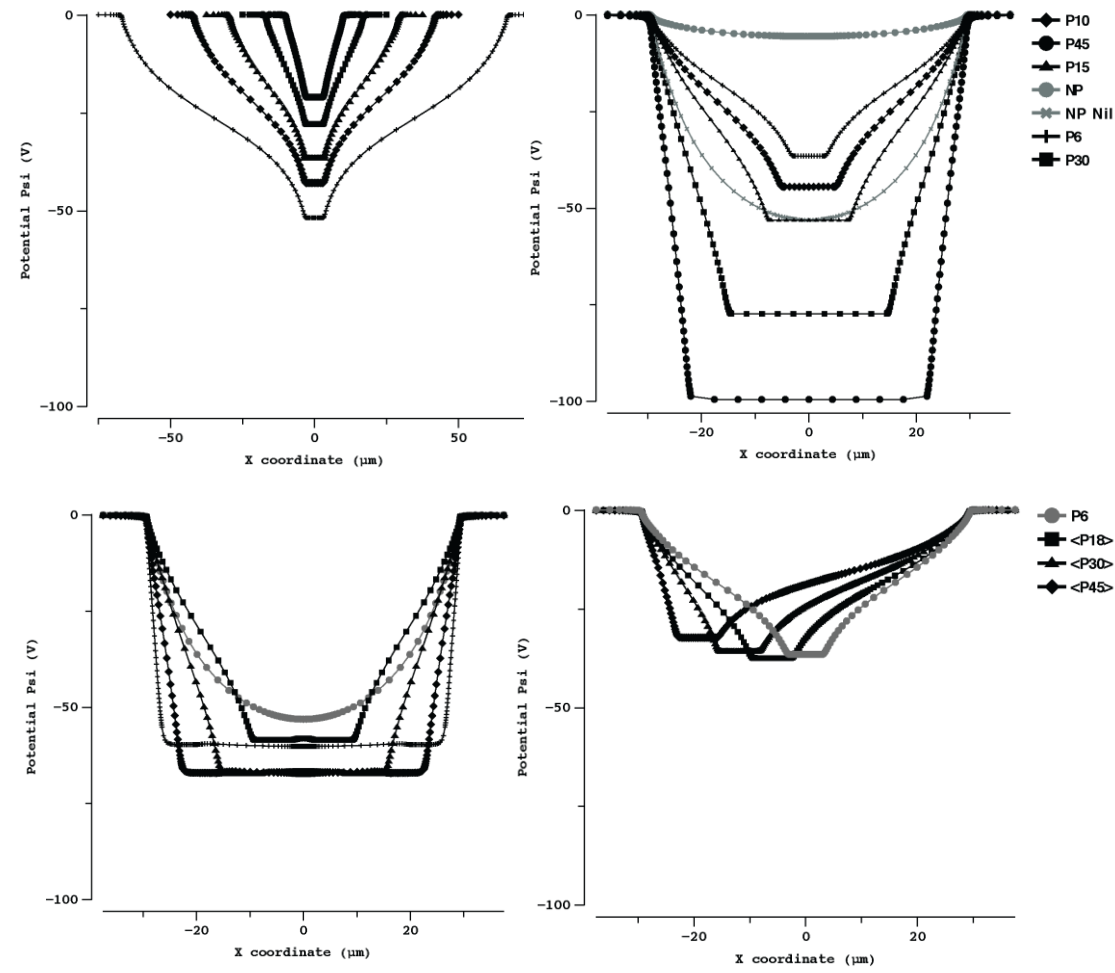
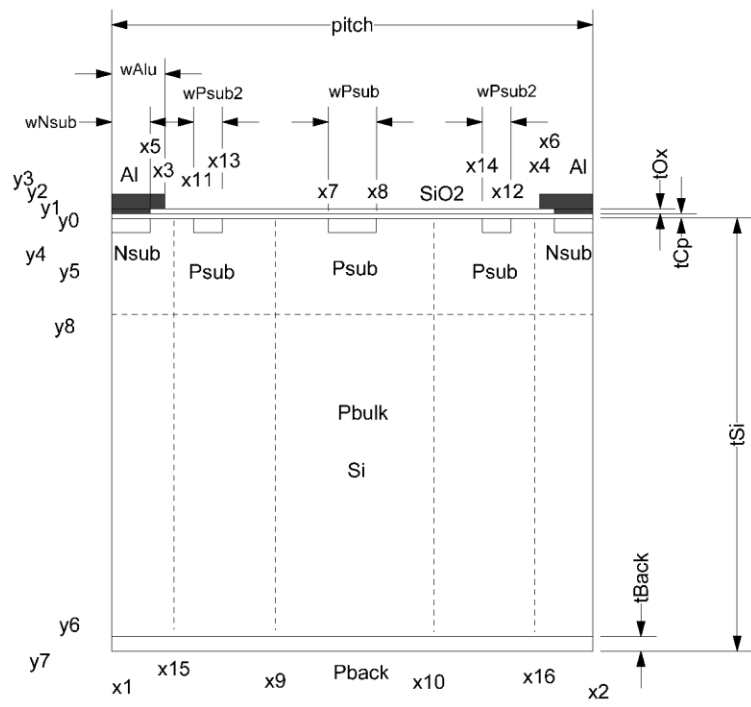


- Problems - Hot spots
 - IR image overlaid on visual image
 - Microdischarge = Onset of leakage current
- How to optimize the structures to reduce the electric fields?



P-stop Structures Optimization

- TCAD simulations

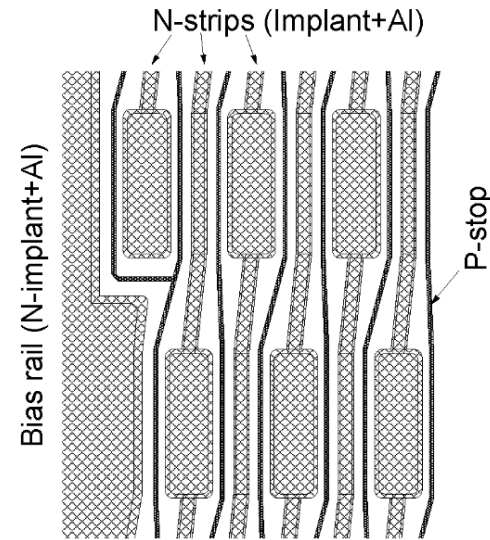
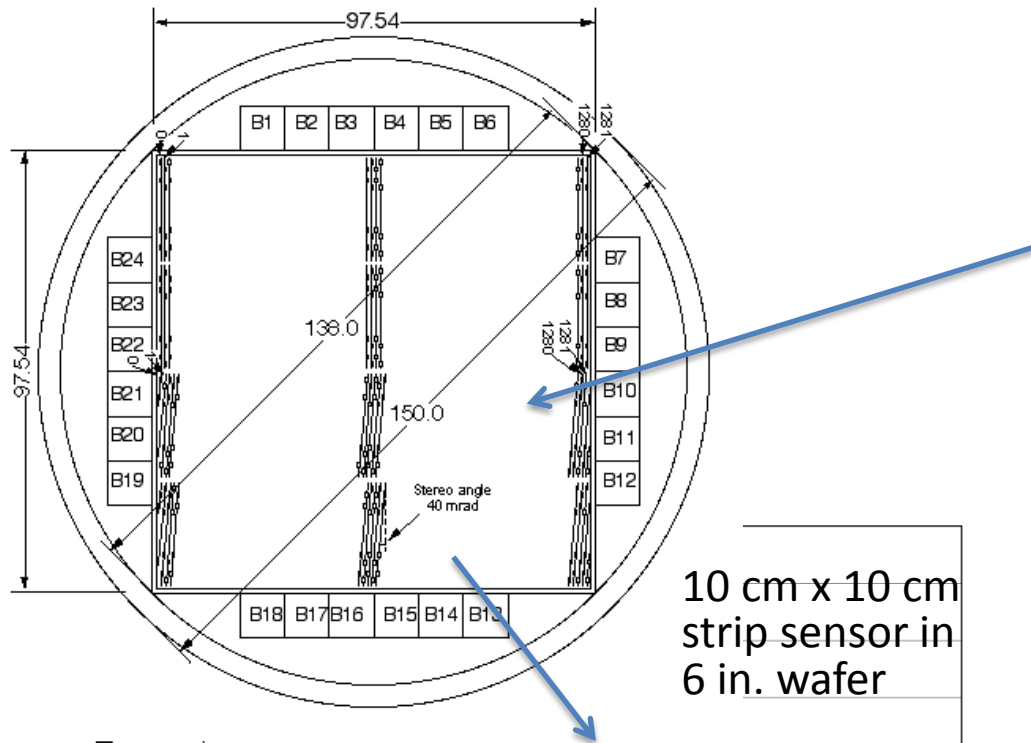


Y. Unno et al., Nucl. Instr. Meth. A636 (2011) S118–S124

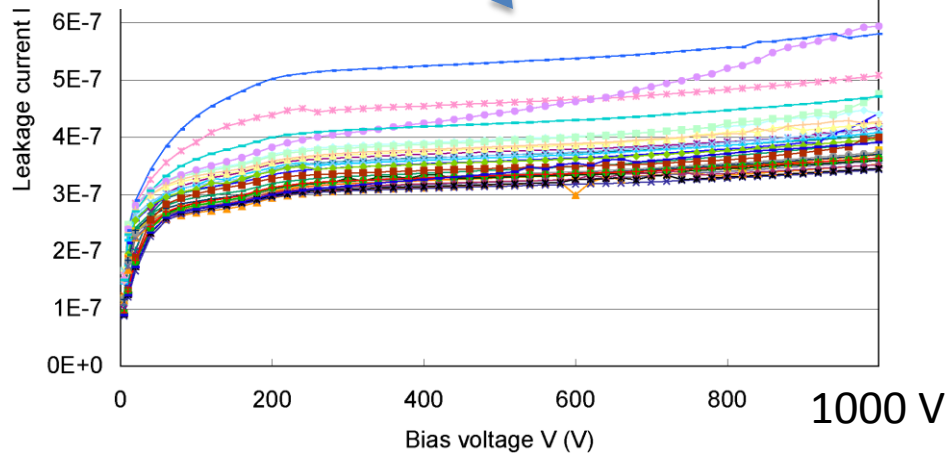


... and comparison with test structures

Optimization of the p-stops



Stereo strip section



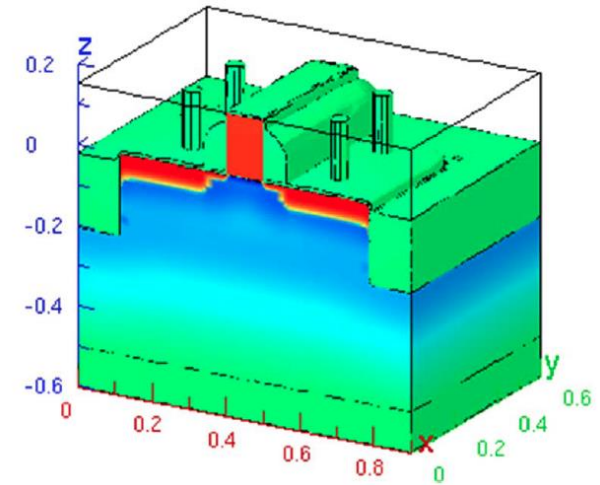
- P-stop
 - away from the n-implant
 - symmetric location
- N-implant
 - pitch not too narrow nor not too wide
- Once known, simple.

Technology CAD (TCAD)

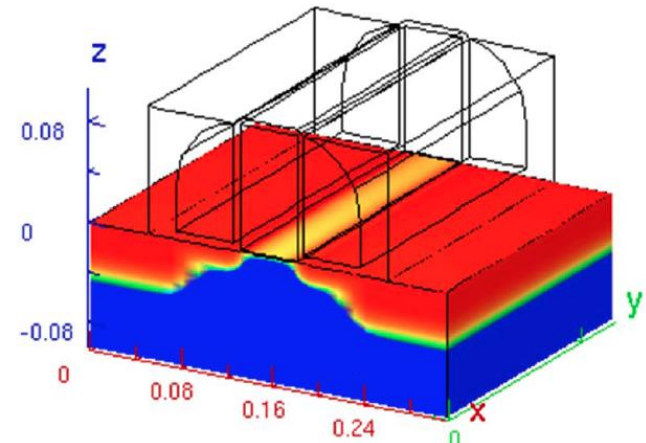
- TCAD started to build the links between the
 - semiconductor physics and electrical behavior
 - to support circuit design
- Modern TCAD consists of
 - Process simulation, and
 - Device simulation
- Originated from the work of
 - Prof. Robert W. Dutton and his group at Stanford Univ.
- Widely used in semiconductor industry
 - to reduce the development cost and time
 - to understand the physics behind
 - that is even impossible to measure
- TCAD: Computer Aided Design for Semiconductor Technology

- The core is the “Finite Element Analysis”.
 - The numerical analysis method with modern computer.

MOS transistor



Process simulation



Device simulation

Brief History

1977: Prof. Dutton, Stanford
Process/Device simulator
SUPREM-I (1D)/PISCES

1979: Technology Modeling Associates
(TMA/Synopsys)
TSUPREM4 (2D)/MEDICI

1989: Silvaco International
ATHENA (2D)/ATLAS

1989: Integrated Systems Engineering AG
(ISE)/Synopsys
DIOS (2D)/DESSIS

1992: TMA
TAURUS (3D TSUPREM4/DEDICI)

1993: Prof. Law, Florida
Process sim: FLOOPS (3D)

2002: ISE
FLOOPS (3D)

2005: Synopsys
Sentaurus (3D TAURUS)

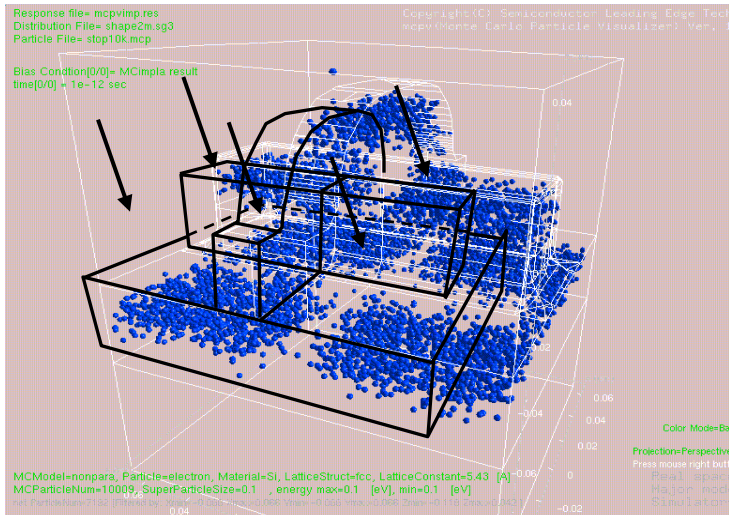
TMA⇒AVANT!/1998⇒Synopsys/2001
ISE⇒Synopsys/2004



Prof. Robert W. Dutton
(from Stanford TCAD Home page)

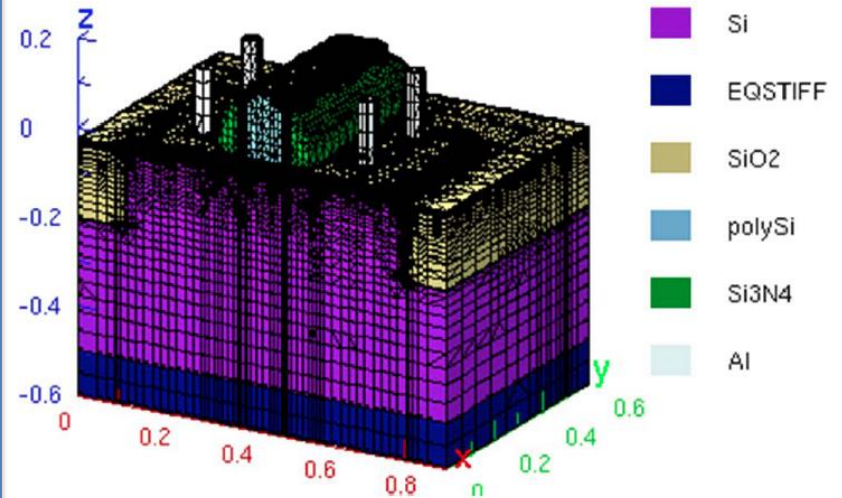
In Japan,
1996: 3D HyENEXSS (Selete/TCAD Int.)
Selete: Consortium of 10
semiconductor co.
2011: 3D HyENEXSS (Selete)
Project ends

Process Simulator | Device Simulator



ion-implantation process (M.C.-model)

- Process steps
 - Oxidation
 - Deposition
 - Etching
 - Ion implantation
 - Annealing
- Mostly for process experts
 - Unless you know the process parameters, you have no way to simulate.



- Solving equations
 - Poisson eq. (ψ , n , p)
 - Current continuity eq. J_n , J_p (ψ , n , p)
 - Heat conduction eq. (“Drift Diffusion model) (TL)
 - ...
- Four equations and four variables
 - potential ψ , electron-density n , hole-density p , and lattice-temperature TL

Caveat

- **Jungle of semiconductor physics models and parameters**
 - Device simulator e.g.,
 - Transport models
 - Mobility models
 - Generation-recombination models (SRH, Auger, II, trap, surface...)
 - SRH: Shockley-Read-Hall model
 - II: Impact Ionization model
- **Finite Element method**
 - 3D vs. 2D
 - 3D: Usually “very” time consuming
 - 2D: Most of the cases, good enough
 - Meshing: resolution vs. time
 - Convergence of calculations
 - **Try and error for finding best procedures** (method, physics model)
- The real caveat is
 - **“What you get is what you put.”**
 - Although semiconductor industry is trying to simulate perfectly, we may still miss models, e.g., radiation damages

TCAD Simulations

- Semiconductor Technology Computer-Aided Design (TCAD) tool
 - ENEXSS 5.5, developed by SELETE in Japan
 - Device simulation part: HyDeLEOS
- N-in-p strip sensor
 - 75 μm pitch, p-stop $4 \times 10^{12} \text{ cm}^{-2}$
 - 150 μm thickness
 - p-type bulk, $N_{\text{eff}} = 4.7 \times 10^{12} \text{ cm}^{-3}$, $V_{\text{FDV}} = 80 \text{ V}$ at 150 μm
- Radiation damage approximation:
 - Increase of acceptor-like state \rightarrow Effective doping concentration
 - Increase of leakage current \rightarrow SRH model
 - Increase of interface charge \rightarrow Fixed oxide charge

Bulk leakage current

After irradiation, the current increases as a function of fluence

$$\Delta I / V \sim \alpha \times \phi (n_{eq}/\text{cm}^2)$$

$\alpha \sim 4 \times 10^{-17} \text{ (A/cm)}$: damage constant

E.g.,

$$\text{Volume} = 75 \mu\text{m} \times 1 \mu\text{m} \times 150 \mu\text{m} = 1.13 \times 10^{-8} \text{ cm}^3$$

$$\phi = 1 \times 10^{15} \text{ n}_{eq}/\text{cm}^2$$

$$\Delta I \sim 45 \text{ nA}$$

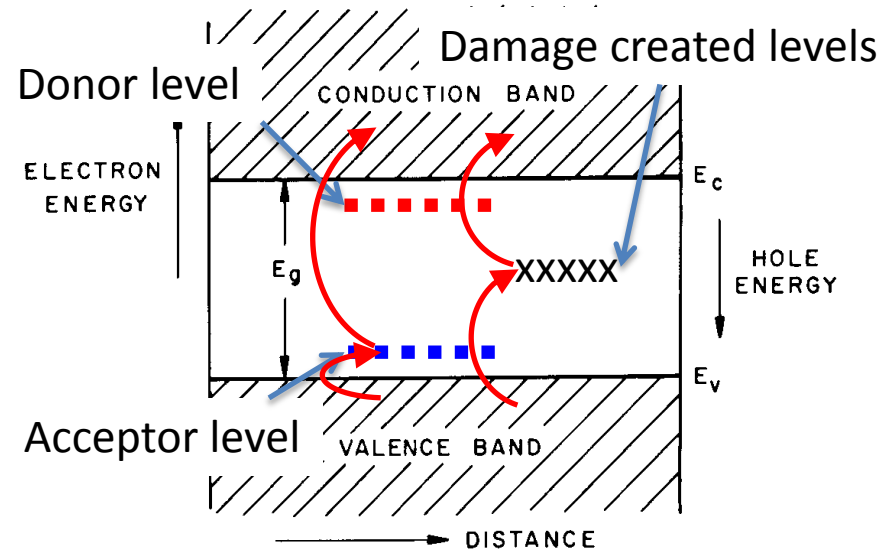


Fig. 6 Simplified band diagram of a semiconductor.

- Community has a view that
 - the leakage current increases with an introduction of levels near the middle of the forbidden band,
 - with the energy of band gap being half (of the full gap), the leakage current flows order of magnitude larger...
- Unfortunately, we have no freedom to change/add a program to the ENEXSS, but
 - we can simulate the leakage current by modifying the model parameters to an unrealistic world...

Shockley-Reed-Hall (SRH) Model

- Leakage current: SRH model
 - Generation-recombination of carriers (electrons and holes) by thermal effect
 - A_n, A_p : model parameters
 - Decrease them as though increasing temperature

$$U_{SRH} = \frac{n_i^2 - pn}{\tau_p (n + n_i) + \tau_n (p + n_i)}$$

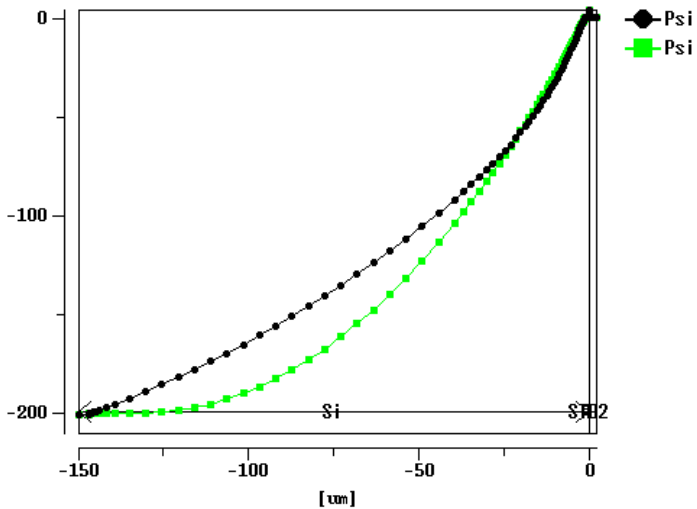
$$\tau_{n,p} = A_{n,p} \left(\tau_{\min}^{n,p} + \frac{\tau_{\max}^{n,p} - \tau_{\min}^{n,p}}{1 + \left(N / N_t^{n,p} \right)^{B_{n,p}}} \right)$$

n_i : intrinsic carrier density,

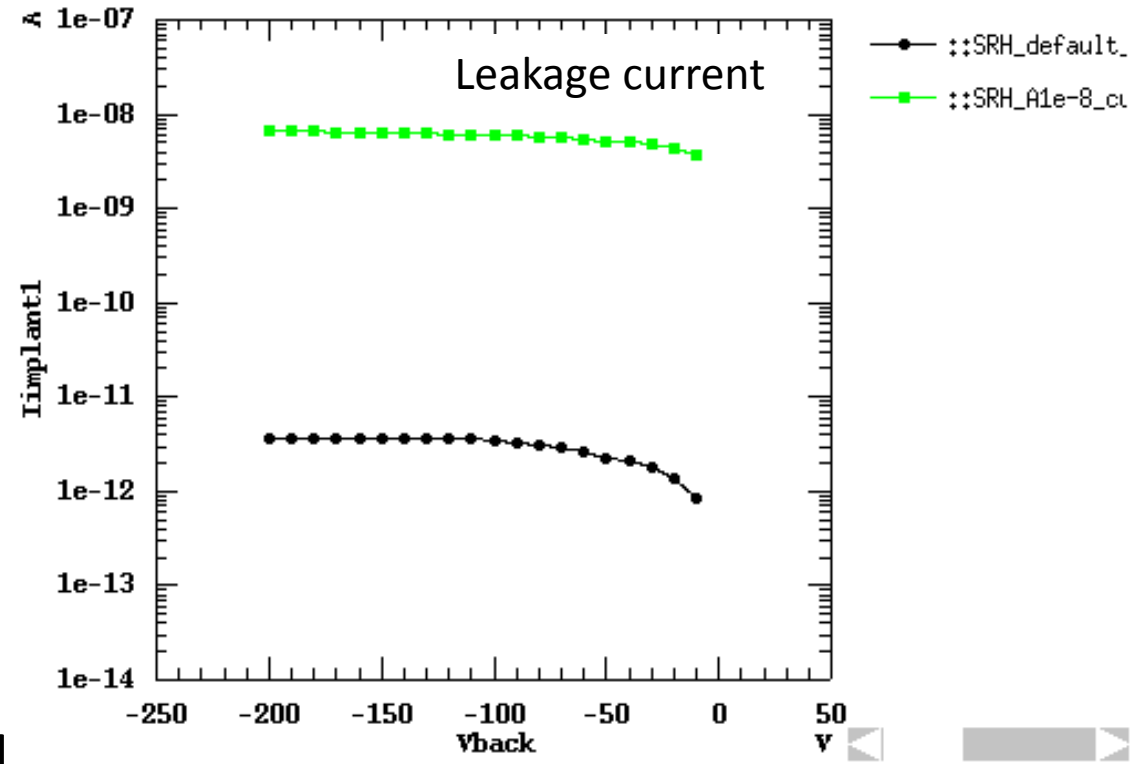
n, p : electron, hole carrier density

Radiation Damage Approximation

Potential in bulk



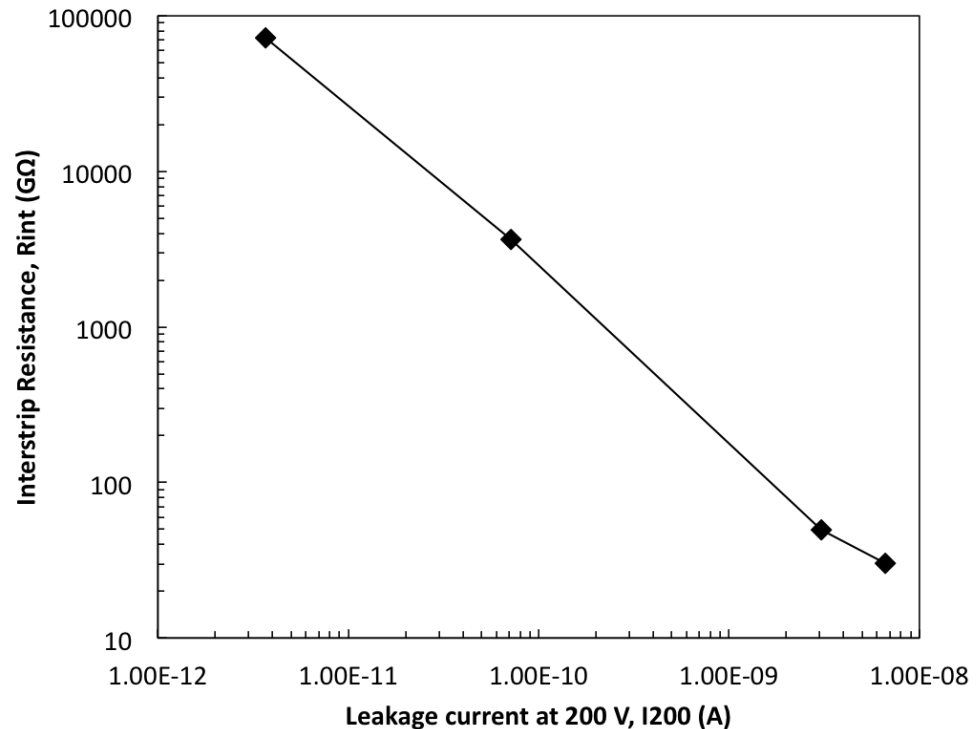
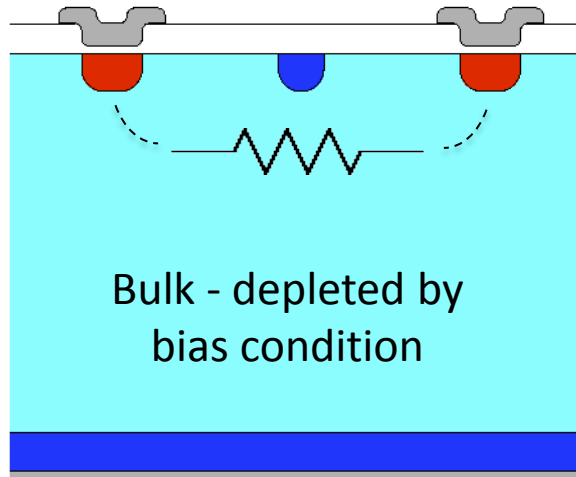
Leakage current



Backplane at 200 V

- Black: non-irrad.
 - $N_{eff} = 4.7 \times 10^{12} \text{ cm}^{-3}$, $A_n, A_p = 1.0$
- Green: Irrad.
 - Increase of full depletion voltage, $N_{eff} = 1.5 \times 10^{13} \text{ cm}^{-3}$
 - Increase of leakage current, $A_n, A_p = 1 \times 10^{-8}$

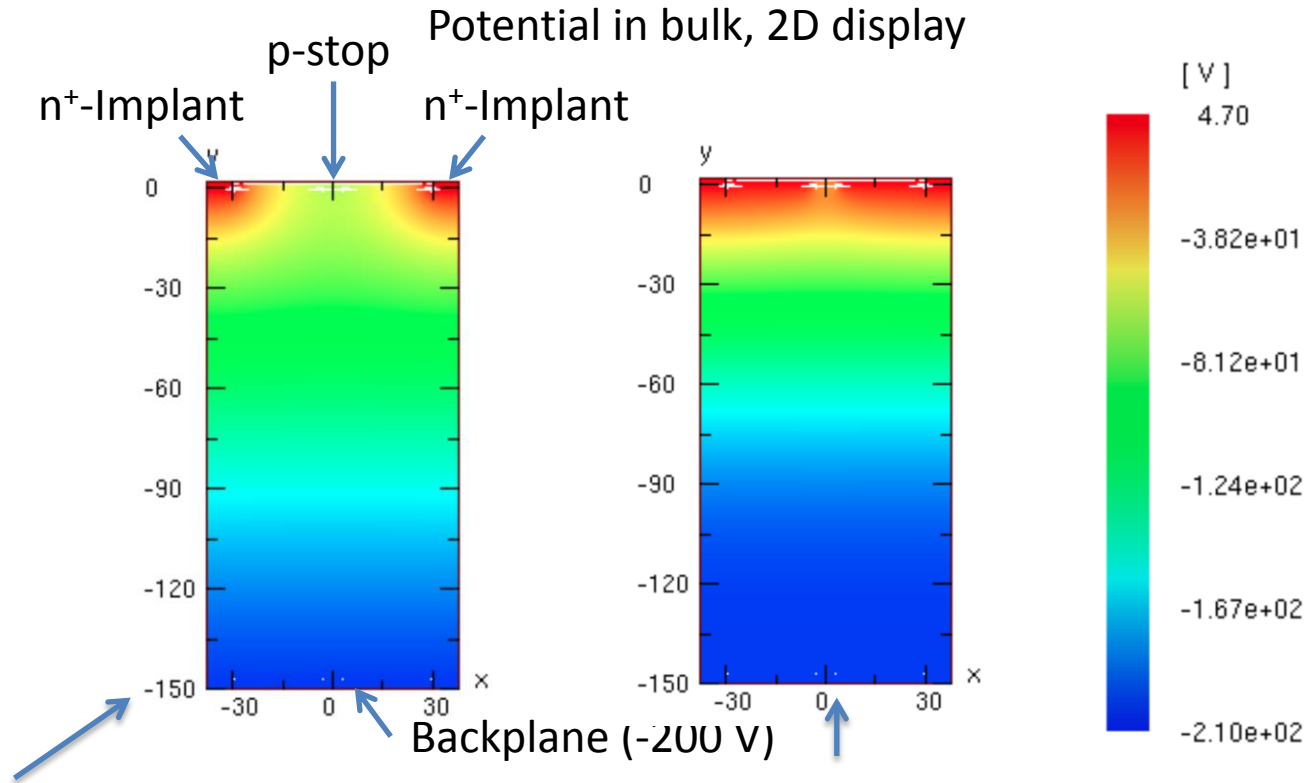
Interstrip Resistance, R_{int}



- Decrease of interstrip resistance after irradiation
 - is quantitatively explained by the increase of leakage current.
 - Other factors, the effective doping concentration nor the oxide interface charge, do not change the interstrip resistance.
 - In retrospect, it is natural that the current is the other manifestation of the resistance.

Electric potential of p-stop

- Introduction of Si-SiO₂ interface charge -



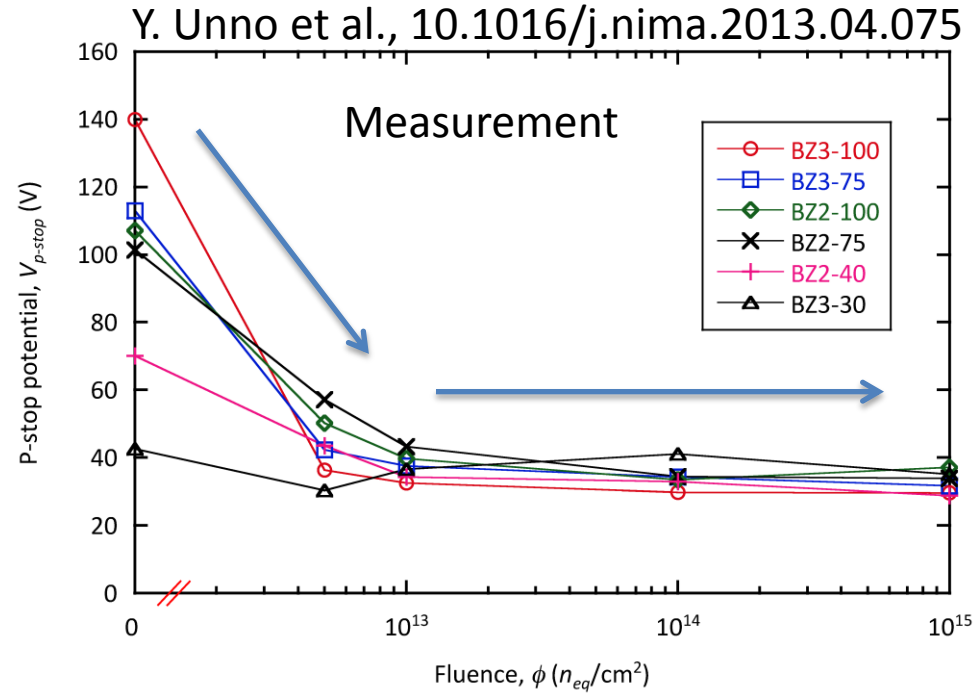
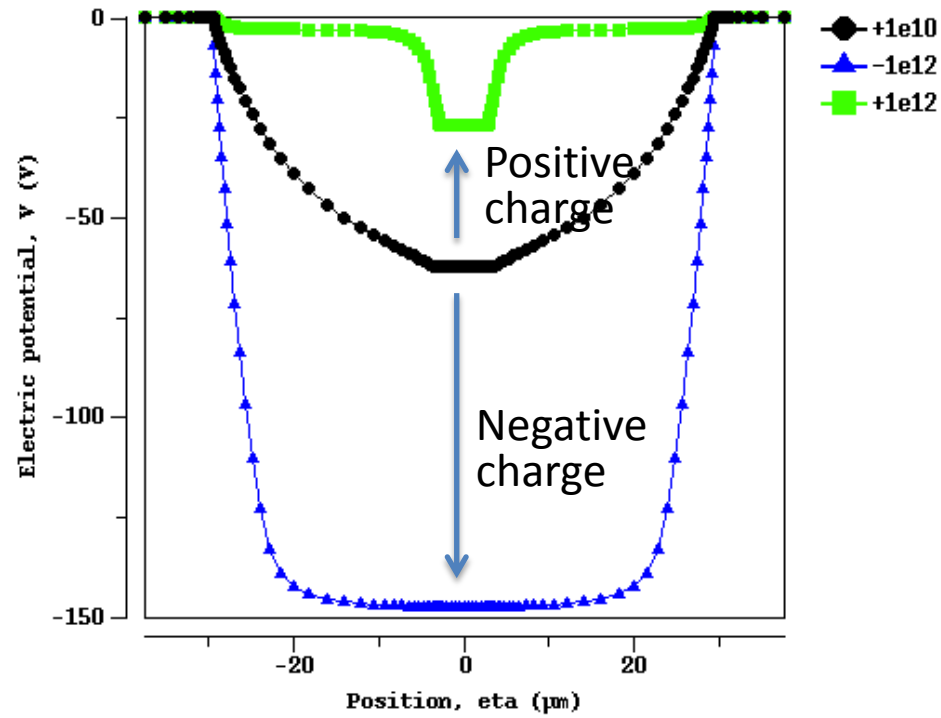
- Non-irrad:

- $N_{eff} = 4.7 \times 10^{12} \text{ cm}^{-3}$,
- SRH $A_n, A_p = 1.0$,
- Fixed Oxide Charge = $1 \times 10^{10} \text{ cm}^{-2}$

- Irrad:

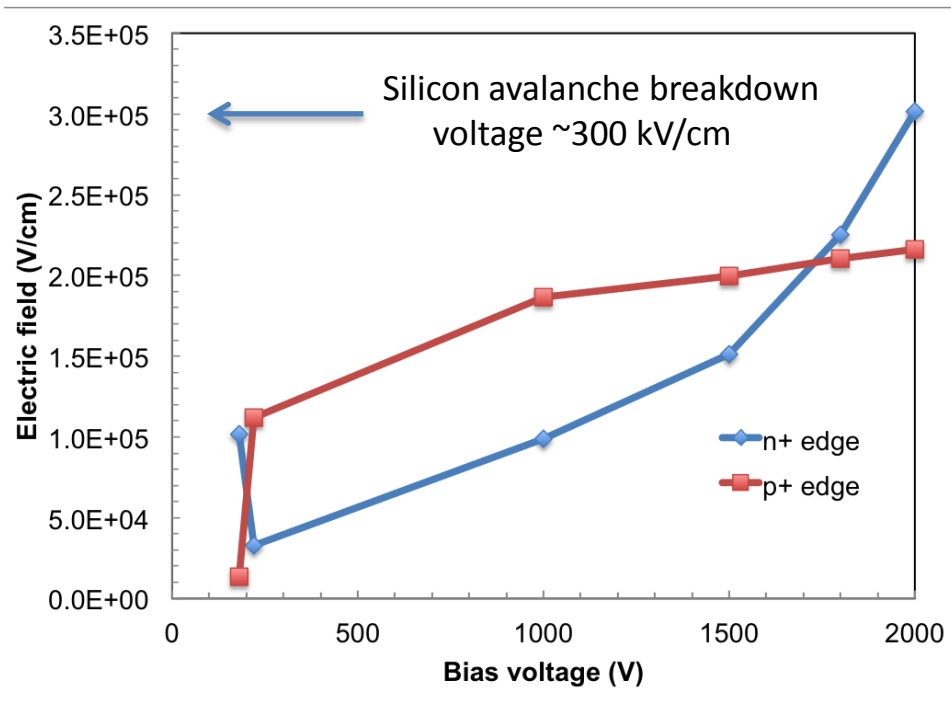
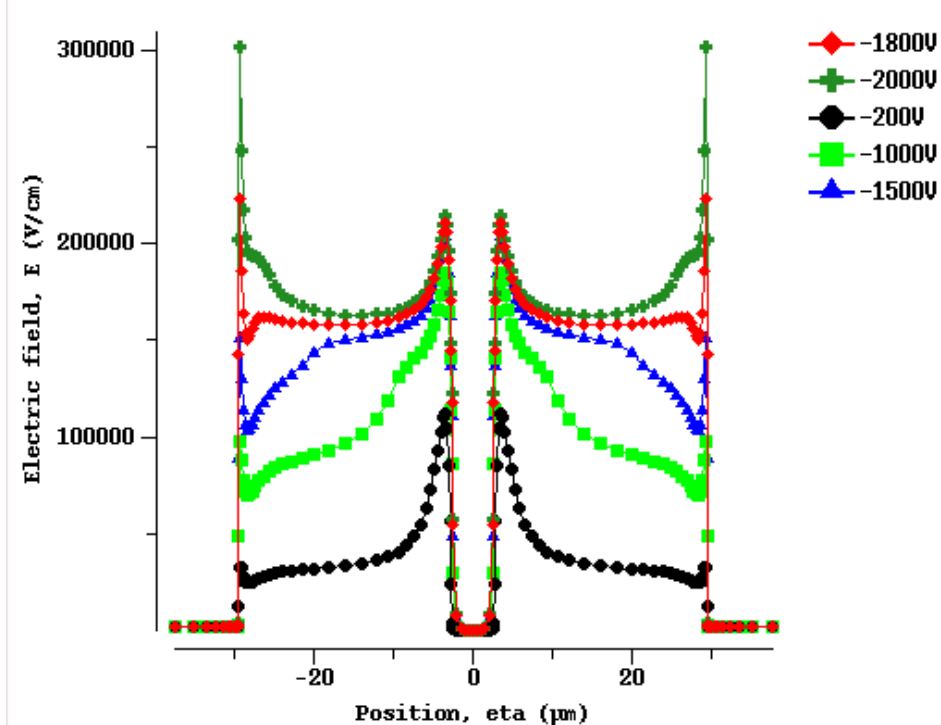
- $N_{eff} = 1.5 \times 10^{13} \text{ cm}^{-3}$,
- SRH $A_n, A_p = 1 \times 10^{-8}$,
- Fixed Oxide Charge = $1 \times 10^{12} \text{ cm}^{-2}$

Electric Potential between Strips



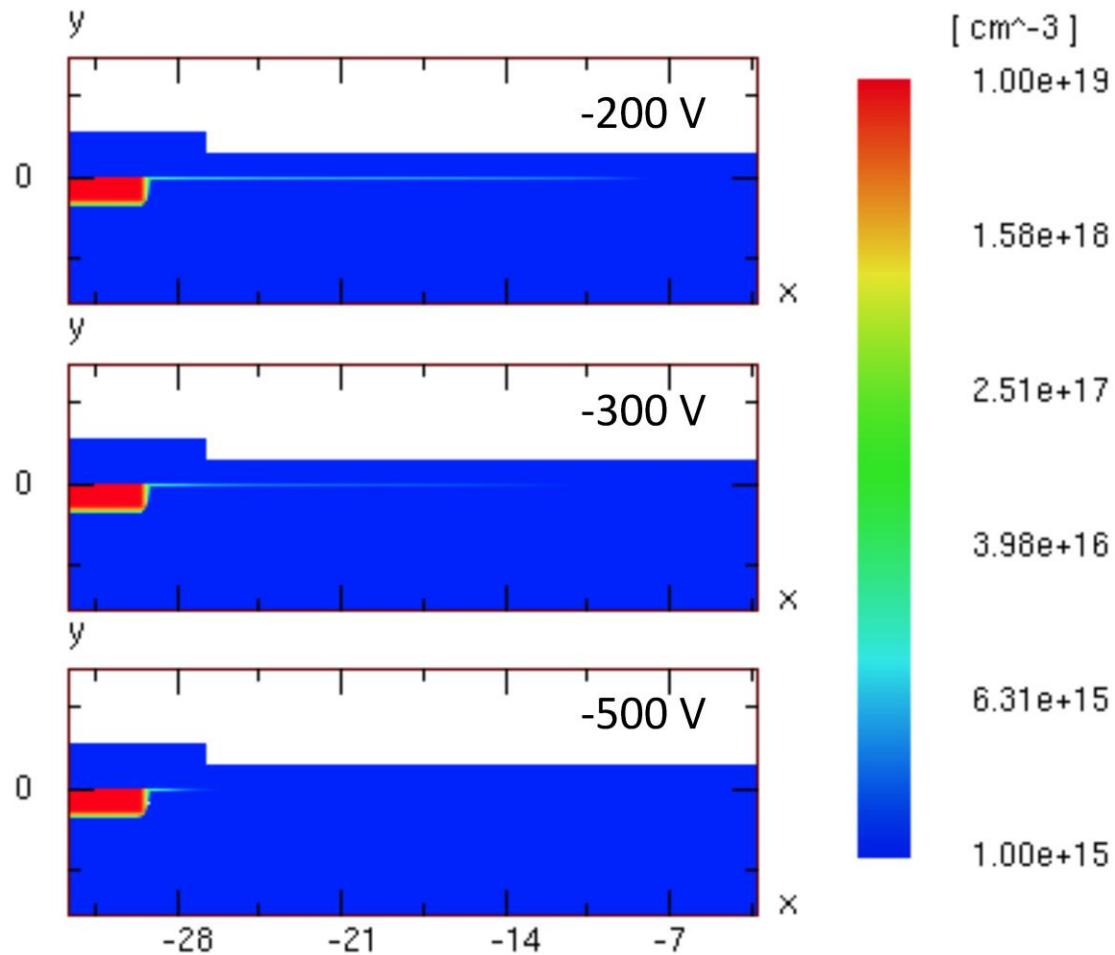
- Electric potential of p-stop
 - decreases as the interface charge increases positively,
 - increases as the interface charge increases negatively.
- Measurement confirms that the interface charge is positive.

Breakdown at High Voltages



- Under the “Irradiated” condition
- Breakdown occurs at high voltage at the n^+ edge, although the p-stop edge was the higher electric field initially.
- The rate to increase of the electric field at the p-stop edge is saturating at higher voltage.
- The p-n junction eventually overtakes the highest electric field by the time of breakdown.
- Why?

Insight into the physics



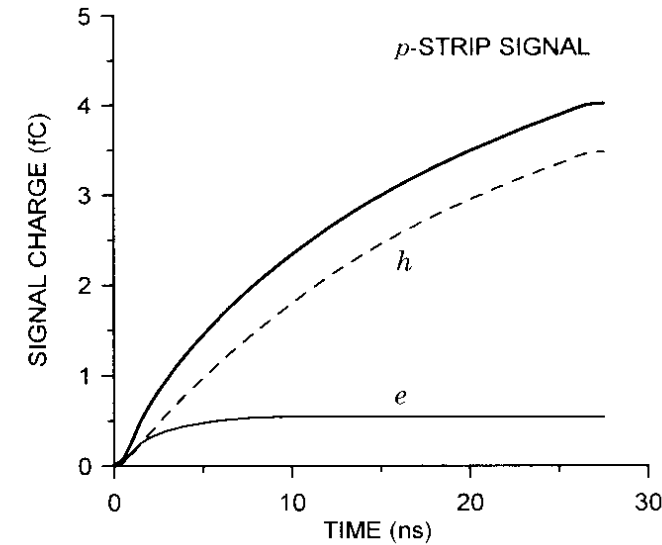
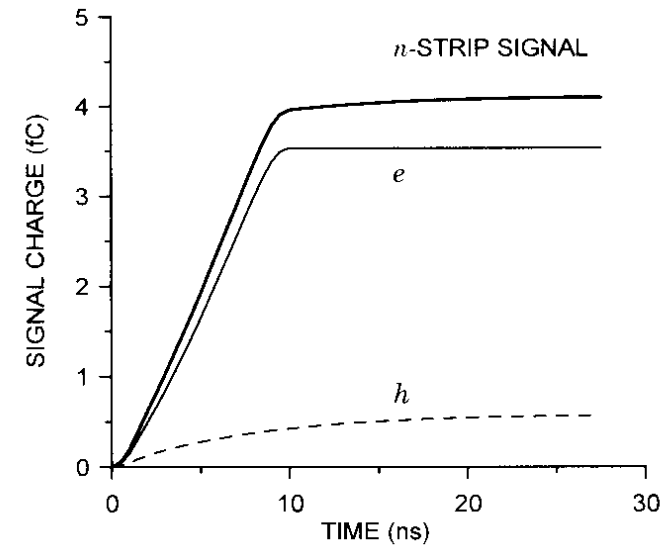
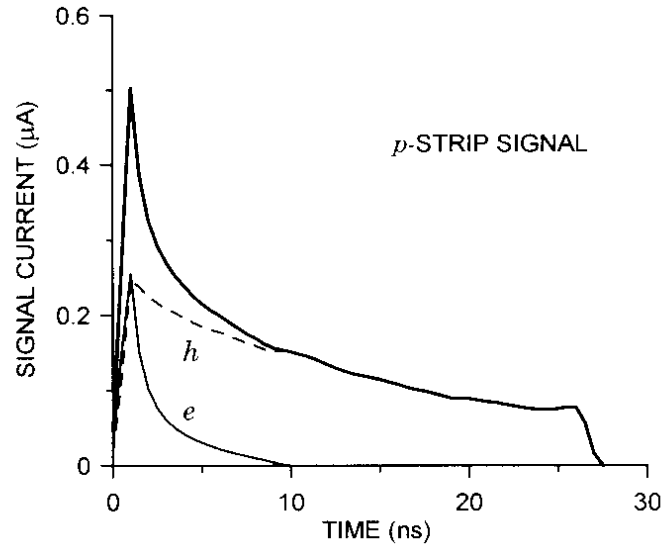
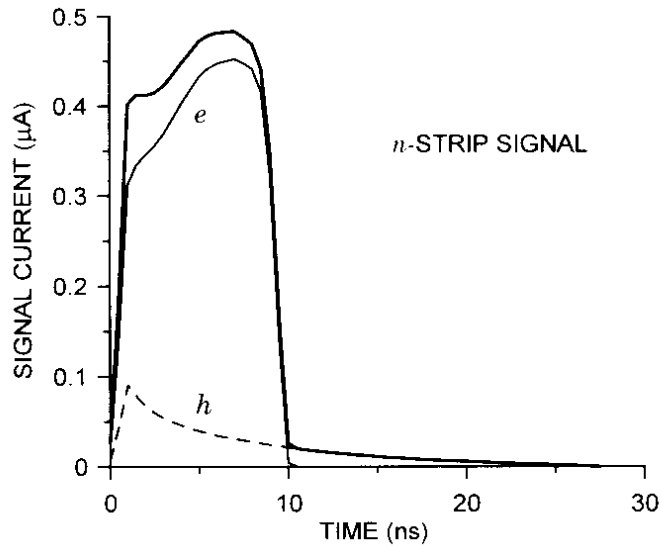
- Electron inversion layer is diminishing
 - as the bias voltage is being increased.
 - This also explains that in p-bulk the bias voltage helps to isolate the n^+ implants.
- Understanding the underlying physics is only possible with TCAD simulation, eventually ...

Summary

- Brief overview of the current ATLAS silicon microstrip tracker (SCT)
 - ATLAS SCT strip detector is working well (so far).
- Issues and achievement of the LHC tracker
 - Radiation level, 2×10^{14} 1-MeV n_{eq}/cm^2
 - Radiation damage effects were identified
 - High voltage operation was designed up to 500 V
 - Strip edge hardening against high electric field was applied
- New technological achievement for the high-luminosity LHC tracker
 - Radiation level, $\sim 1 \times 10^{15}$ 1-MeV n_{eq}/cm^2
 - High voltage operation up to 1000 V
 - Minimum dead area in the edge is evaluated
- TCAD simulation
 - Simulating radiation damage effects with approximation
 - Very effective in understanding the underlying physics
- This is all about of the conventional planar silicon microstrip sensor...
 - You still have a lot of challenges ahead in different world/requirements.

Appendix

Signal from n^+ or p^+ strips

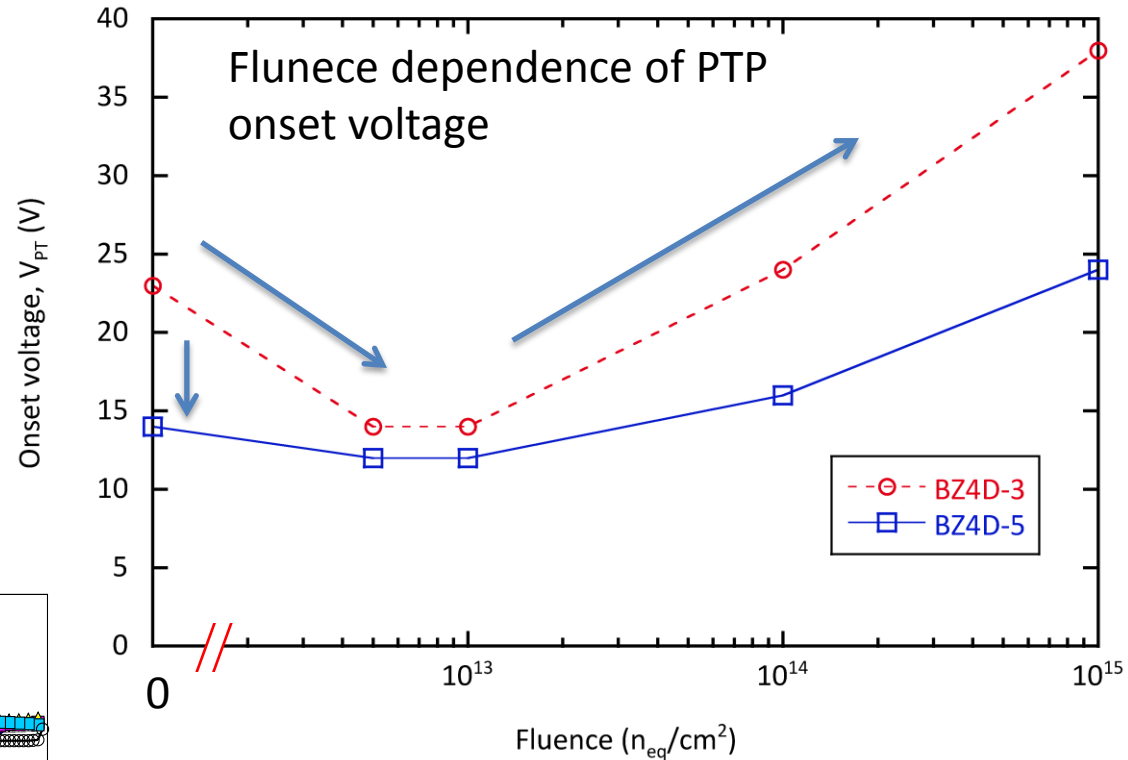
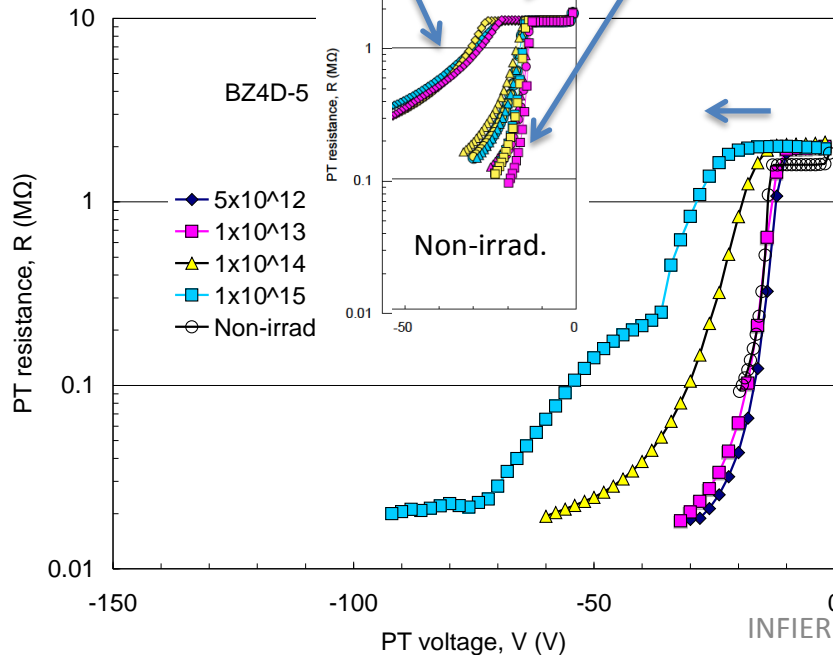
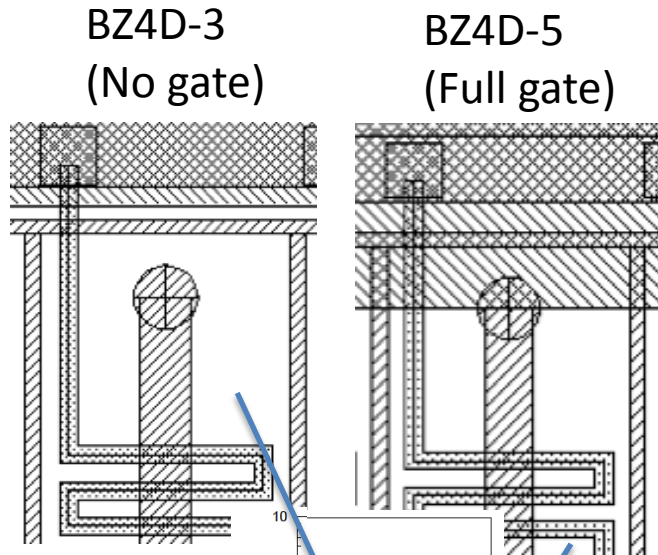


Signal \sim half of the carriers.

- High field around the strips
- Weighting field, mobility

n-bulk:
Low field toward n^+ strip

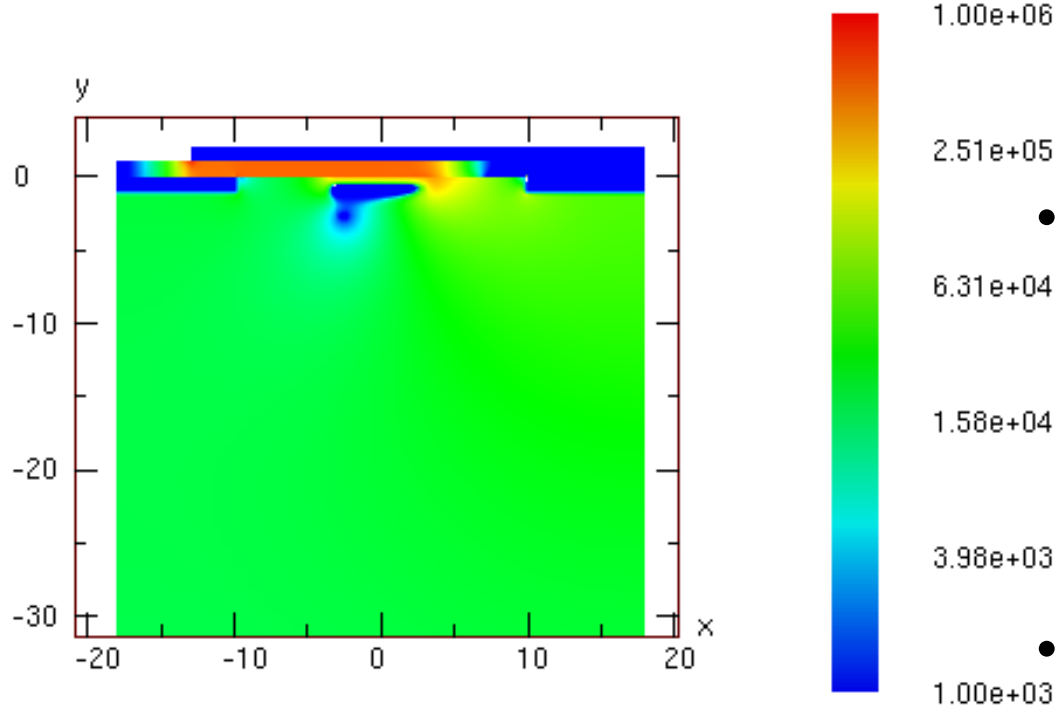
Punch-Through Protection (PTP) Structure



- “Full gate” induced PTP onset in lower voltages than “No gate”.
- Onset voltage went down first and then started to increase.
 - What causes the transitions?

PTP Simulations

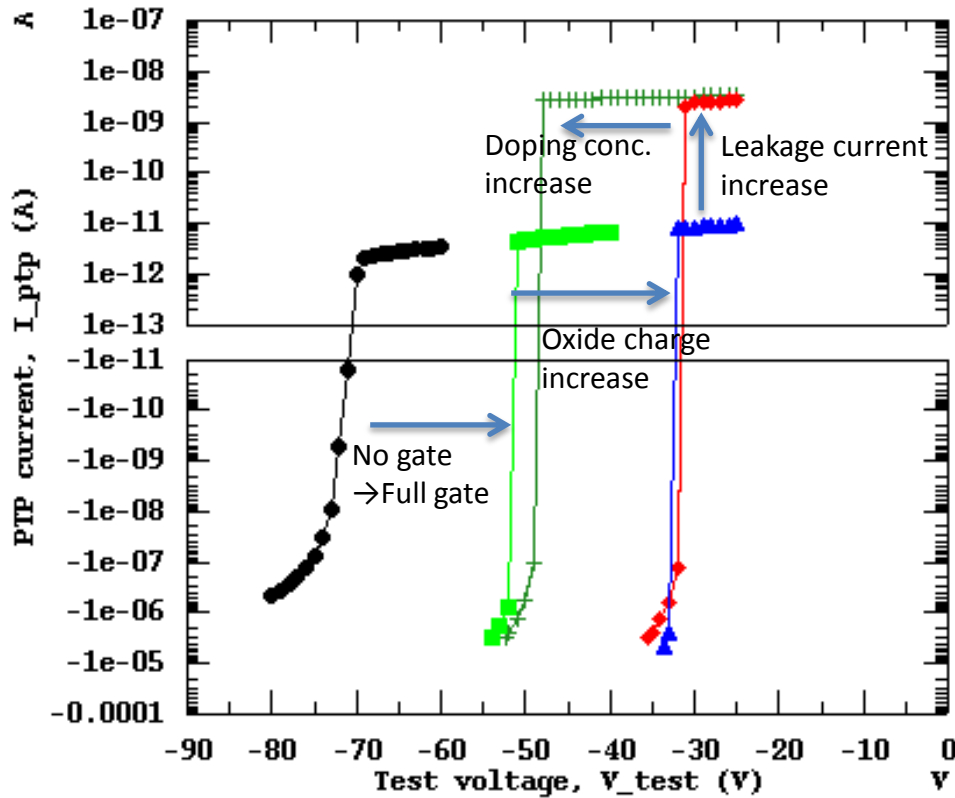
Electric field, NB*HT*HC -50V



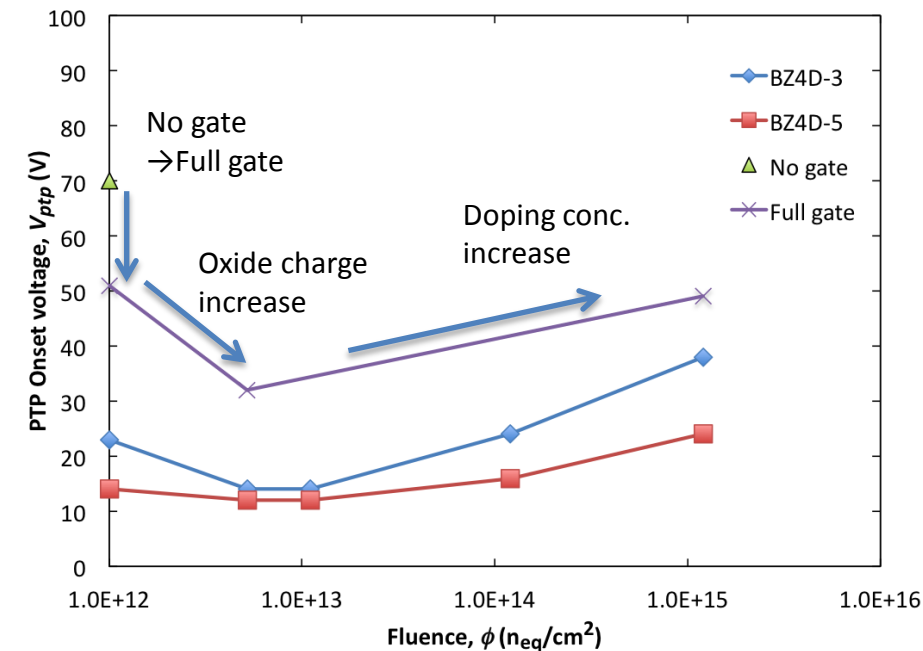
TCAD simulation of
“Full gate” PTP, irradiated
Electric field at onset
when the backplane bias voltage at -200 V
 V_{test} (left implant) at -50 V

- TCAD
 - no bias resistor in parallel
 - NPTP: “No gate”
 - Others: “Full gate”
- Parameters:
 - NB/DB: non/damaged bulk
 - LT/HT: lo/hi interface charge
 - LC/HC: lo/hi current
 - Non irradi: NB*LT*LC
 - Irrad: DB*HT*HC
- Irrad. simulation
 - Damaged bulk,
 - hi interface charge,
 - hi leakage current

PTP Simulations



- The fluence dependence can be understood as the effect of
 - Build-up of the Interface charge and
 - Increase of acceptor-like levels.
- The systematic “offset”
 - difference between the 2D simulation and the 3D real.



- Onset voltage decreased as
 - No gate (black) → Full gate (colored)
 - Interface charge increased
- Increased as
 - acceptor-like state increased