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Track Selection Performance of the **CMS Cathode Strip Chamber Track Finder and Level 1 Trigger**

Kristin Beck

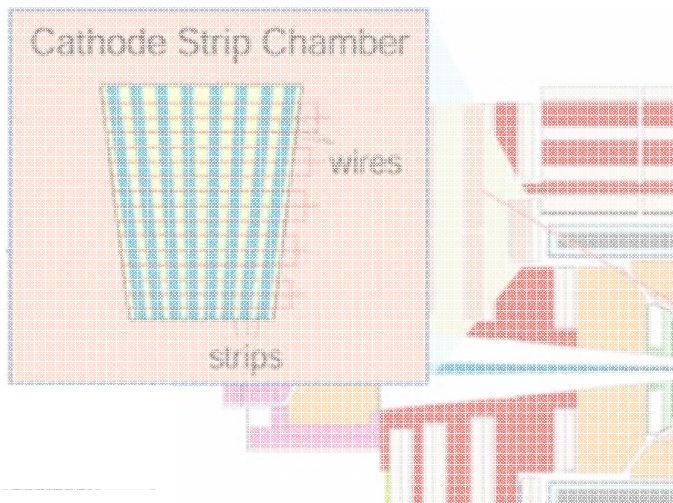
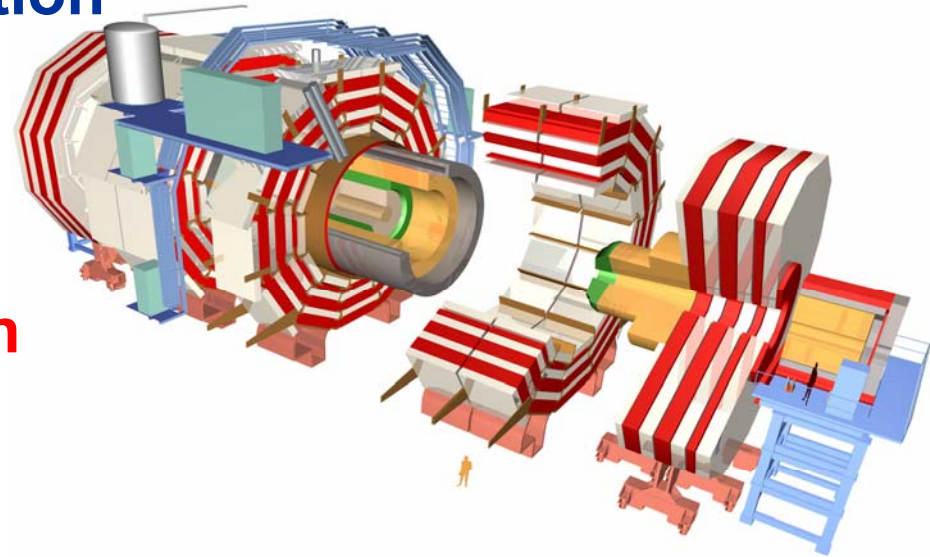
Advisors: Dan Holmes, Darin Acosta

14 August, 2008



Cathode Strip Chambers

- **One of three muon detection systems in CMS = Compact Muon Solenoid**
- **Located in the endcaps**
- **Responsible for muons in $0.9 \leq \eta \leq 2.5$**

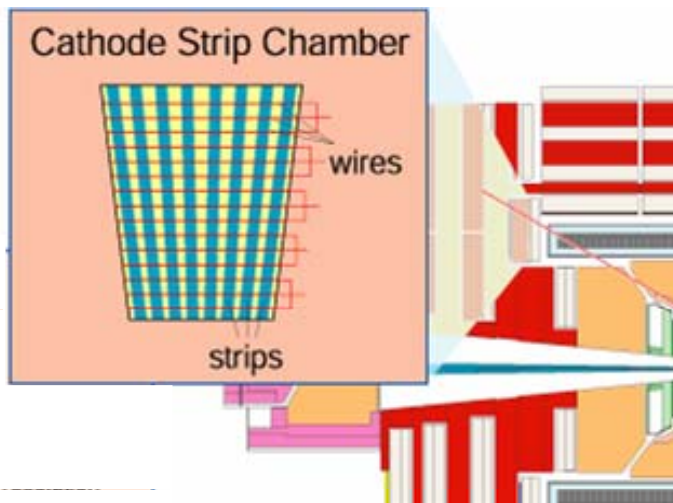
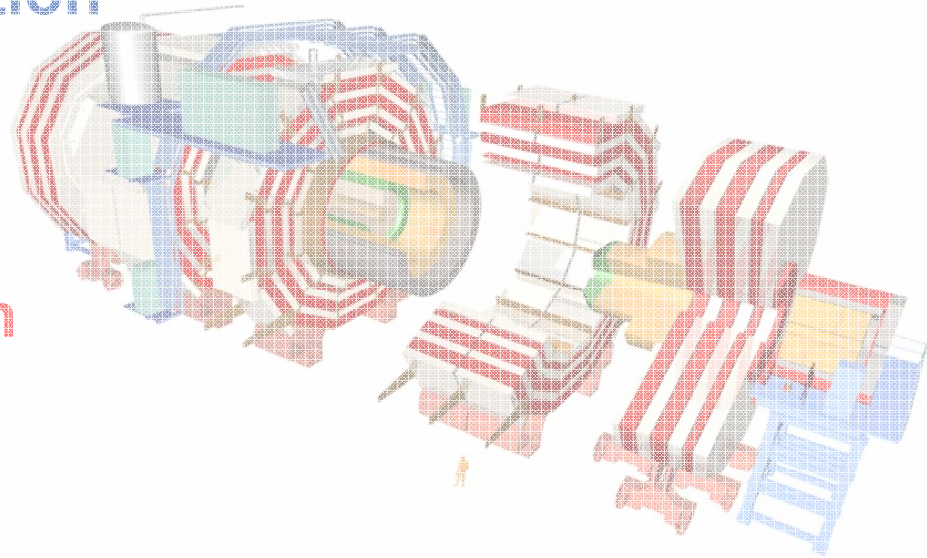


- **CSCs consist of wires and strips arranged in a grid pattern**
- **Muons produce LCTs = Local Charged Tracks**
- **Track Finder uses multiple LCTs to reconstruct a muon's path**



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Level 1 Trigger

the problem

- At full luminosity, expect 40MHz from CMS detector
- Can only store ~200Hz

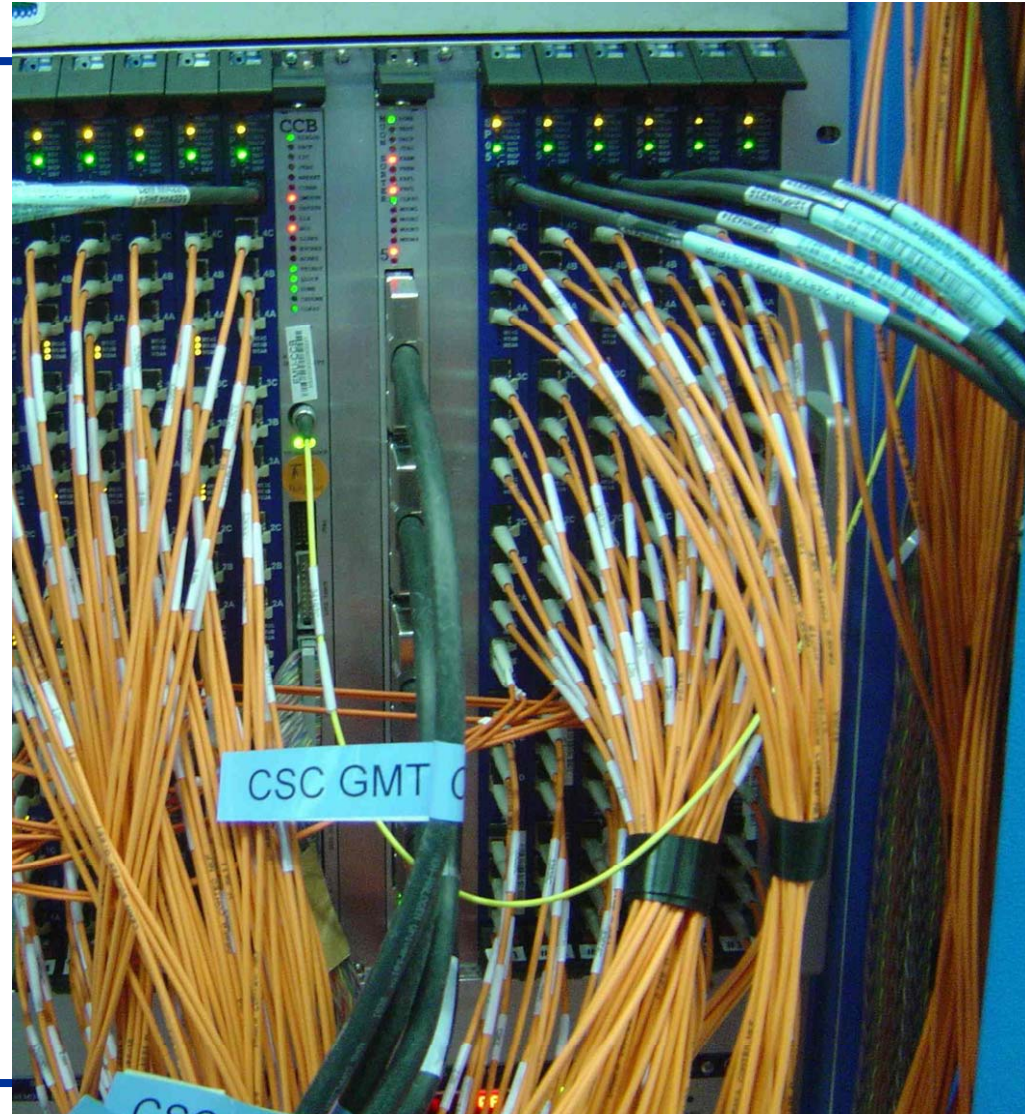
- Select data to store before saving
- Two stages : Level 1 & HLT = High Level Trigger
- L1 is custom electronics that reduce data rate to 100kHz
- HLT consists of commercial processors

CMS's solution



CSC Track Finder/Level 1 Trigger

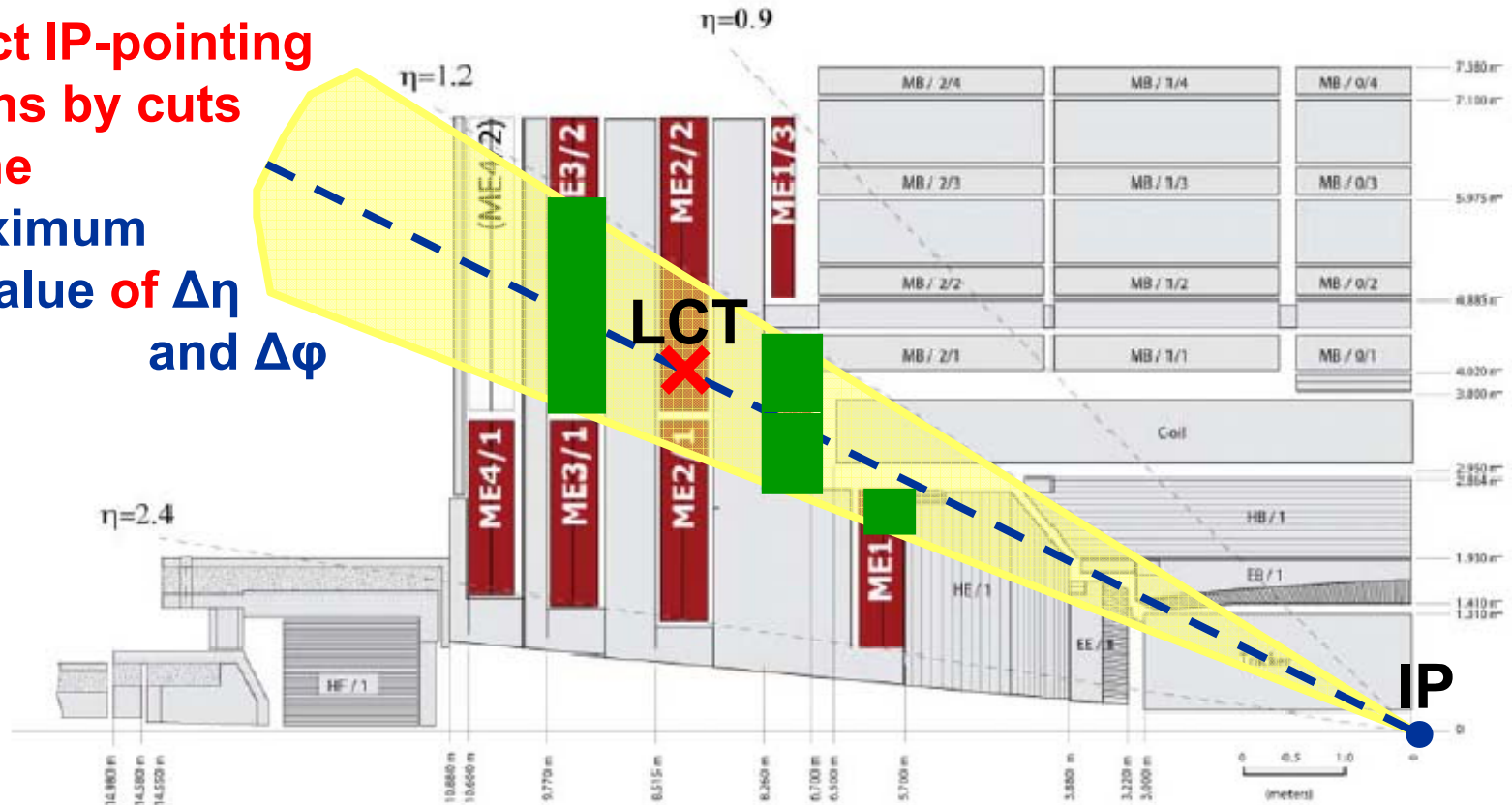
- **CSC L1 Trigger selects up to 4 muons per bunch crossing (25ns) to pass on to GMT = Global Muon Trigger**
- **Extrapolations by track mode**
- **Input parameters include η , φ , $\Delta\eta$ and $\Delta\varphi$**
- **Output to GMT is the quality and P_T of track candidates**
- **Highly configurable, FPGA-based trigger**





Identifying Tracks

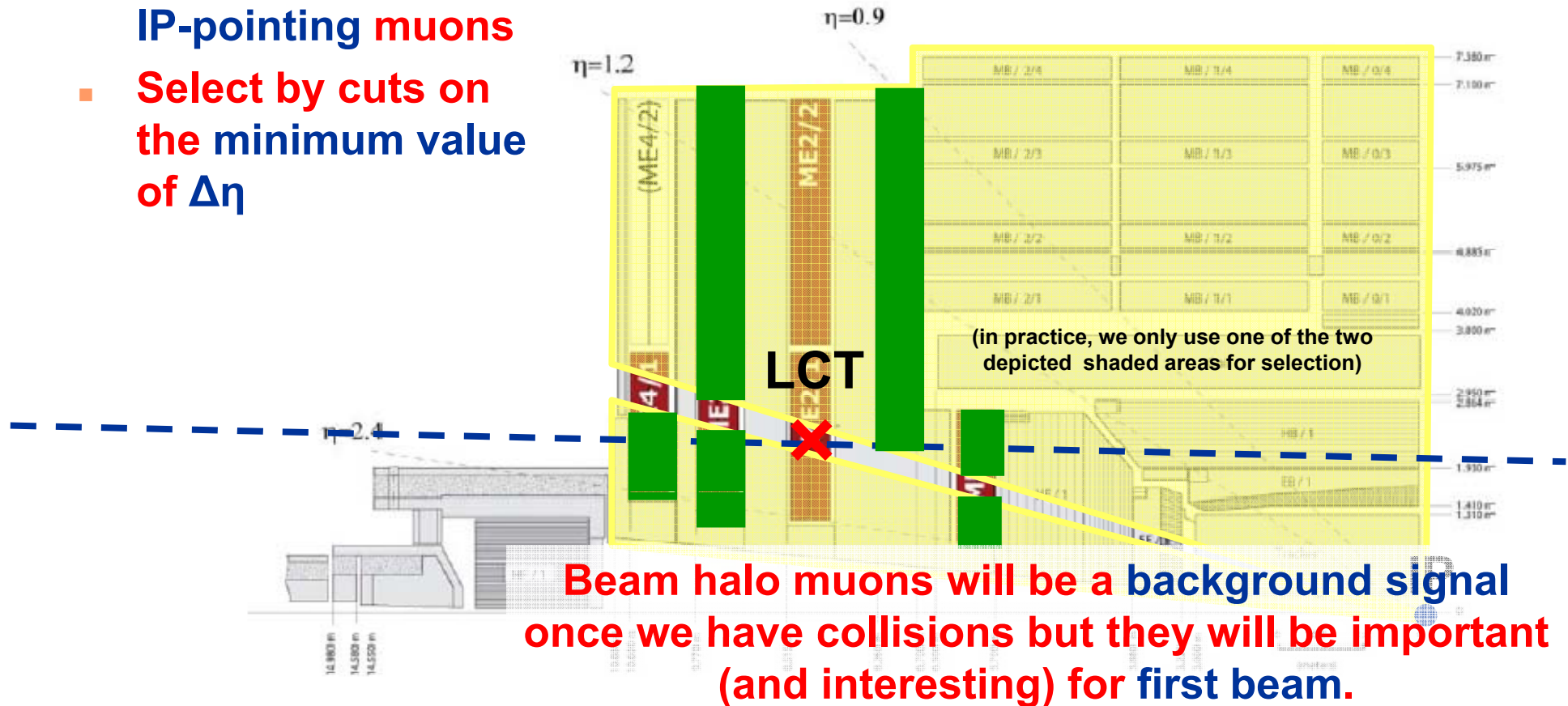
- Muons coming from new physics originate near the IP = Interaction Point
- Expect \sim constant η , ϕ
- Select IP-pointing muons by cuts on the maximum value of $\Delta\eta$ and $\Delta\phi$





Identifying Tracks

- **Beam Halo Muons are produced beam+pipe and beam+gas interactions**
- **Expect non IP-pointing muons**
- **Select by cuts on the minimum value of $\Delta\eta$**



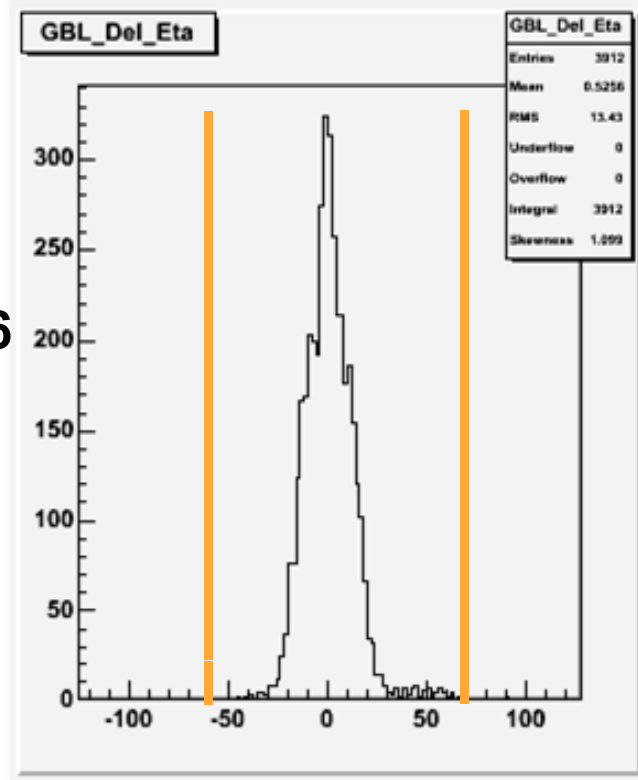


Track Selection Performance: $\Delta\eta$

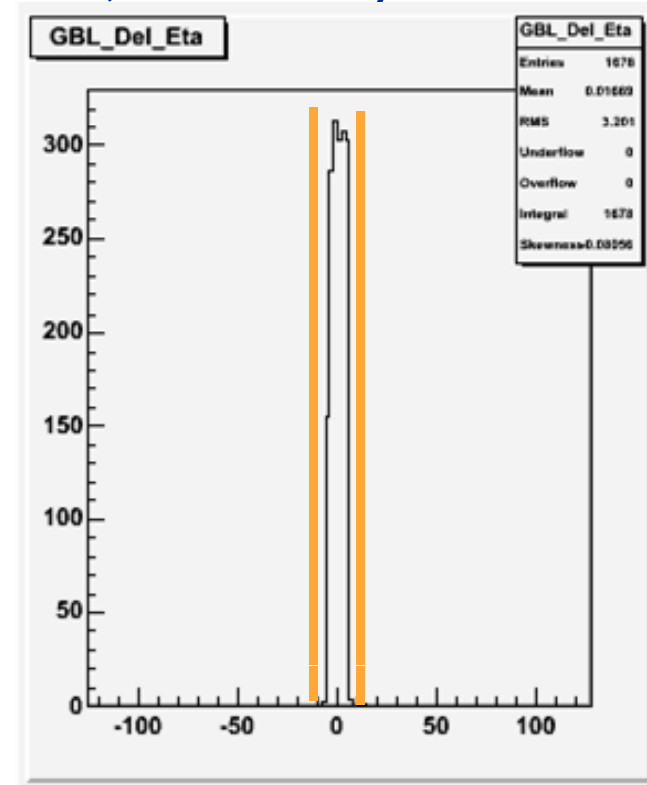
■ $\Delta\eta$ Cut Confirmation (...maybe)

- ◆ Two runs from CRUZET2, one with **no restriction on $\Delta\eta$** and one with **$\Delta\eta$ set to $0x5$ ($\Delta\eta < 0.25$, or $\theta < 5^\circ$)**

Max: 97
Min: -96



46788



46794

Max: 13
Min: -8

- ◆ What about the tracks with **$|\Delta\eta| > 0x5$** ? (**$< 1\%$** of all tracks)



Tracking down that last 1%

Are the track mode assignments self consistent?

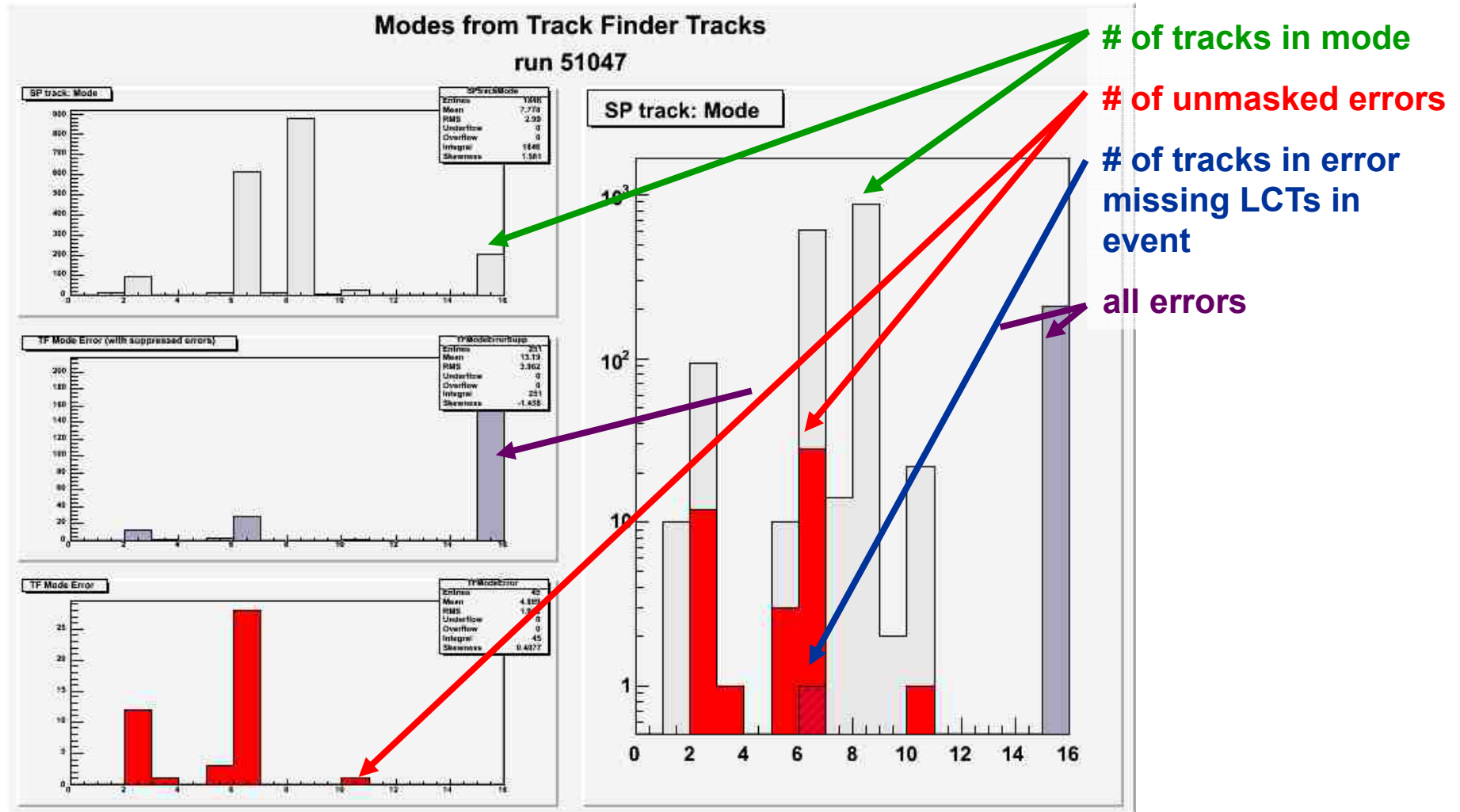
- **Complications**

- ◆ Recent **firmware update** not reflected in software
- ◆ Undocumented **special cases** for track mode assignment
- ◆ Unrecorded **“out of time”** LCTs

... yes (mostly)

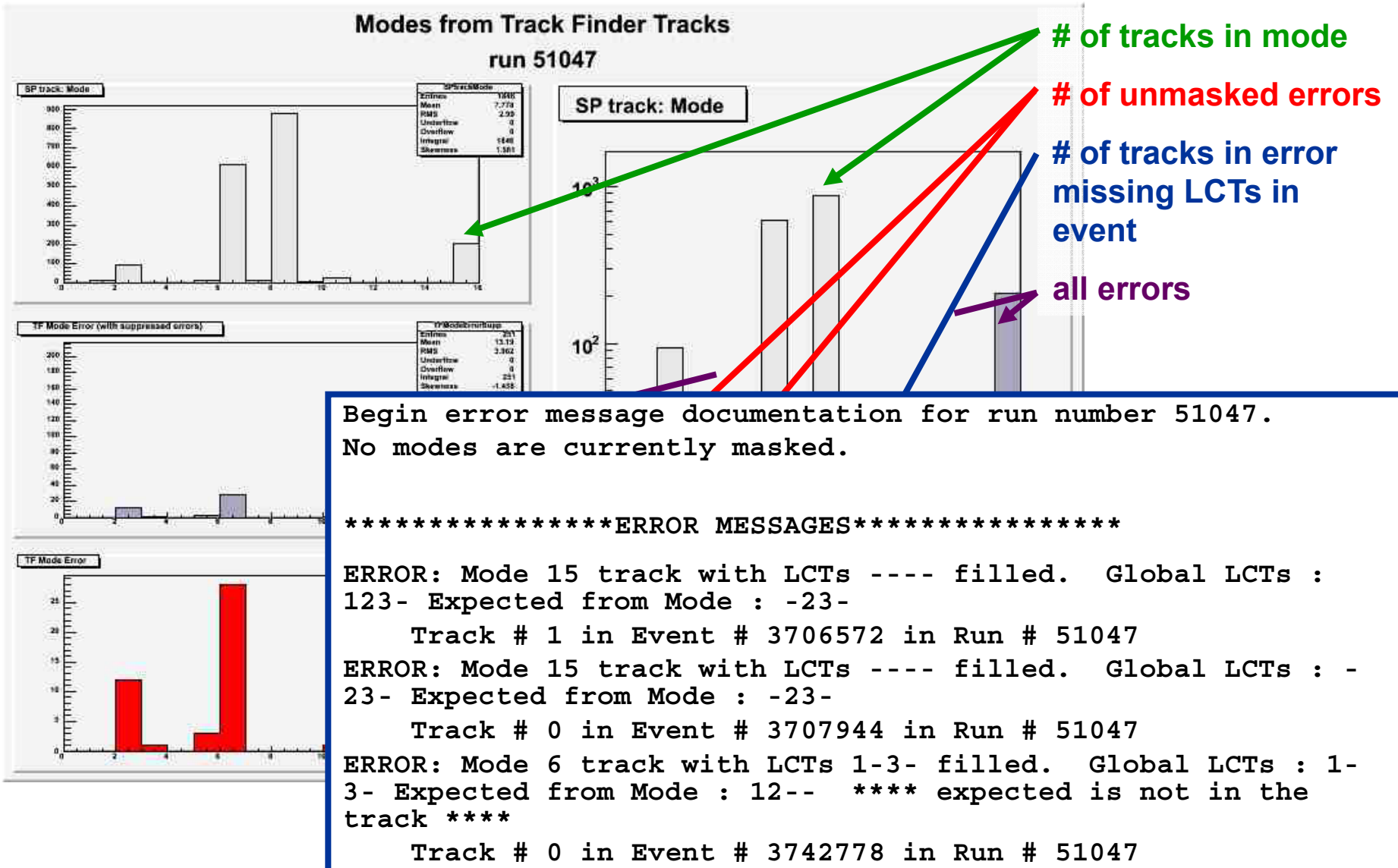


Mode Analysis Output (graphical)





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... **yes (mostly)**

- **Are we looking at $\Delta\eta$ between the wrong LCTs?**

- **In run 46794, tracks with $|\Delta\eta|>0x5$ are in mode 6 and mode 8**
- **These modes only contain 2 LCTs**

... **no**

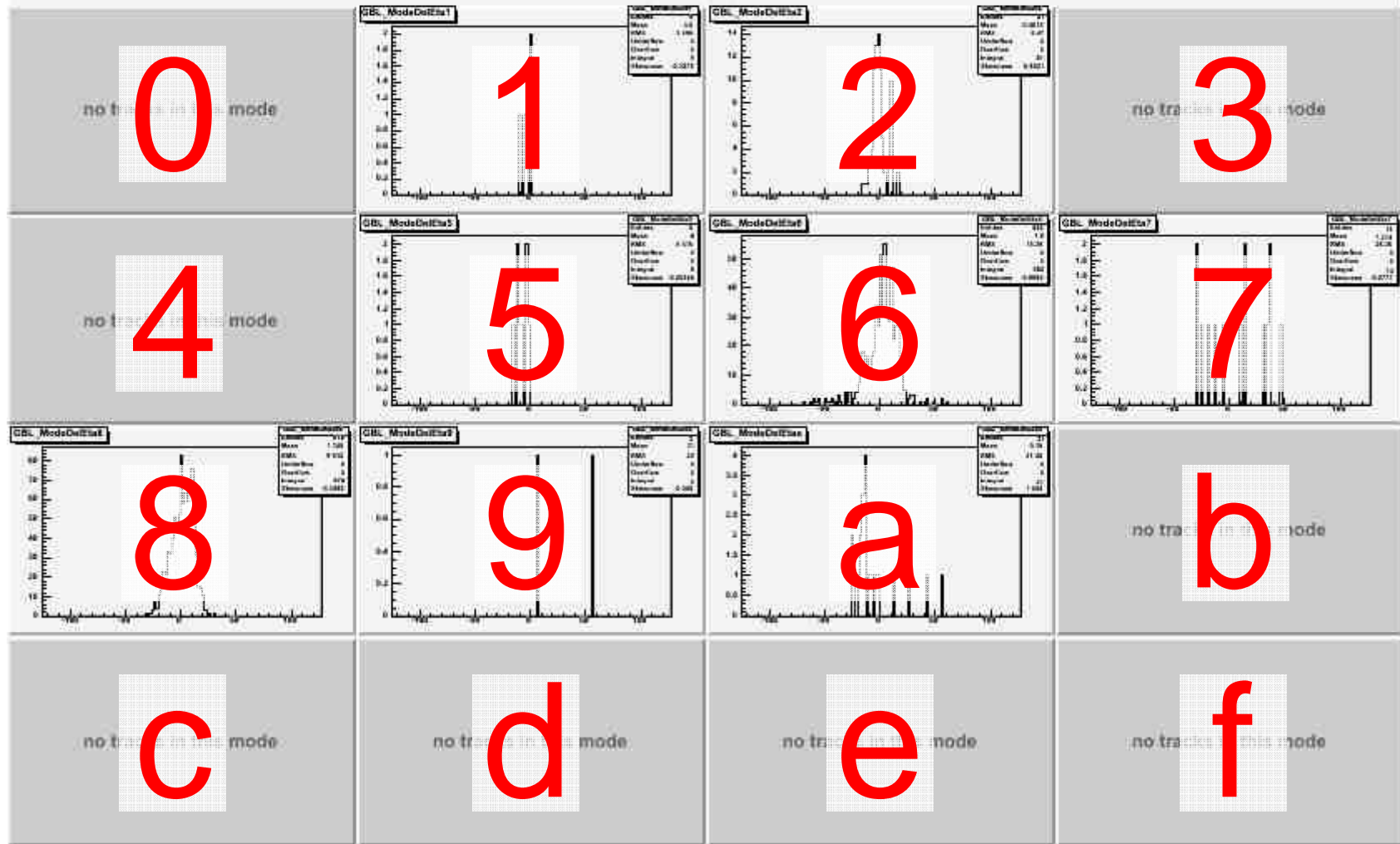


$\Delta\eta$ Output (graphical)

Mode #

Delta Eta from Track LCTs separated by Mode

run 51047





Conclusions

- **Basic $\Delta\eta$ cut works (to first order)**
- **Further study needed**
 - ◆ What are the **outstanding issues** in the $\Delta\eta$ cut?
 - ◆ Is the current error level **manageable**?
 - ◆ How is track finder performance with **mode-specific cuts**?
 - ◆ What about **cuts on other parameters**?
- **Deliverables**
 - ◆ Analysis package that computes $\Delta\eta$ and $\Delta\phi$ and checks LCT agreement with **assigned track mode**

Documentation: <http://cern.ch/csctf/studies/studies.html>

Technical Presentation: <http://indico.cern.ch/conferenceDisplay.py?confId=39150>



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Questions?

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