BPS amplifier for TBL

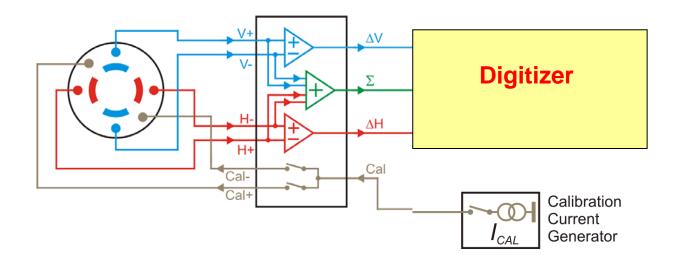
Gabriel Montoro*, Antoni Gelonch, Yuri Koubychine Universitat Politècnica de Catalunya (UPC)

*E-mail: montoro@tsc.upc.edu



General scheme of the analog head electronics

IFIC UPC LAPP

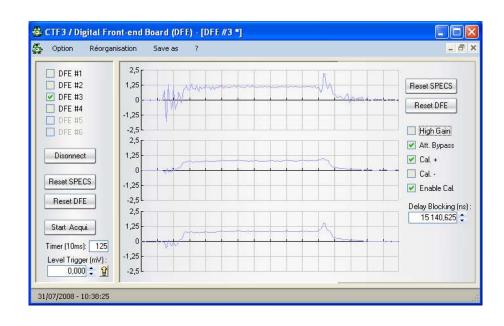




It's been installed the first prototype of the BPS +amplifier



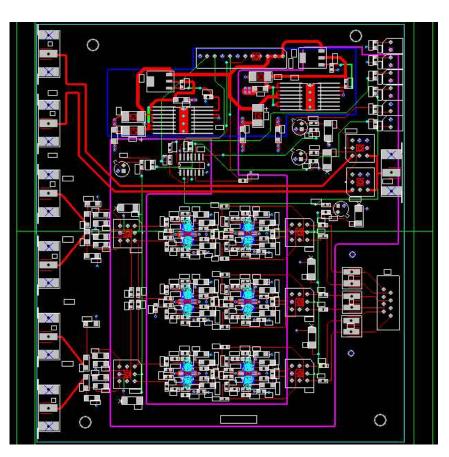
First amplifier prototype (as installed last July)



BPS+amplifier+digitizer test: calibration response



Completed the PCB routing of the second prototype version of the amplifier, incorporating some <u>improvements</u>...



- -... it's been changed the position of the connectors in order to eliminate the need of internal cables for conexion
- ... it's been changed the width of some tracks for a better achievement of 50 ohms matching
- ... and it's been solved a lot of minor mistakes

Next work to do ...

... to send to the PCB manufacturer the second amplifier prototype

.... to build and test it (on October)



Thanks!

Gabriel Montoro montoro@tsc.upc.edu

