

# CTF3 Collaboration Technical Meeting TAIL-CLIPPER KICKER

M.J. Barnes

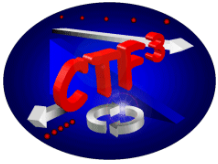
CERN TE/ABT

Also representing:

Tony Fowler<sup>1</sup>, Gianfranco Ravida<sup>1</sup>,  
Iker Rodríguez<sup>2</sup> & Fernando Toral<sup>2</sup>

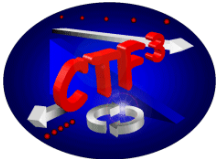
<sup>1</sup> ⇨ CERN

<sup>2</sup> ⇨ CIEMAT

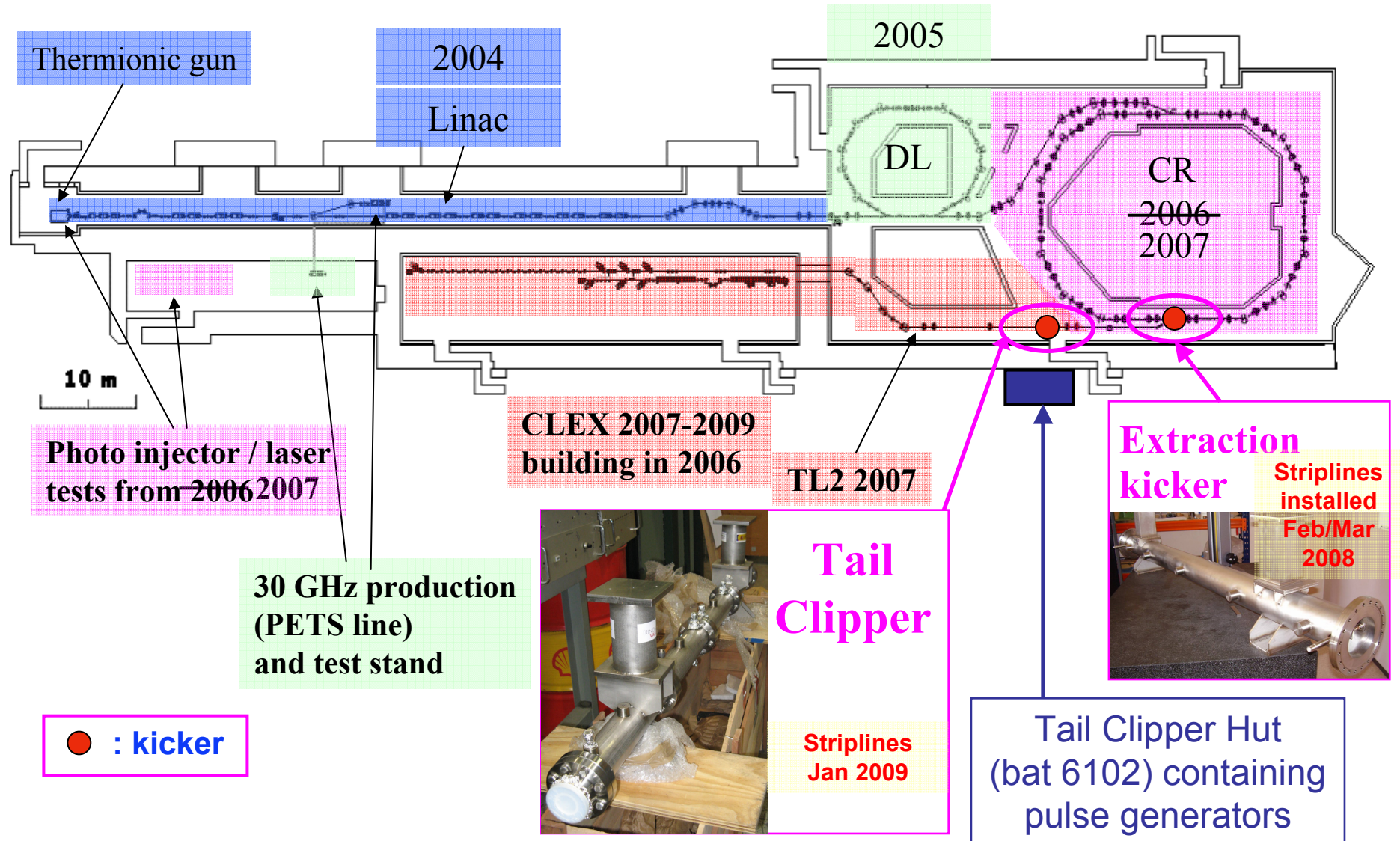


# OVERVIEW

- Review of design and status of tail-clipper  
– several measurements presented;
- Outstanding work for tail-clipper;
- Future kicker systems that would be required for CLIC.



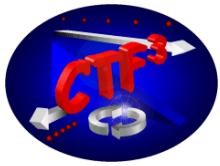
# CTF3 Programme



M.J. Barnes

January 29, 2009

3

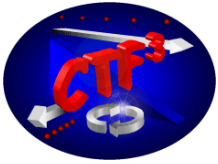


# CTF3 & CLIC Stripline Kickers

Generator 2007; Striplines 02/2008

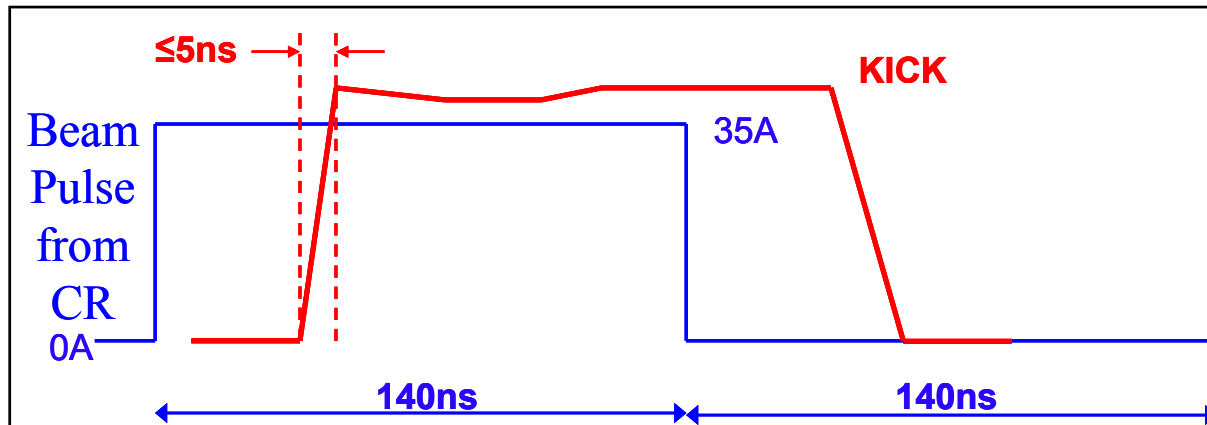
Jan. 2009

		CTF3 CR Extraction	Tail Clipper	
Beam energy		300	200	MeV
Total kick deflection angle ("B" & "E" Fields)		7	1.2	mrad
Stripline plate separation		40	40	mm
Stripline electrical length		1.7	4 x ~0.295	m
Available length (including transitions)			1.625	m
Field rise time (0.25% to 99.75%)		≤70	≤5 !	ns
Pulse duration		200	Up to 140	ns
Flat-top reproducibility		± 0.1	NA	%
Flat-top stability (including droop)		± 0.25	NA	%
Field homogeneity			± 18	%
Repetition rate	Initial	5	5	Hz
	Nominal	50	50	Hz
Stripline Pulse voltage		±12.6	±2.0 for 1.18m ±2.65 for 0.885m	kV
Pulse current (into 50 Ω load)		±252	±40 for 1.18m ±53 for 0.885m	A
Timing Jitter			≤ 1 rms	ns



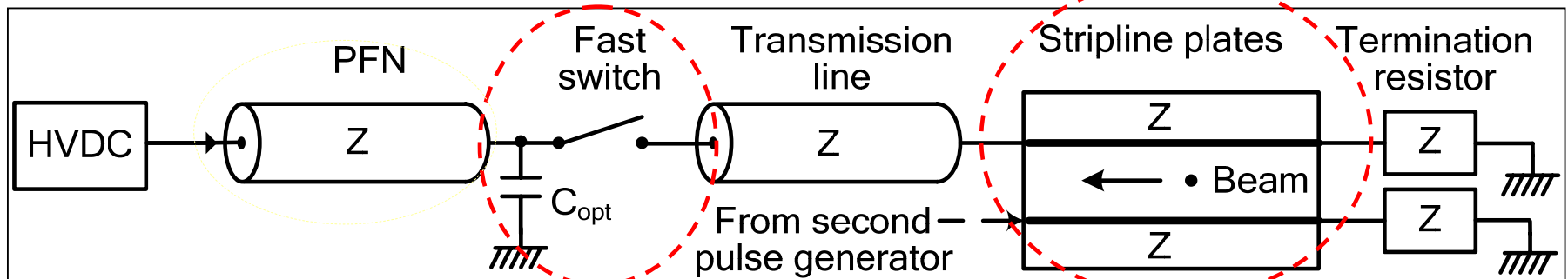
# Tail Clipper: Overview

## Beam Pulse

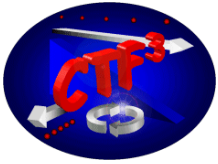


The beam pulse extracted from the CR is 35 A and 140 ns. The tail-clipper must have a fast field rise-time, of 5 ns or less, to minimize uncontrolled beam loss. The flatness of the kick pulse is not important as deflected beam is to be thrown away.

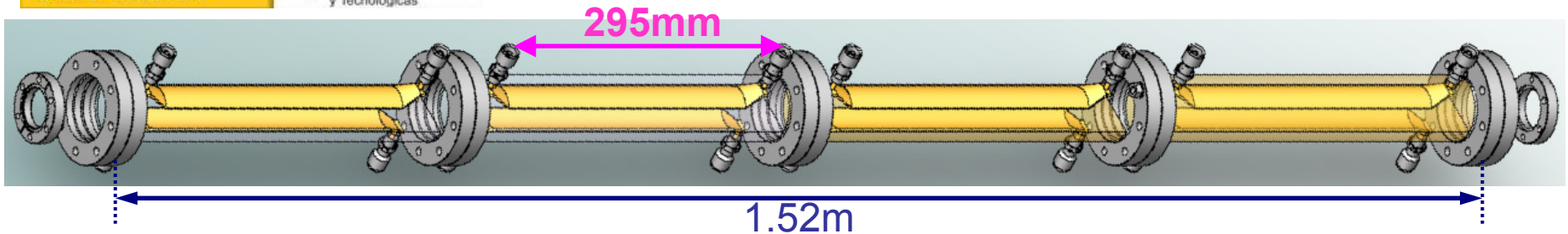
## Schematic of Tail-Clipper



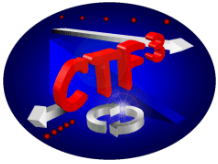
Each pulse generator is composed of a  $50\Omega$  ( $Z$ ) Pulse Forming Network (PFN), a fast semiconductor (Behlke) switch,  $50\Omega$  stripline plates and a matched terminating resistor.



# Tail-Clipper Striplines (1)



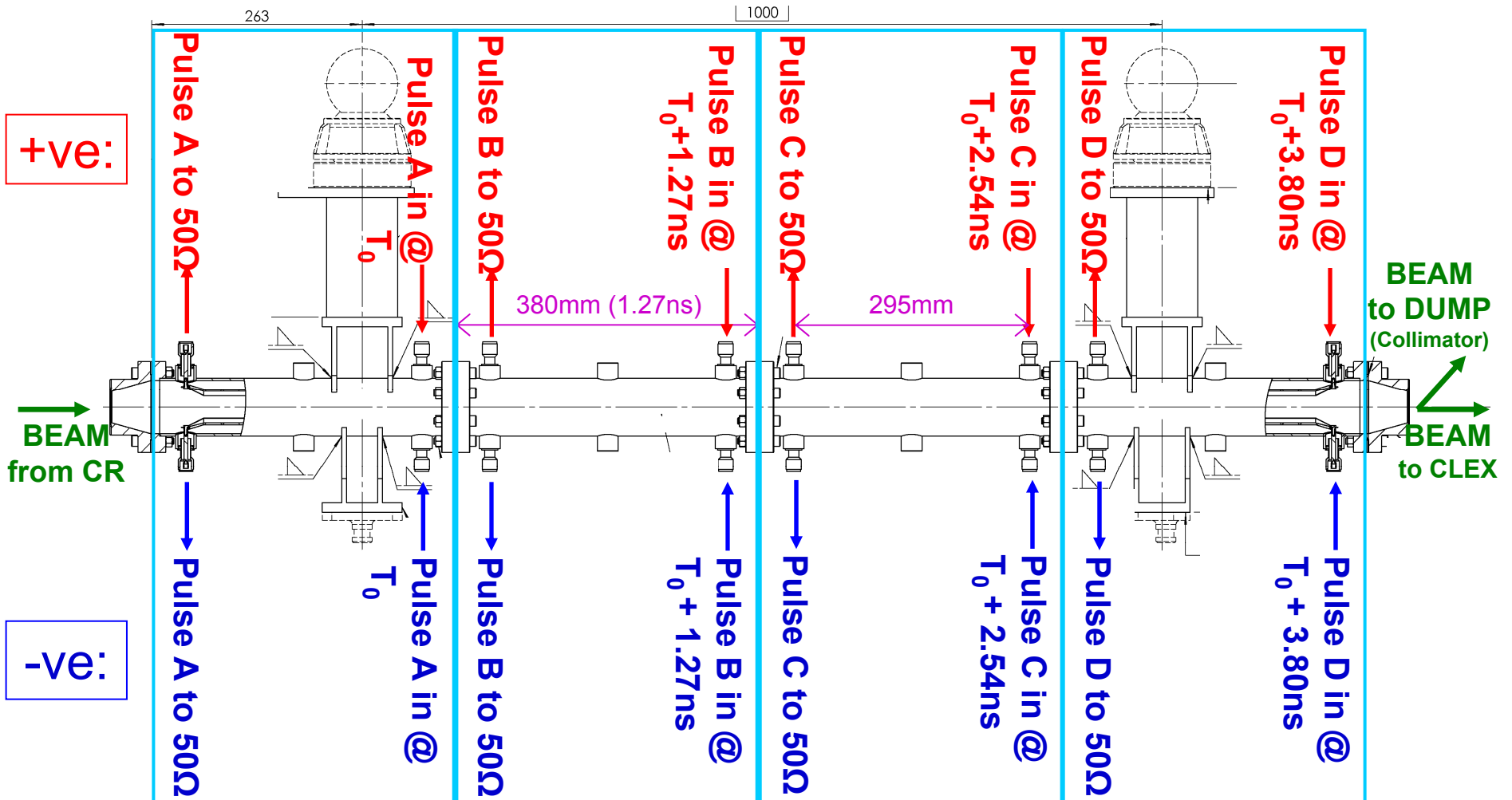
- Four sets of striplines.
- Smooth transitions from coaxial feedthru to stripline.
- Tapered electrodes preserving  $50\Omega$  characteristic impedance to minimize reflections.
- At a radius of 10mm the deflection is up to 4.3% less than the on-axis deflection.
- For an overall electrical length of 0.885m ( $3 \times 0.295\text{m}$ ), a current of  $\sim 53\text{A}$  into  $50\Omega$  provides the specified kick of 1.2 mrad.
- A 4th set of striplines provides redundancy.
- “Over-driving” striplines also reduces field rise-time.

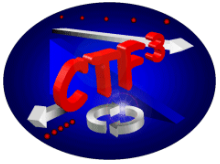


# Tail-Clipper Striplines (2)



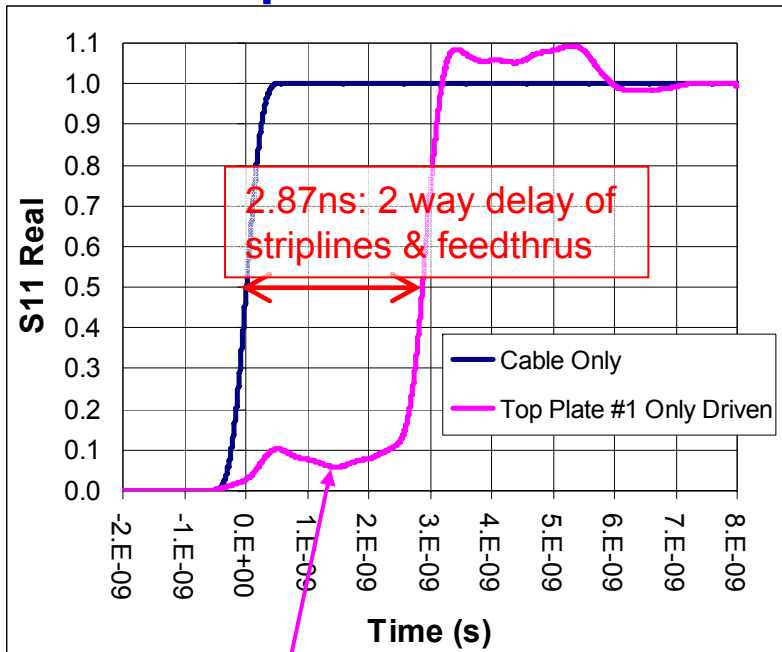
1.2mrad deflection. Striplines fed from CLEX end.  
Pulse voltage:  $\pm 2.0\text{kV}$  for 1.18m; or  $\pm 2.65\text{kV}$  for 0.885m



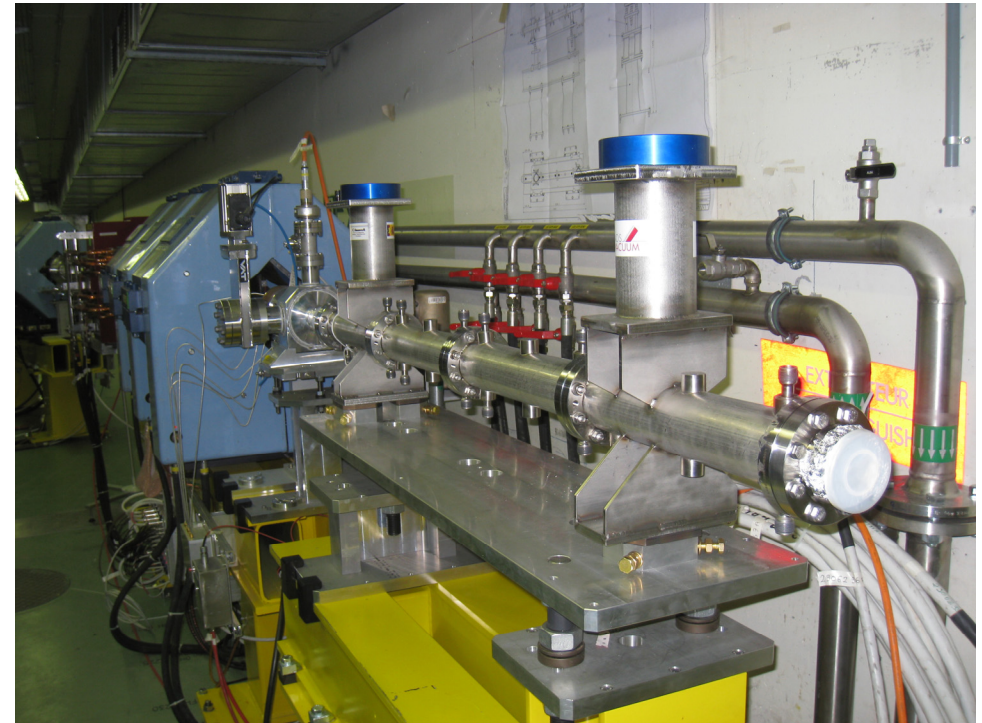


# Tail-Clipper Striplines (3)

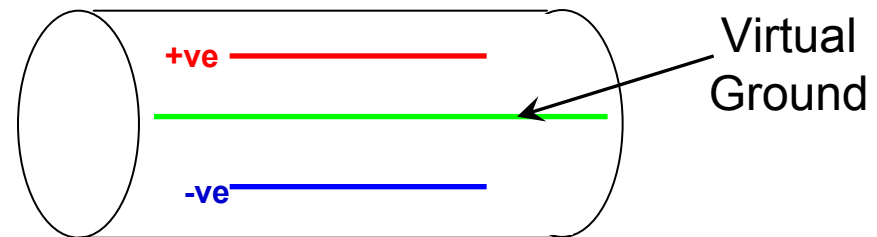
Measured pulse response with only one plate of a set of striplines driven:



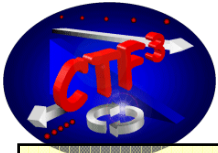
Mismatch ( $S_{11}=0.06$ ):  $Z \approx 56\Omega$ . Impedance of  $56\Omega$  is due to only one-plate being driven (confirmed from simulations). To do: Double-check using a hybrid.



Striplines in situ in TL2

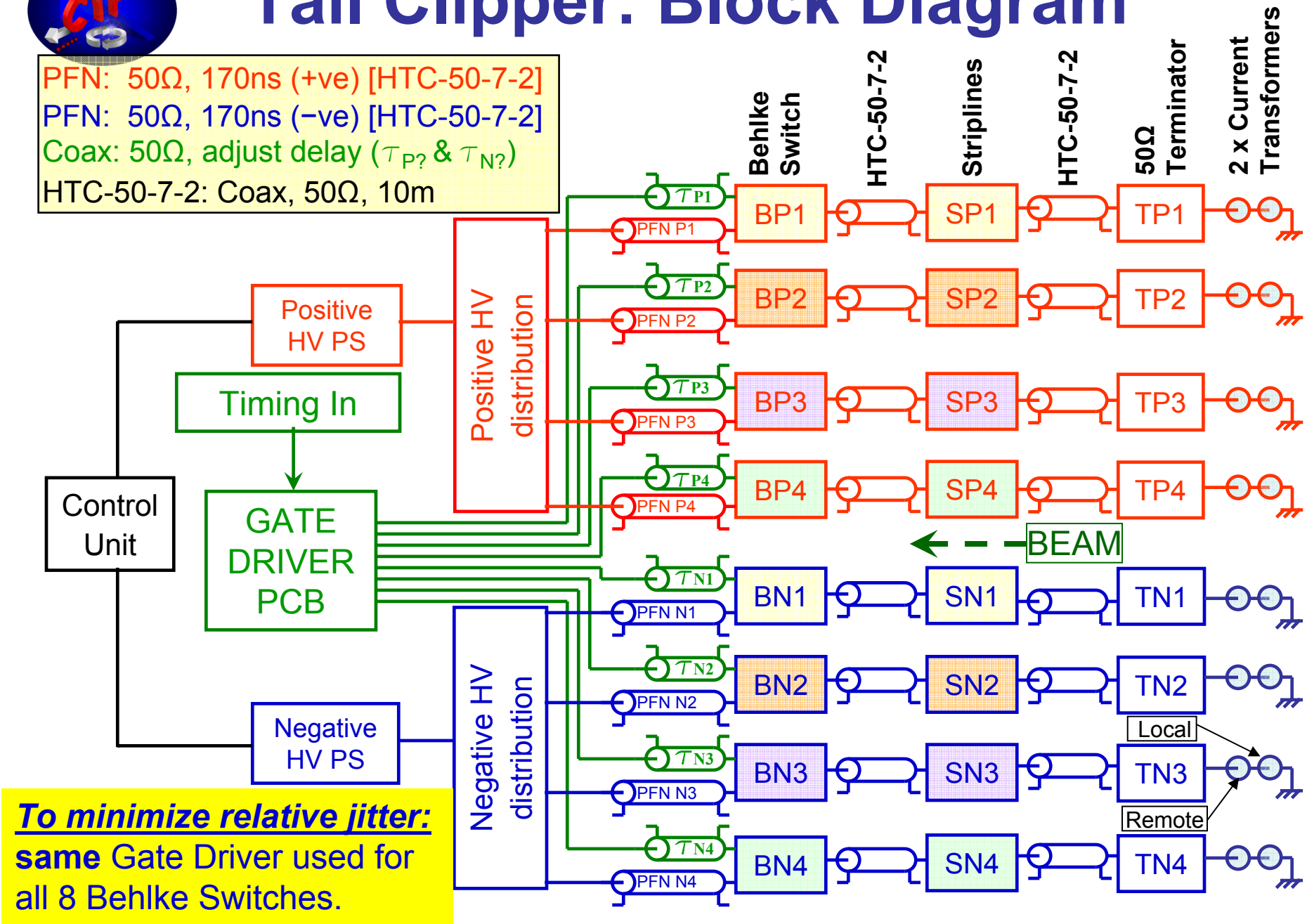




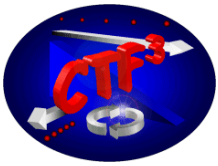


# Tail Clipper: Block Diagram

PFN: 50Ω, 170ns (+ve) [HTC-50-7-2]  
 PFN: 50Ω, 170ns (-ve) [HTC-50-7-2]  
 Coax: 50Ω, adjust delay ( $T_{P?}$  &  $T_{N?}$ )  
 HTC-50-7-2: Coax, 50Ω, 10m



**To minimize relative jitter: same Gate Driver used for all 8 Behlke Switches.**

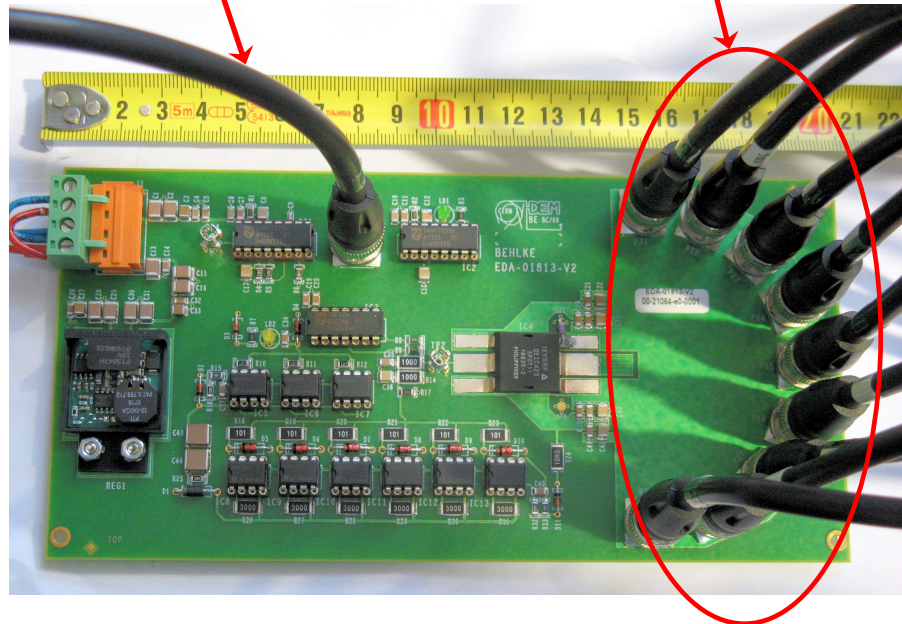


# Tail-Clipper Hardware

10V Trigger from Gate Driver PCB

Input Trigger (5V)

9 Parallel 50Ω Outputs



Gate Driver PCB

(9 Parallel 50Ω Outputs  
[8 Behlke Switches])

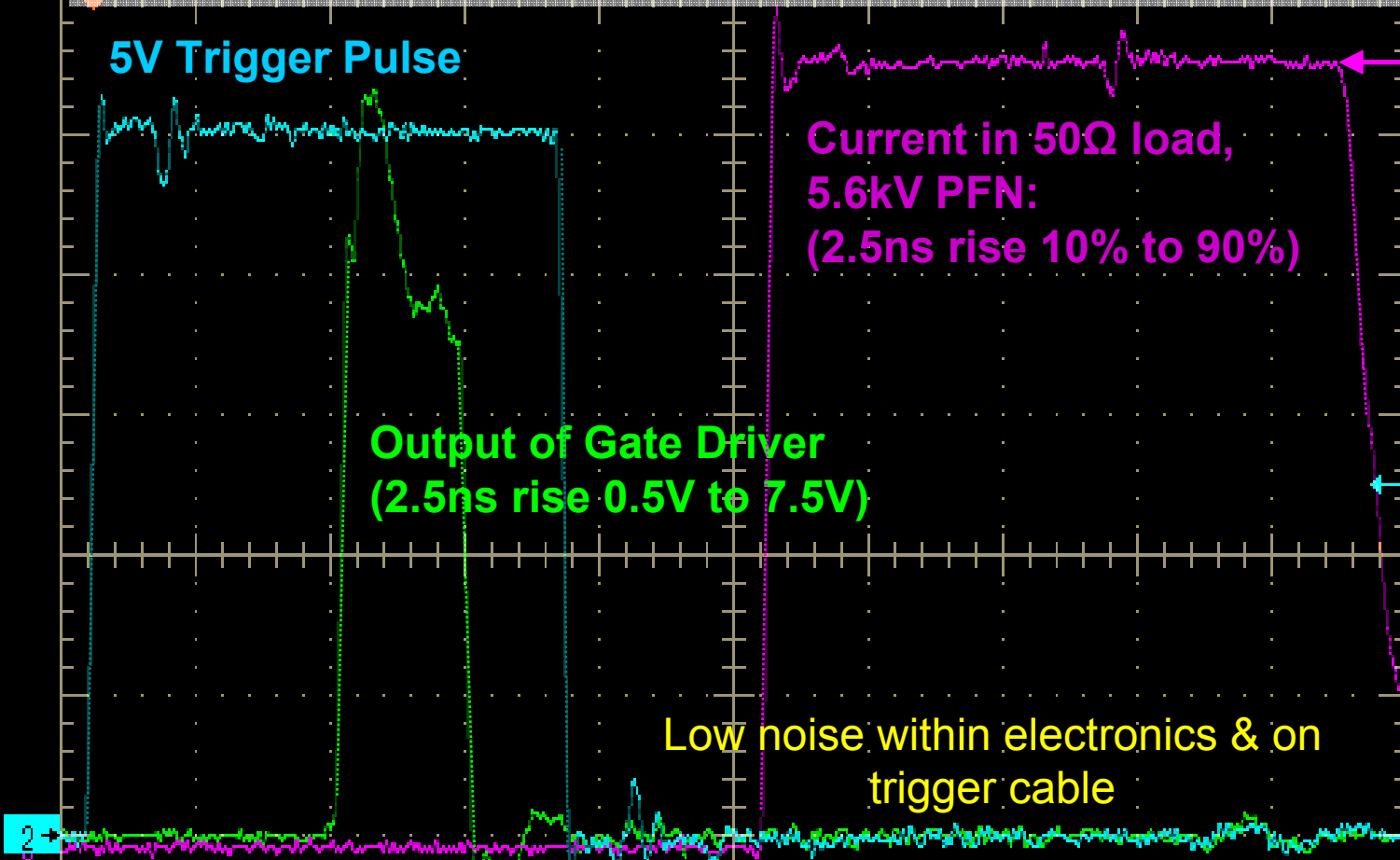
Previous measurements on Behlke switches show need for fast rising (few ns) trigger pulse  $\geq 7V$ .

Peaking Capacitor



Behlke Switch: Very Low Inductance Connections

# Meas. Waveforms: Normal Op.



56A  
 (53A [40A] reqd. for 3 [4] sets of striplines).  
 Field rise-time of ~4.0ns [~3.2ns] predicted using PSpice (with  $\tau_d$  of 1.27ns), 0.25% to 99.75%, with measured current waveform, for 56A.

Current in 50Ω load, 5.6kV PFN:  
 (2.5ns rise 10% to 90%)

Output of Gate Driver  
 (2.5ns rise 0.5V to 7.5V)

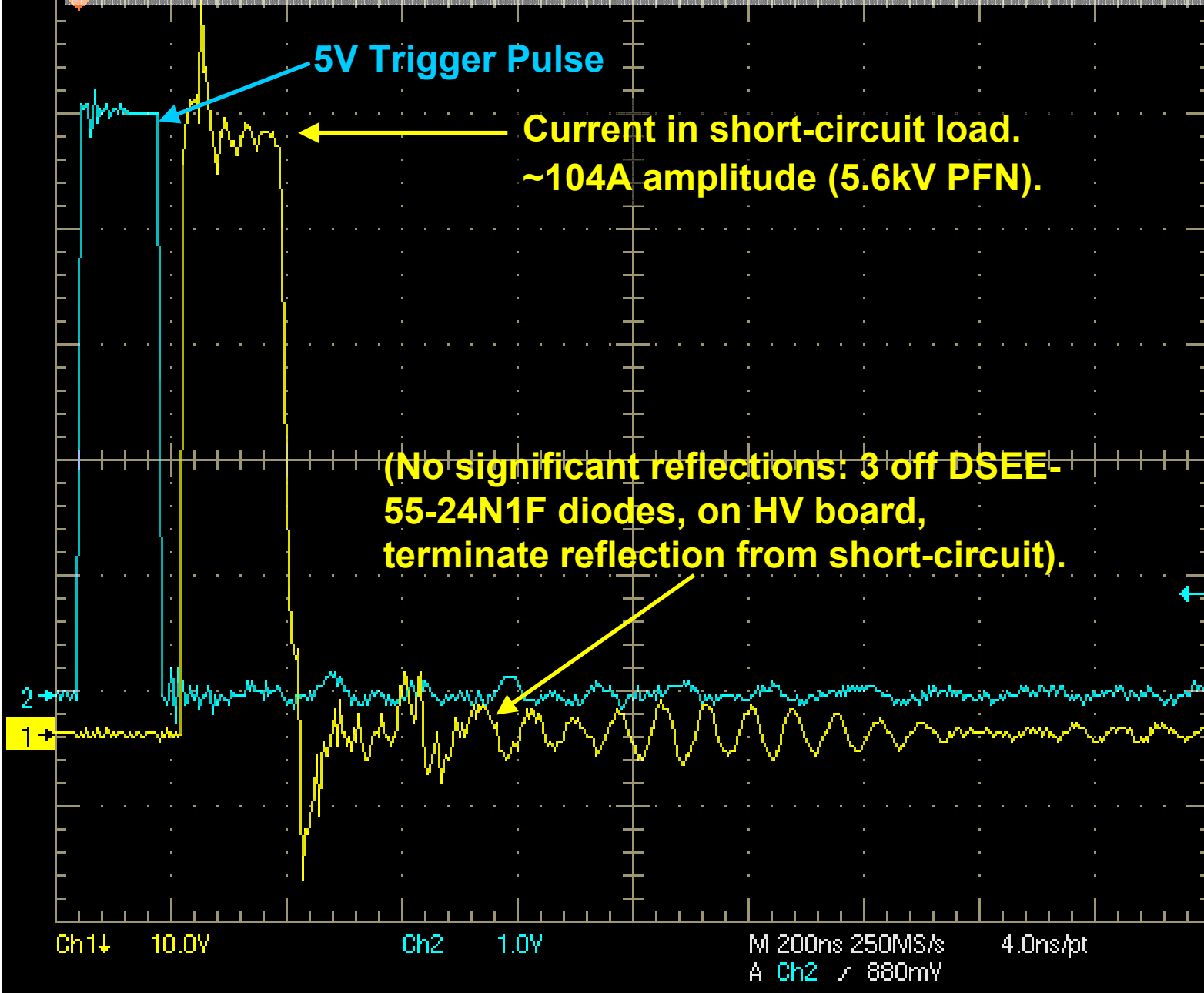
Low noise within electronics & on trigger cable

Delay(C2,C4)	74.39ns	μ: 74.409544n	m: 74.34n	M: 74.48n	σ: 24.67p	n: 642.0
Rise(C2)	2.637ns	μ: 2.6609251n	m: 2.467n	M: 2.799n	σ: 49.35p	n: 642.0
Rise(C3)	2.488ns	μ: 2.4903961n	m: 2.369n	M: 2.577n	σ: 29.24p	n: 643.0
Ampl(C3)	28.0V	μ: 27.999688	m: 27.9	M: 28.0	σ: 5.573m	n: 643.0
Delay(C2,C3)	201.0ns	μ: 201.11838n	m: 201.0n	M: 201.2n	σ: 36.5p	n: 642.0
Rise(C4)	2.481ns	μ: 2.4875705n	m: 2.353n	M: 2.585n	σ: 41.45p	n: 643.0

$$T_{field} \approx T_r + \left( \frac{0.38}{c} \right)$$

# Meas. Waveforms: S.Cct Load

Buttons



Ch1 Position

-2.38div

Ch1 Scale

10.0V

Pos Wid(C1)! 170.3ns

$\mu$ : 169.74712n

m: 169.2n M: 170.3n

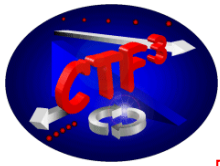
$\sigma$ : 799.9p n: 2.0

Ampl(C1)! 52.0V

$\mu$ : 52.199999

m: 52.0 M: 52.4

$\sigma$ : 282.8m n: 2.0

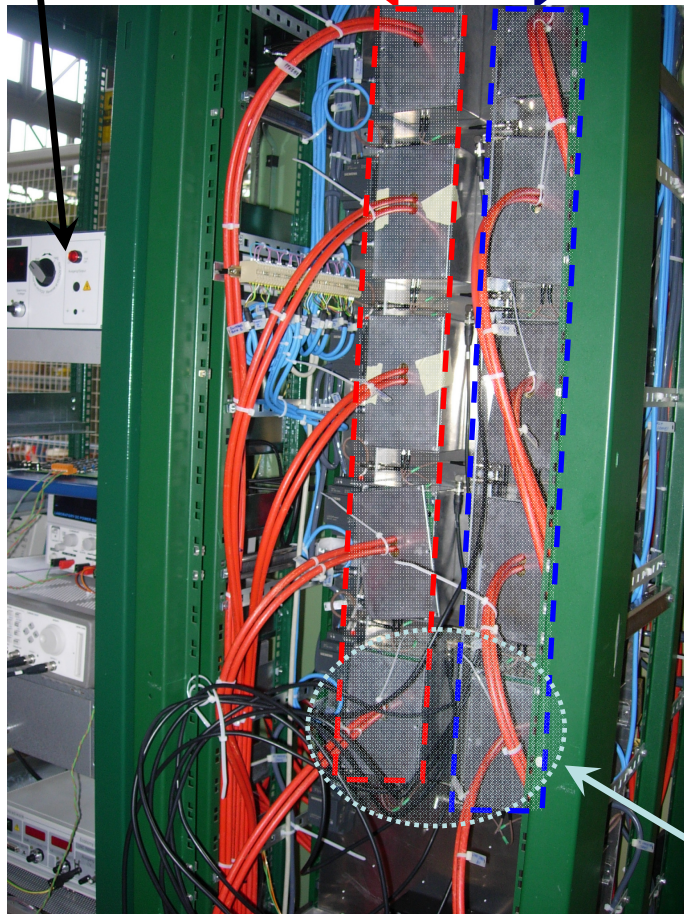


# Kicker Cabinet During Assembly

5 x Positive  
Polarity  
Switches

5 x Negative  
Polarity  
Switches

HVPS



“Top” CT  
to Control  
Room

“Bottom”  
CT to  
Integrator  
or Local  
Diagnostics

Spare  
Switches



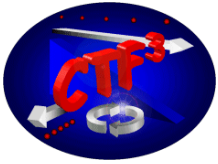
M.J. Barnes

**FRONT VIEW**

January 29, 2009

**BACK VIEW**

13



# Diagnostics & Controls

HOME SPECIALIST SETTINGS HISTORY

**STATUS**

HV MAINS ON

POSITIVE ON

NEGATIVE ON

GLOBAL CONTROL REMOTE

**INTERLOCK**

SAFETY KEY

SWITCH RACK

BOX SWITCH

ACTUAL/MAX

BAD DISCHARGE  0 10

OVER VOLTAGE  0 10

UNDER VOLTAGE  0 10

NO I LOAD  n 10

**ERRORS**

MON JAN 4 11:42:04 2009 INVALID RANGE HVPS POS

**CONTROL**

HVPS MAINS ON OFF

RESET INTERLOCK RESET

COMMANDS V mA

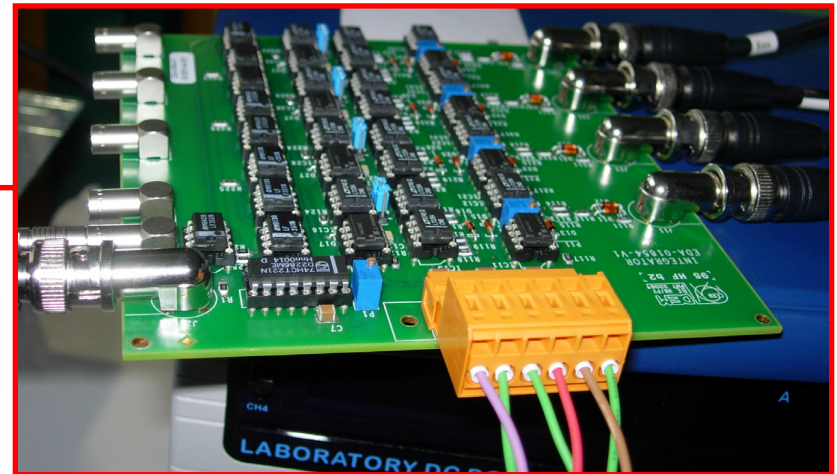
POSITIVE HVPS ON POS OFF POS 5600 5

NEGATIVE HVPS ON NEG OFF NEG 5600 5

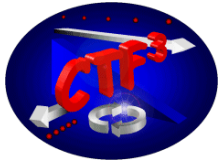
**ACQUISITIONS**

	V.ACQ (V)	Pulse Length (ns)	Pulse
PFN 1	5554	172.25	<input checked="" type="checkbox"/>
PFN 2	5557	172.58	<input checked="" type="checkbox"/>
PFN 3	5551	172.7	<input checked="" type="checkbox"/>
PFN 4	5558	172.56	<input checked="" type="checkbox"/>
PFN 5	5553	172.6	<input checked="" type="checkbox"/>
PFN 6	5556	172.7	<input checked="" type="checkbox"/>
PFN 7	5553	172.59	<input checked="" type="checkbox"/>
PFN 8	5556	172.55	<input checked="" type="checkbox"/>

**Screen-Dump: Local Controls**  
(courtesy of Jan Schipper)



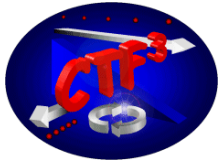
**Four Channel Integrator Card, for  
"Online" Local Diagnostics**



# Tail-Clipper Status

Striplines received at CERN early December 2008:

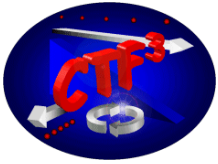
- Shock detector activated during shipment !, but no problems apparent;
- Impedance checked: 1 plate of a pair driven  $\Rightarrow 56\Omega$  (as per predictions);
- HV pulses applied to striplines in air (6kV pulses to one plate at a time;  $\pm 5.6$ kV pulses to both plates of a stripline, simultaneously). No breakdowns;
- Results from Jan Hansen, re vacuum:
  - Leak rate  $< 1 \times 10^{-10}$  mbar•l/s;
  - $8.8 \times 10^{-8}$  mbar pressure on downstream gauge after 72 hours pumping;  $\Rightarrow$  expected pressure in TL2 line of  $\sim 1 \times 10^{-7}$  mbar downstream of the kicker with the taper installed and using a standard CTF3 pumping port;
  - $3.4 \times 10^{-9}$  mbar pressure on downstream gauge 5 days after a  $120^\circ\text{C}$  bake out for 24 hours  $\Rightarrow$  expected pressure in TL2 line of  $\sim 7 \times 10^{-9}$  mbar downstream of the kicker with the taper installed and using a standard CTF3 pumping port (no bake out of striplines foreseen in the machine).
- Situated in TL2.
- **Fruitful collaboration with CIEMAT on BOTH TL2 Tail Clipper & CR Extraction kicker !**



# Tail-Clipper Status & Ongoing Works

- **Tests on TL2 Pulse Generator:**
  - show excellent (fast) rise-time;
  - system has been reliable
  - low noise levels within pulse generator.
- **Kicker cabinets installed in building 6102, January 2009.**
- Method developed to measure relative timing of Behlke Switches: method to be applied and, if necessary, relative timing modified.
- Radiation will be higher than originally appreciated, therefore cable connectors with ceramic insulators ordered (to replace existing connectors with plastic insulators).
- Cables between cabinets & striplines to be made, pulled & relative timing of pulses at striplines checked (before week 11 !!).
- FID (Fast Ionization Dynistor) expected delivery in 2 months time!! – will be evaluated.





# Future Kicker Requirements

## ➤ DRIVE BEAM

- Combiner Ring #1 Extraction (**2.4GeV**; ~830kHz burst rate for 63 pulses; 100Hz rep-rate; 240ns flattop; **voltage may be very high?? – depending on aperture**).
- Combiner Ring #2 Extraction (**2.4GeV**; ~320kHz burst rate for 42 pulses; 100Hz rep-rate; 240ns flattop; **voltage may be very high?? – depending on aperture**).
- Decelerating ring kickers (extraction into decelerating structure) – 42 systems!; **Pulse to pulse stability may be important.**
- Phase feedback kickers (<100ns latency) – 96 kickers per side!

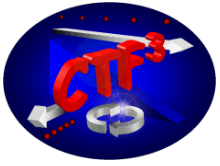
## ➤ MAIN BEAM

- Injection and Extraction. Extraction requires a **high stability flattop**.
- Inter-train feedback kickers at intersection points (**pulse to pulse correction**). 1TeV, 1nrad, **<40ns latency (10ns preferred)**.
- Beam Abort Kickers?? (e.g. 1 kicker per drive beam sector, **21<sup>st</sup> (final)** kicker per side (1.5 TeV) ⇒ **1T•m**, 150Hz: R.W. Assmann, F. Zimmermann, “Efficient Collimation And Machine Protection For The Compact Linear Collider”, EPAC06).

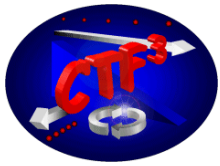
## ➤ DAMPING RING EXTRACTION

- **High precision (e.g.  $\pm 1 \times 10^{-4}$ )**, 2.9GeV, 50Hz, 160ns pulse width, 1 $\mu$ s rise & fall, ~5mm aperture.

Table of preliminary parameters, for above, to be presented to kicker group, for discussion, in next couple of weeks ! – important for our input into CDR.

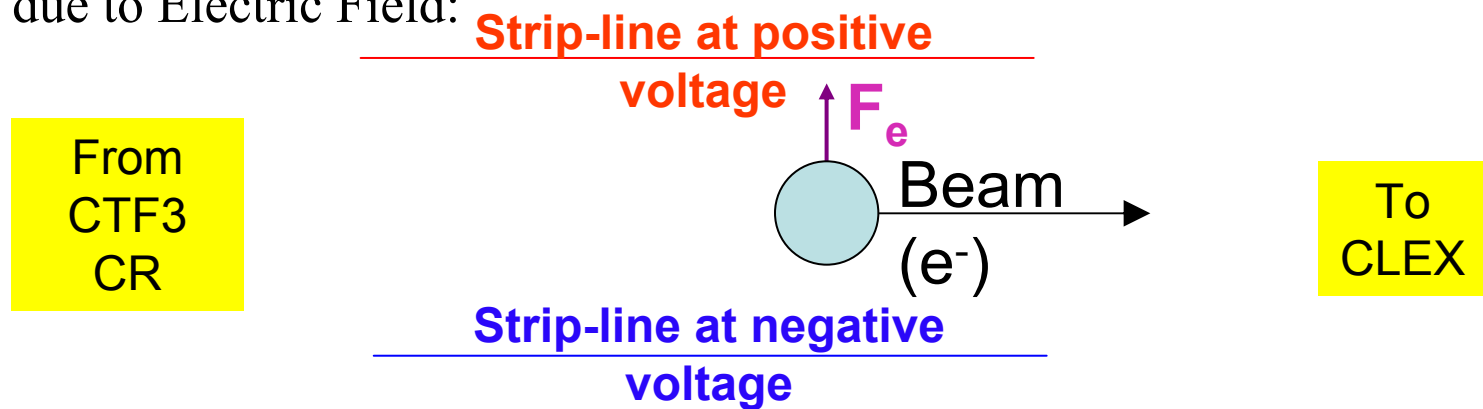


# Questions ???



# Tail Clipper: Deflection

Deflection due to Electric Field:



Deflection due to Magnetic Field:

