

- No data on FRM bits F1: Frame Type, 2 16-bit words
- Frame Info on FRM bits F2: No Frame Type, 2 16-bit words
- Frame Type on FRM bits F3: No Frame Type, 1 16-bit words
- Data Frame on FRAME bits F4: Frame Type, 0 16-bit words

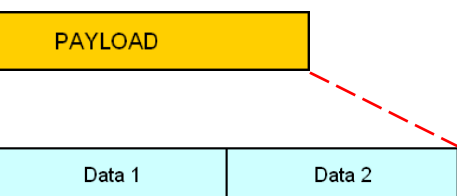
Link data stream



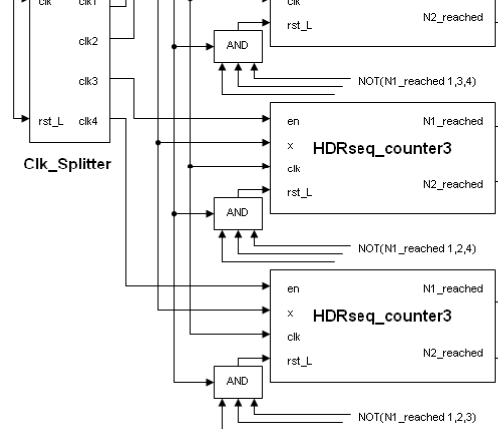
Structure



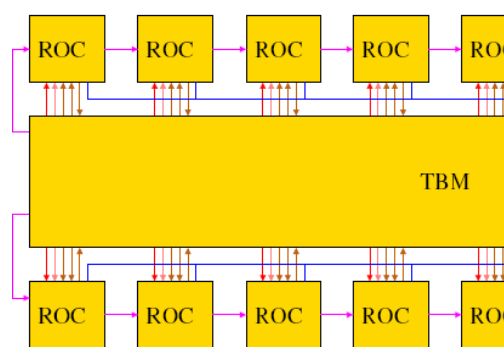
Structure



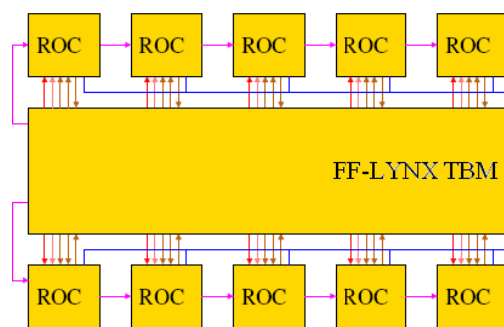
payload: 3 byte I²C command



Case



Current architecture: “standard” controls and token based analog Architecture proposed for the P distribution with “fast” I²C control the digital readout → 2×160 MB



“Hybrid” architecture for the P distribution, custom token based FF-LYNX based digital readout fibers available for DAQ.

Phase I and Phase II upgrades

