

What Have we Learned from the Commercial Buck Converters?

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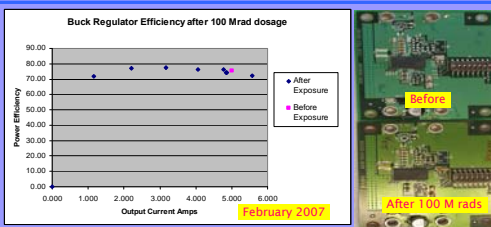
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Abstract

Commercial power converters that have voltage ratios greater than ten and are capable of running near the LHC collision region would increase the efficiency of the power distribution system of the ATLAS Silicon Tracker high luminosity upgrade. The devices must operate in a high magnetic field (2T) and be radiation hard to ~50-100 Mrad and ~10¹⁵ Neq/cm². These converters are to be mounted on the same multi-chip modules as the ASIC readout chips or in close vicinity without introducing any additional readout noise due to the high switching frequencies. Such devices will permit higher voltage power delivery to the tracker and thus increase overall power efficiency by limiting the ohmic losses in the stretch of cable (about 100 meters) between the tracker and the power sources.

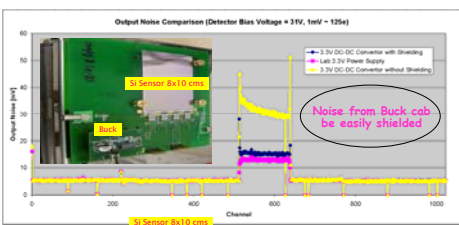
Need for New methods of Power Distribution

Presently LHC inner detector electronics use DC power supplies located in the counting house that feed low voltage power over a long distance (30 m for CMS and 140 m for ATLAS detector). Here we focus on the powering of the silicon tracker for the high luminosity LHC that shall result in x10 higher luminosity and use x10 more detecting elements. The 'Power Delivery with Existing SCT Cables' plot illustrates the problem. At present 10.25 V power from the counting house is delivered by 4088 power cables each with a resistance of 4.5 Ω. The 10 chip ASIC readout chip hybrid kapton PCB needs 1.5 amps @ 3.5 Volts. The bar graph shows the power delivery efficiency of ~33%. In an upgraded ASIC design with finer lithography and x2 more chips, the voltage drops to 1.3 V and with the same cables the power delivery efficiency drops to 10%. By inserting a DC-DC with x10 voltage converter on the 20 chip hybrid kapton PCB, the efficiency climbs to over 80%.



Tests Done in 2007

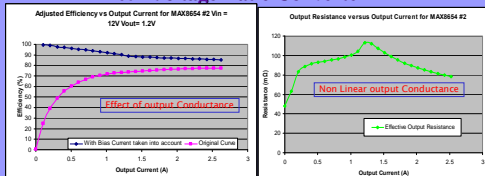
- Radiation Test - Enpirion 6 amp Device 0.25 μm CMOS - 100 Mega rads
- Evaluation Boards: Enpirion, Linear Technology, Micrel, MPS, TI etc
- Air Coil Tests on Chips with External Inductors
- Pick up Noise: RHIC Polarimeter Silicon with Analog Readout
- RF Leakage
- Noise and Pickup Tests - ATLAS Silicon Tracker Module at RAL
- Magnetic Field Vout increased 1in 900 @ 7T
- Load Efficiency Tests
- Noise Same PCB Tests: DC-DC Converter on the Hybrid @ BNL & Yale
- Antenna Effect Tests



Tests Done in 2008

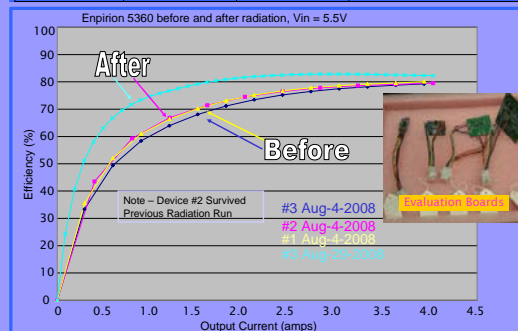
- Radiation Test - A dozen devices-
- Why are Enpirion chips (IHP Foundry) Rad Hard ?
- Combination of Foundry & Circuit Design
- Discuss with IHP, Chip Designers, etc
- Air Coil Developments
- Load Efficiency Tests for 10:1 Voltage Conversion ratio

10:1 Voltage Ratio Converter



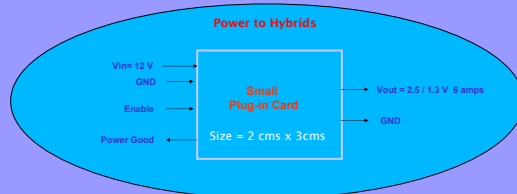
2008 Radiation Damage Tests

Device	Time in Seconds	Dose before Damage Seen (krad)	Observations
TPS 62110	720	40	Increasing input current
ISL 8502	730	40.6	Increasing input current
MAX 8654	850	47.2	Loss of output voltage regulation
ADP 21xx	1000	55.6	Loss of output voltage regulation
ST 1510	2250	125	Loss of output voltage regulation
IR3822	2500	139	Increasing input current
EN5382	2000	111	Loss of output voltage regulation
EN5360 #3	864000	48 Mrad	MINIMAL DAMAGE
EN5360 #2	TESTED IN 2007	100 Mrad	MINIMAL DAMAGE



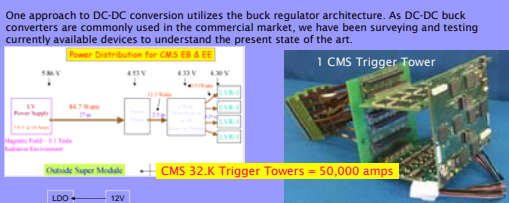
Features inside a Buck Chip

- Power Down: Low power mode. Shut output switches
- Output High side current limit pulse by pulse (turn off high side FET. After 16 times go to soft Start)
- Output Low side current limit pulse by pulse (turn off low side FET)
- Power OK if Vout with in 10% of set voltage
- Vout overvoltage > Disable high side FET
- Thermal Shutdown on over temp. Restore on cool down
- Good thermal contact to PCB for heat removal
- Produce 5V with a LDO from higher voltage
- Current monitor 1000:1 Sense FET or 10 mV Resistor shunt
- Under voltage Input protection
- External Protection > Limit Absolute Max Power Supply Voltage
- Slow Turn on but NO SLOW TURN OFF- Inductive Kick ???
- ASIC to have slow Turn off

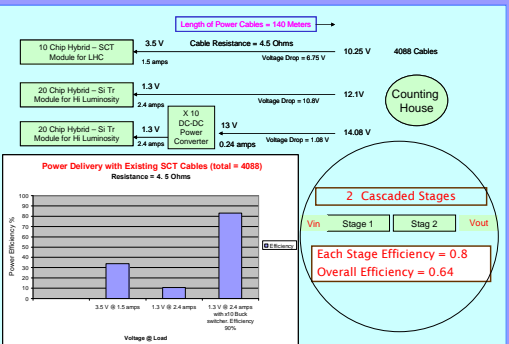


CMS ECAL Powering

There is a clear need for a new system of power delivery to the upgraded ATLAS Silicon Tracker for the SLHC. Conventional powering will result in an efficiency of power delivery to the detector of about 10% with existing cables whose size are limited in cross section due to space and mass constraints. A system featuring DC-DC converters with voltage ratios of ten will result in an estimated efficiency on the order of 70-80% with existing cables.



Power Distribution Schemes and Efficiencies



Air Coils Proximity Effect increases Losses

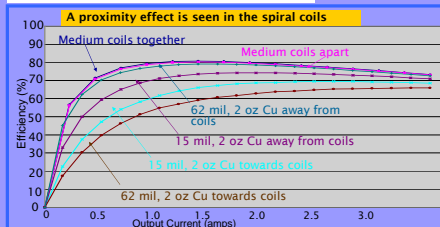
Coil Spacing	100 KHz	1 MHz
Wide	L 1.21 μH R 0.098 Ω	L 1.16 μH R 0.094 Ω
Near but not touching	L 1.80 μH R 0.088 Ω	L 1.70 μH R 0.300 Ω
Pressed Together	L 2.37 μH R 0.080 Ω	L 1.93 μH R 1.300 Ω

Spiral Coils 3 Sizes
2 oz Copper on 3 mil FR4

Medium Trace = 30 mils Space = 10 mils

Small Trace = 20 mils Space = 10 mils

Large Trace = 30 mils Space = 10 mils



Conclusions/ Future Work

Enpirion EN5360 is a proof of principle that a commercial COTS device can be radiation hard. In our visit to the HP factory in Nov 2008, it was revealed that they have developed 12 Volt FET transistors in the same process. This is essential for a 10:1 Voltage conversion BUCK. Samples of these transistors have been irradiated to levels exceeding those expected at the SLHC. We are attempting to understand differences in the IHP fabrication process that lead to a successful device. Additionally, as next generation devices come on the market we will use the infrastructure we developed to quickly evaluate these devices