Ideas on low noise preamp with controlled Zi

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Calorimeter Electronics Upgrade - LAL - April 8th 2009

Outline

- I. Requirements
- II. Noise
- III. Feedback amplifiers
- IV. Cable coupled

I. Specifications PMT gain must be reduced by a factor 4-5

	Value	Comments
Energy range	0-10 GeV/c (ECAL)	1-3 Kphe / GeV
Calibration	4 fC /2.5 MeV / ADC cnt	4 fC input of FE card: assuming 25 Ω clipping at,PMT base 12 fC / ADC count if no clipping
Dynamic range	4096-256=3840 cnts :12 bit	Enough? New physic req.? Pedestal variation?
Noise	<≈1 ADC cnt or ENC < 5 -6 fC	< 1 nV/√Hz ?
Termination	$50\pm5~\Omega$	Passive vs. active
AC coupling	Needed	Low freq. (pick-up) noise
Baseline shift prevention	Dynamic pedestal subtraction (also needed for LF pick-up)	How to compute baseline? Number of samples needed?
Max. peak current	4-5 mA over 25 Ω	50 pC charge at PMT output
Spill over correction	Clipping	Residue level: 2 % \pm 1 % ?
Spill over noise	<< ADC cnt	Relevant after clipping?
Linearity	< 1%	
Crosstalk	< 0.5 %	
Timing	Individual (per channel)	PMT dependent

II. Noise computation

• Total noise power (or variance) at the output preamplifier (gain A, time constant τ) + integrator (time constant τ_i , integration time T):

$$\psi_{white}^{2}\left(t_{0}=t_{1}+T\right)=\frac{1}{2}e_{niwhite}^{2}\left(\frac{A}{\tau_{i}}\right)^{2}\left(T-\tau\left(1-e^{-\frac{T}{\tau}}\right)\right)\simeq\left|_{T\gg\tau}\frac{1}{2}e_{niwhite}^{2}\left(\frac{A}{\tau_{i}}\right)^{2}T\right)$$

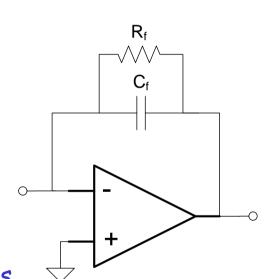
• Signal at the end of integration (τ_s is signal decay time):

$$v_o(t=T) = R_{PMT} \frac{A}{\tau_i} Q \left(1 - e^{-\frac{T}{\tau_s}}\right) \approx \Big|_{CLIPPING} R_{PMT} \frac{A}{\tau_i} Q$$

- Then, for $\sqrt{\text{noise power = signal of } Q_{LSB}}$ Clipping impedance // 50 Ω $e_{niwhite} < \frac{R_{PMT}Q_{LSB}}{\sqrt{T/2}} \simeq \frac{\sqrt{17\Omega \cdot 5fC}}{\sqrt{25ns/2}} \simeq 0.75 \frac{nV}{\sqrt{Hz}}$
- For a line adaptation (50 Ω) noise requirement is relaxed to 2 nV/ \sqrt{Hz}
- Additional shaping (delay line int. reset or any high pass filter) was not taken into account, it will improve (a little bit): LF is removed

III. Feedback amplifiers

- Electronically cooled termination:
 - Transimpedance amplifier
 - Charge amplifier if clipping in base
- Dominant pole of the amplifier should be higher than BW of the signal $\frac{GBW}{G_0} \equiv \omega_d >> BW$
- Input impedance is inductive: stability
- Low noise:
 - Amplifier can be optimized
 - Rf contributes to parallel noise, if larg enough it is negligible
- Input impedance depend on open-loop properties of the amplifier:
 - Can controlled to the few % level in ASIC



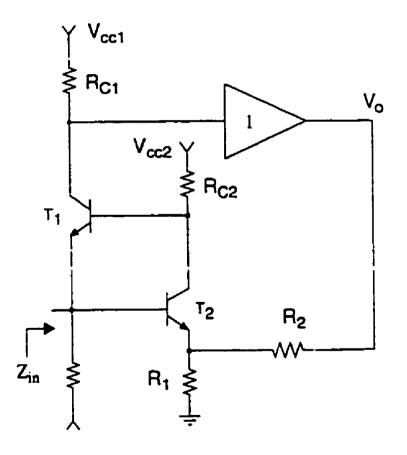
 $Z_{i}(j\omega) \simeq R_{f} \frac{\frac{GBW}{G_{0}} + j\omega}{GBW} \simeq \left| \frac{GBW}{G_{0}} < \omega \frac{R_{f}}{G_{0}} \right|$

IV. Cable coupled amplifier (ATLAS LAr style)

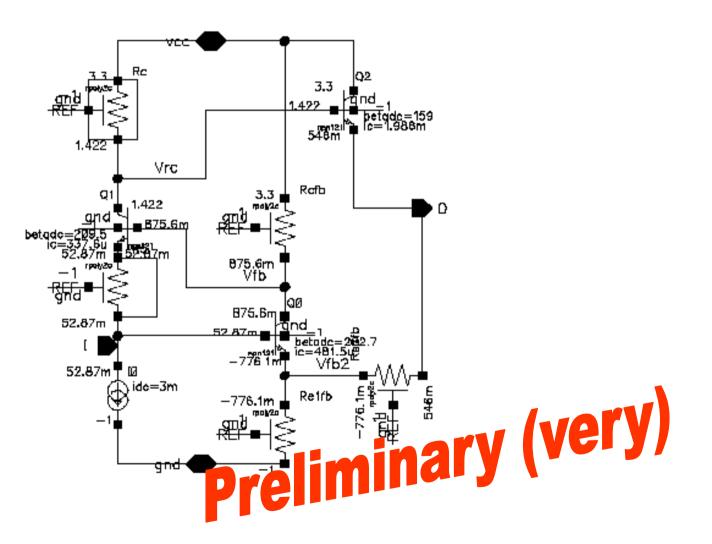
- Common gate with double feedback
 - Inner loop to reduce input impedance preserving linearity and with low noise
 - Outer loop to control the input impedance accurately

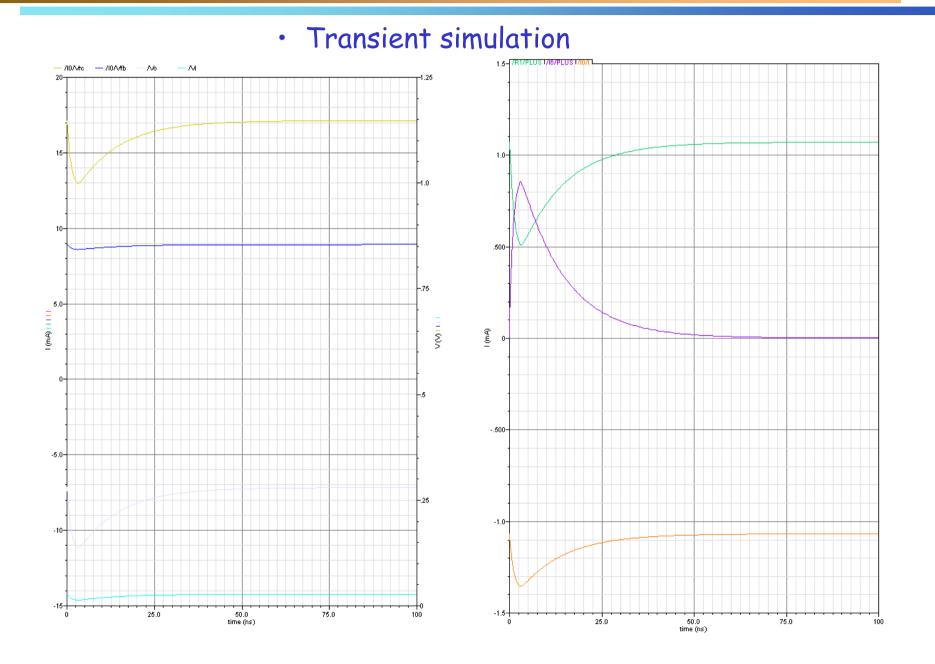
$$Z_i(j\omega) \simeq \frac{1/g_{m1}}{G} + R_{C1} \frac{R_1}{R_1 + R_2}$$

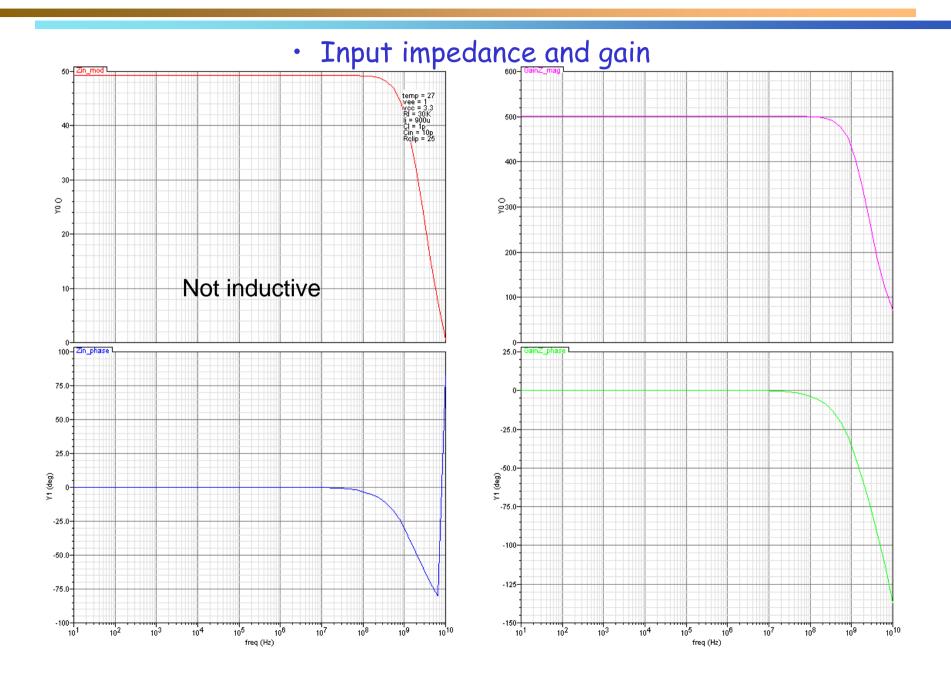
- Transimpedance gain is given by R_{C1}
- Noise is < 0.5 nV/sqrt(Hz)
 - Small value for R_1 and R_2
 - Large g_{m1} and g_{m2}



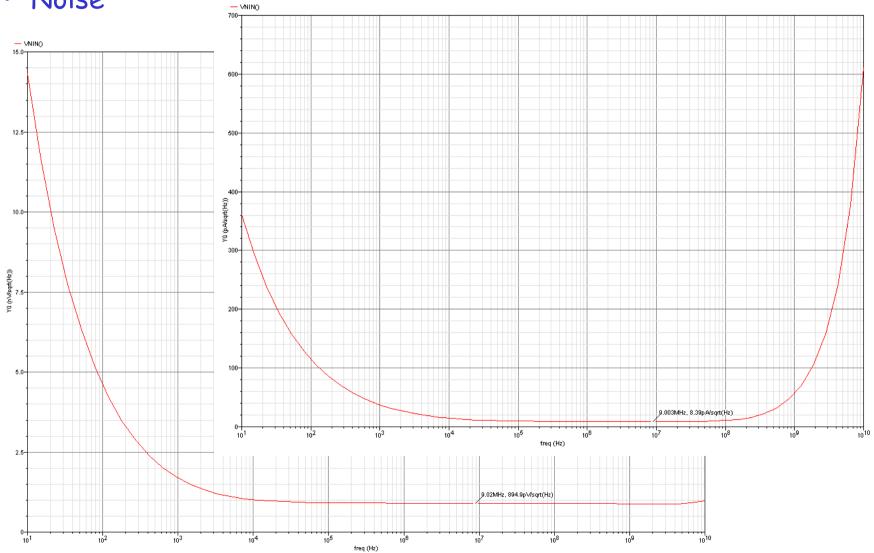
• Some simulations in SiGe 0.35 μ m CMOS (AMS)



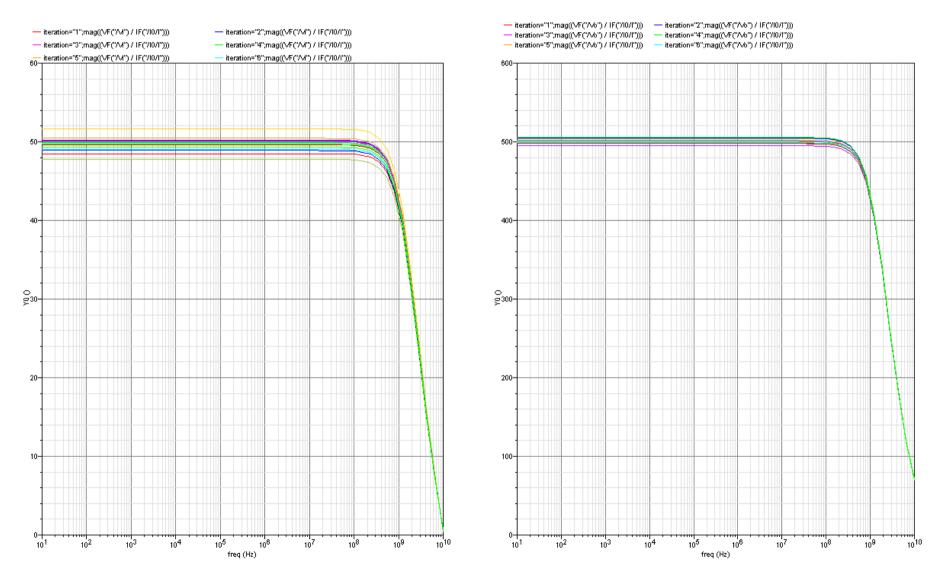




• Noise



• Monte Carlo: Mismatch: no problem



 Process variations: >5% effect, but can be compensated (whole production run)

