

CMOS Monolithic Pixel R&D at LBNL

Friday, 10 March 2006 09:25 (20 minutes)

The talk will present recent progress in the design and characterisation of CMOS pixel sensors at LBNL. The talk will report results of lab tests, beam tests and radiation hardness tests carried out at LBNL on a test structure with pixel of various sizes.

We will also report the first preliminary results of a detailed characterisation of backthinned CMOS pixel sensors and discuss future activities.

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Session Classification: Tracking and Vertexing

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