

Monolithic CMOS Pixel Detectors for ILC Vertex

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The Yale/Oregon effort to develop a CMOS vertex detector sensor with single bunch crossing time stamping will be reviewed. The R&D program is first developing a large pixel (50 μm x 50 μm) sensor, with a four deep memory for each pixel, and eventually will move to fine pixels (10 μm x 10 μm) by changing to a smaller feature size semiconductor process.

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