

## ASIC SOLUTION: INTEGRATOR STAGE

Eduardo Picatoste
Calorimeter Electronics Upgrade Meeting

## **Specifications**



- Characteristics of the fully differential operational amplifier to be used on the integrator stage:
  - Gain bandwidth GBW > 500MHz
  - Phase margin > 65<sup>o</sup>
  - Common mode control
  - Slew rate better than 2 V/µs
  - $-V_{CM} = 1.5 V$

## Design



- Similar amplifier as the one used on the SPD
- First analysis done with:

