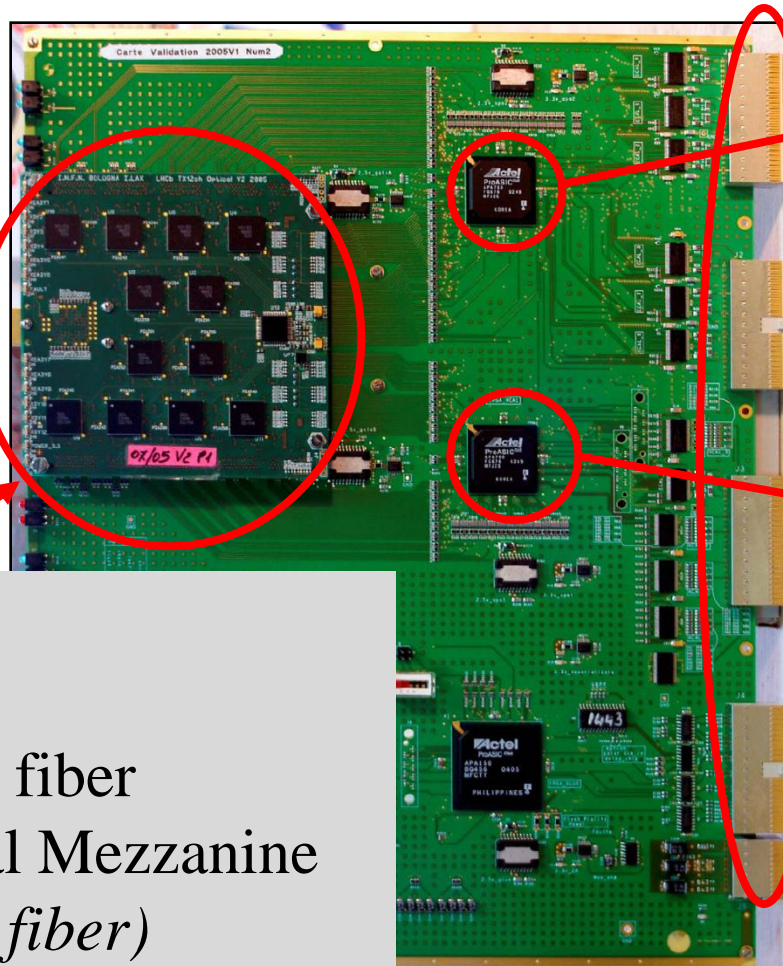


# Trigger Validation Board



FPGA AP/ERPI

Outputs: électron

4 bus HCAL

8 bus ECAL

8 bus PS/SPIO

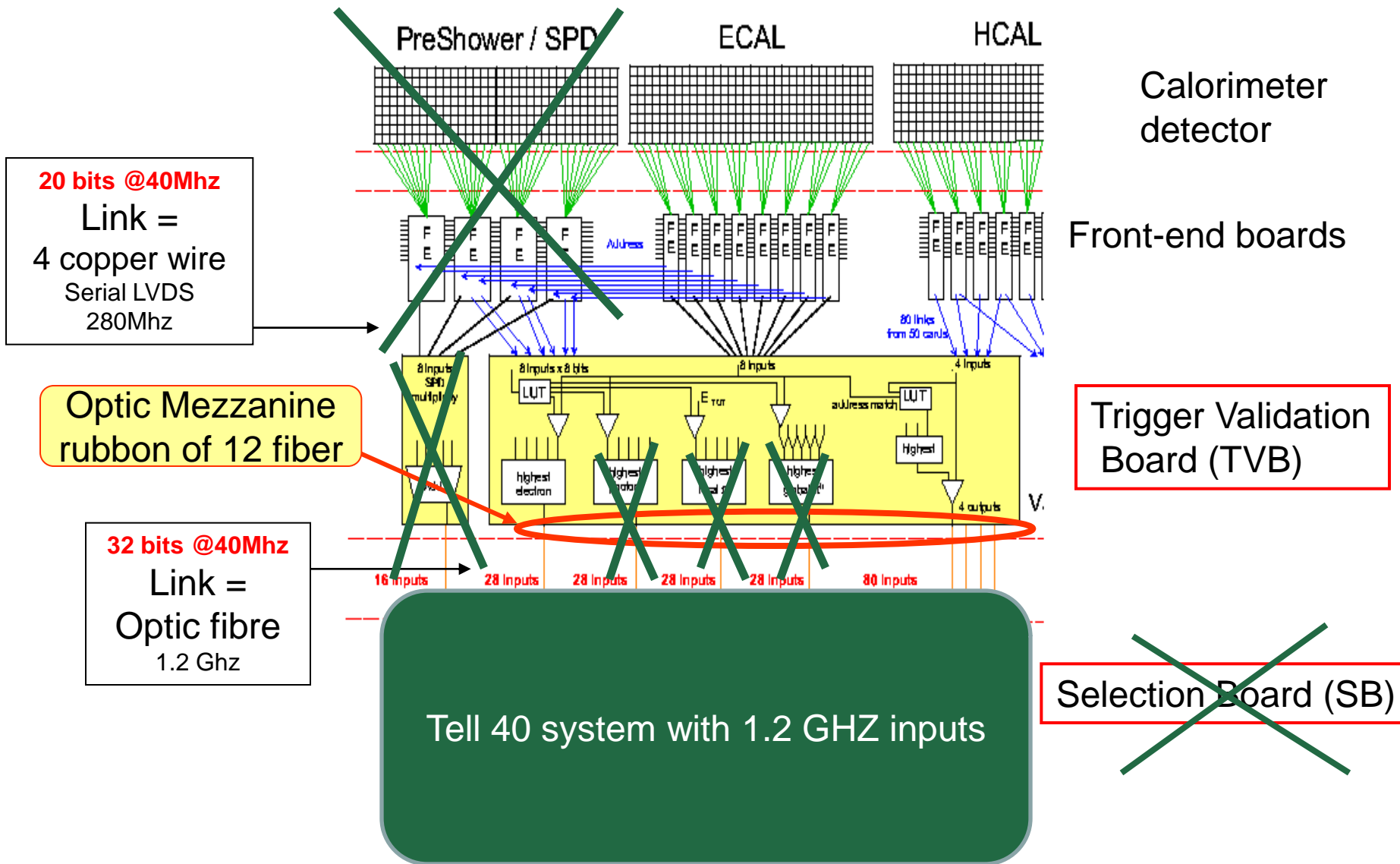
FPGA HCAL

4 outputs « Hadron »

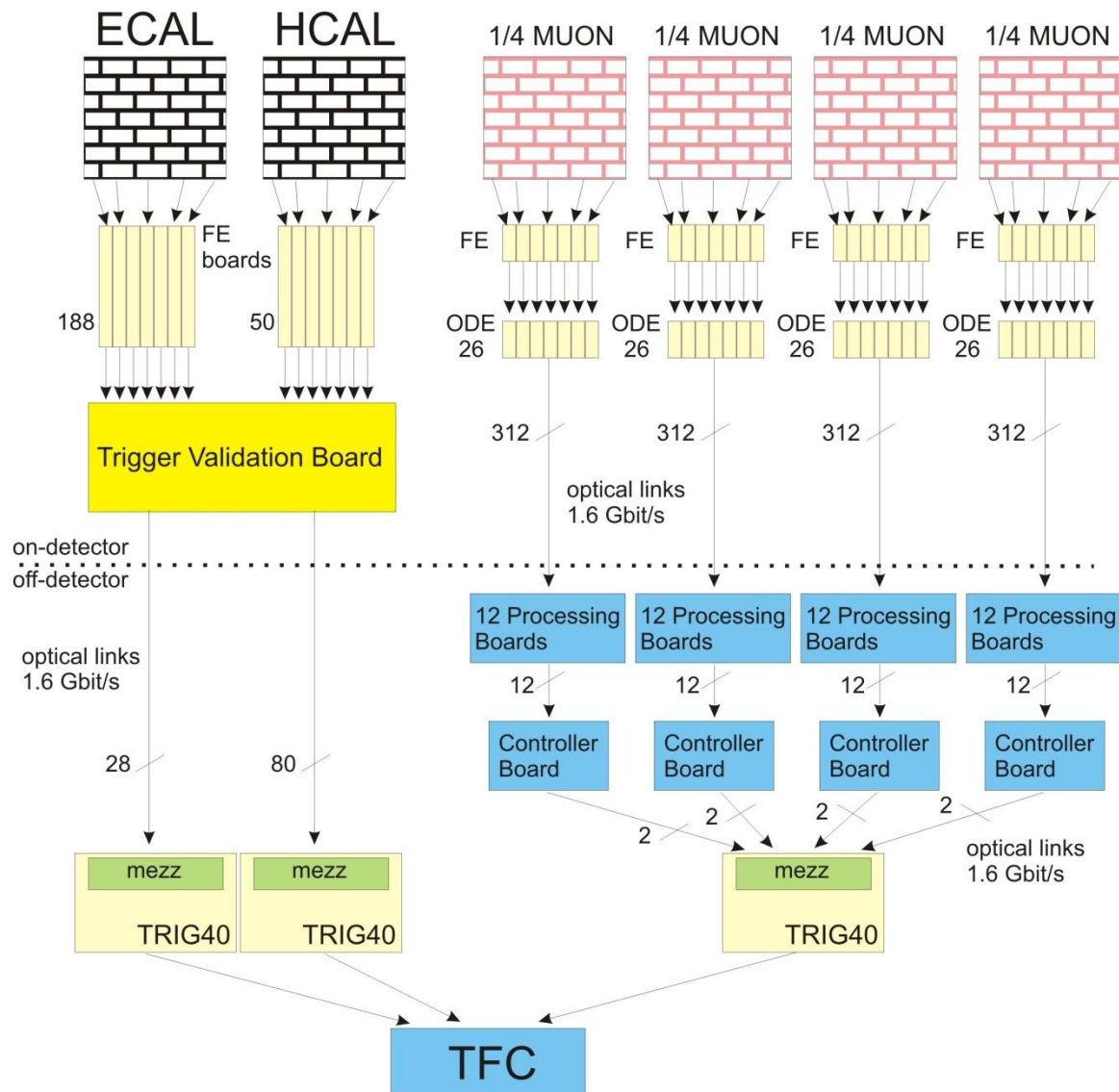
## Outputs

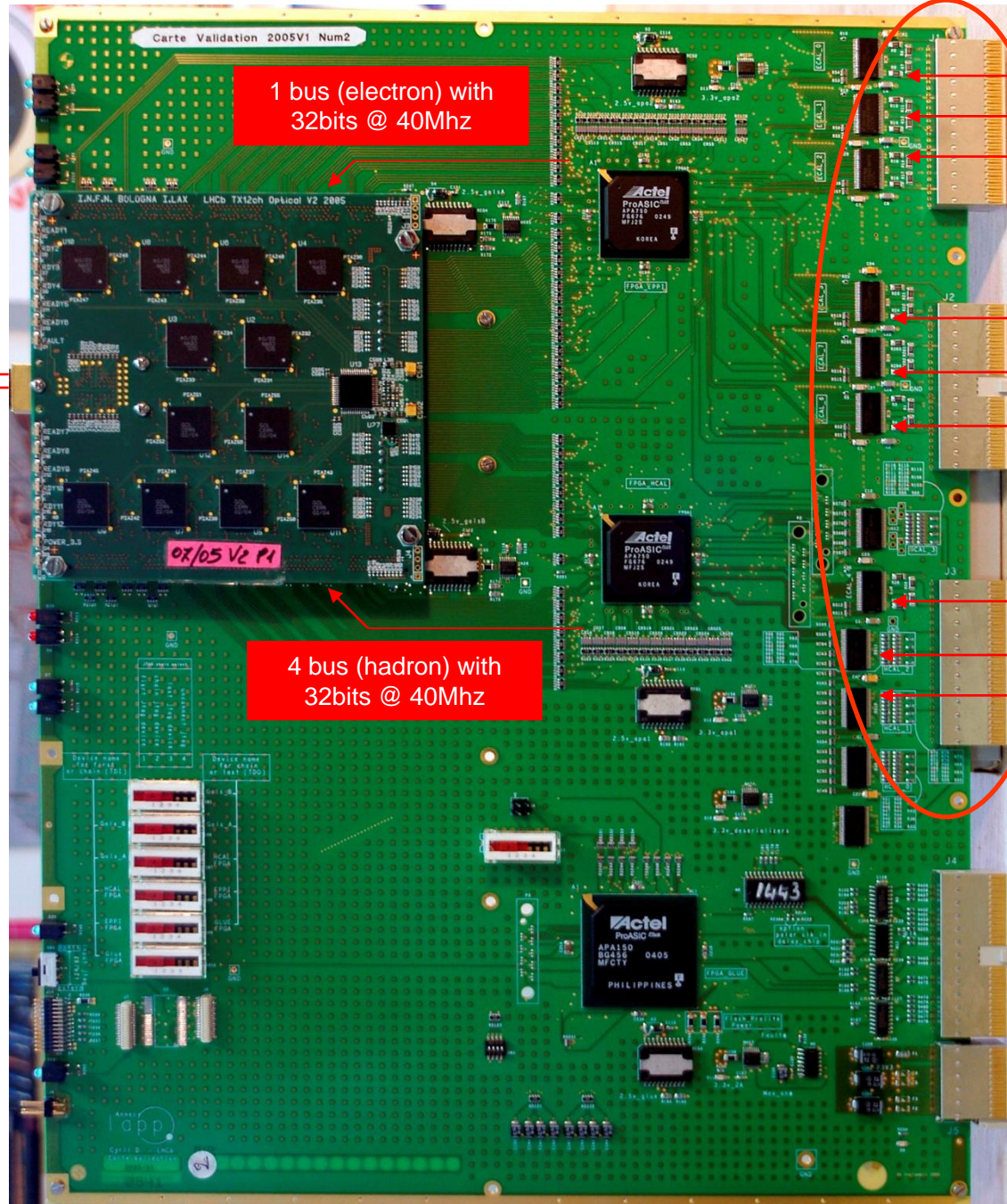
Rubon of 12 fiber  
From Optical Mezzanine  
(only 8 used fiber)

# Upgrade – possibility 1



# Update trigger design at this time....





1 bus (electron) with 32bits @ 40Mhz

5 optical fiber

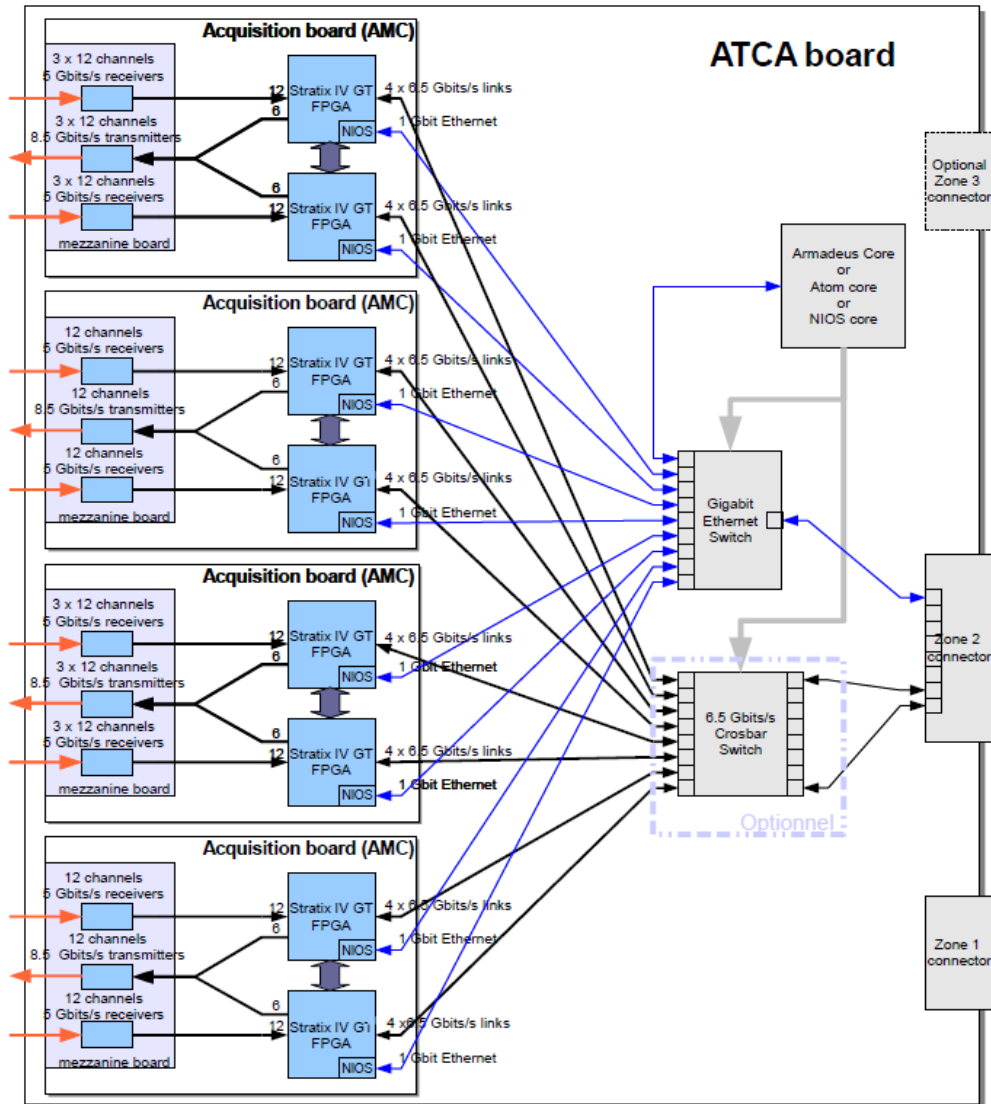
1.2GHz  
Optic level

4 bus (hadron) with 32bits @ 40Mhz

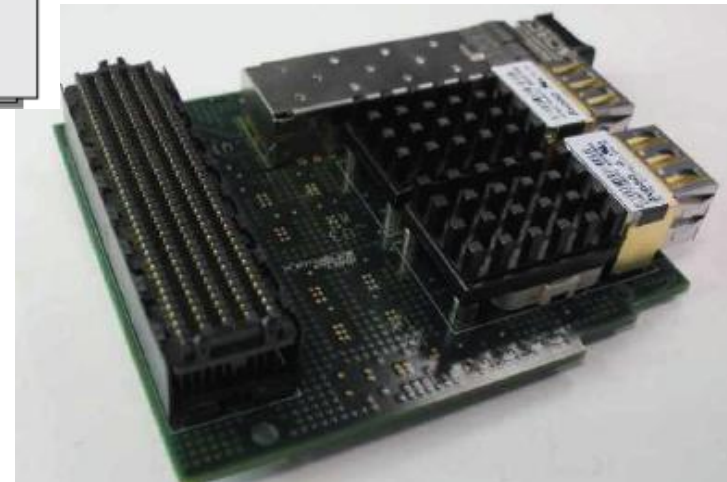
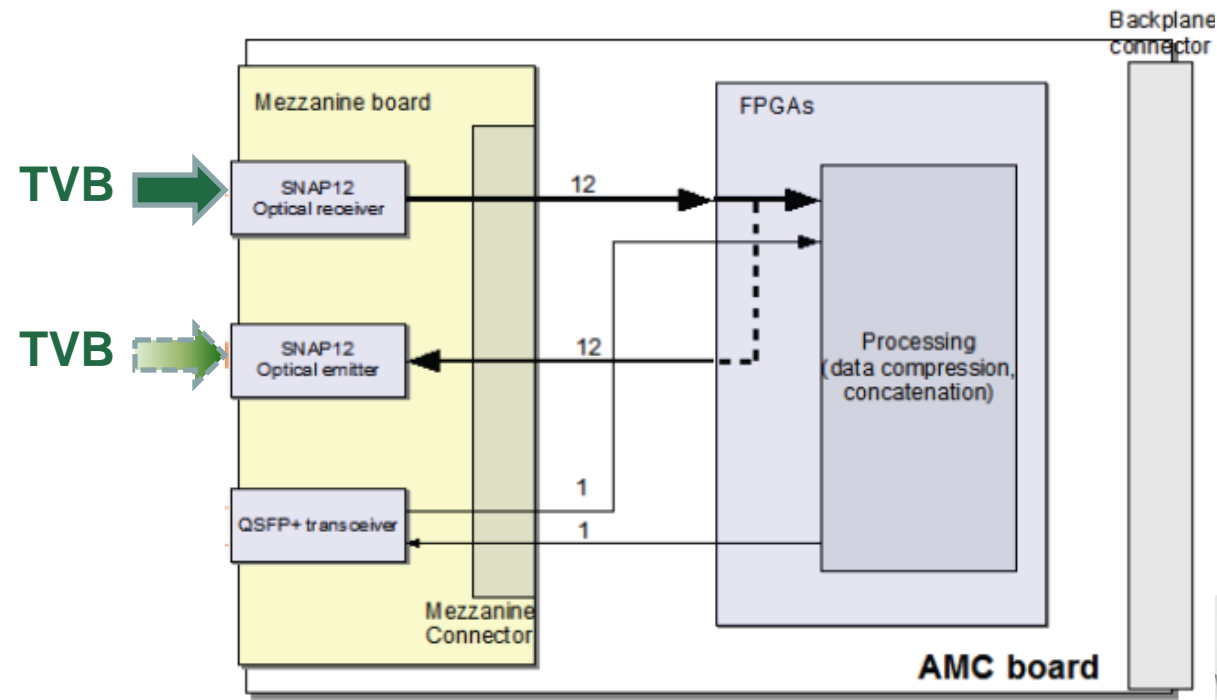
12 bus (8 ECAL + 4 HCAL) with 20 bits @ 40Mhz

280MHz  
LVDS level  
D2+  
D2-  
D1+  
D1-  
D0+  
D0-  
CK+  
CK-

# Tell40 design at this time....



# AMC-Tell40 design at this time....



# Trigger calorimeter Tell40

- 4 AMC in one TELL40
- 80 hadrons TVB outputs => 4 or 8 AMC
- 28 electrons TVB outputs => 2 or 3 AMC
  
- Resume: Calorimeter trigger need 2 or 3 Tell40