

# HLT Configuration(s) and “other business”

Updated version of

<http://indico.cern.ch/getFile.py/access?contribId=95&sessionId=9&resId=0&materialId=slides&confId=44134>

# HltLine (concept)

- An Hlt decision involves more than a single algorithm
  - one might pick some L0 candidates
  - another (sequence) may do some VELO reconstruction
  - a third may match the above two
  - ....
- In addition, it has some ‘standard’ pre/post processing:
  - ‘entry’ filters (ODIN, and/or L0, and/or other HLT decision<sup>(\*)</sup>)
  - Prescale
  - “The algorithm (sequence) which most people think of as the decision”
  - Postscale
- And it needs to record the final yes/no result
- And possibly catch errors in the ‘hosted’ algorithms
- Let’s call this entire structure an ‘HltLine’

(\*) why the small print: because it breaks an important rule: HltLines should be *independent* of each other  
Unfortunately, we need some exceptions: Hlt1 Global (the OR of all Hlt1 Lines) by definition depends on other lines  
exception on the exception: Would be OK for an Hlt2 line to depend on an Hlt1 line

# HltLine (C++)

- Invokes the various stages
  - Each stage is an independent algorithm, HltLine *only* relies on ‘FilterPassed’
- Updates an entry in HltDecReports ‘as it goes along’
  - Catches exceptions and errors in the ‘hosted’ algorithms, updates HltDecReport entry accordingly, and recovers (hopefully)
- As a result, the status is recorded in TES (in HltDecReports) in uniform and reliable way:
  - convention: *all* configured HltI decisions appear in HltDecReports, regardless of their result
  - at some point we may remove negative decision before conversion to rawbank -- but only after it has been shown that on readback we can reliably recreate the (relevant) missing information from the configuration
- Accept/Reject is recorded separately from ‘how far we got’: even if the event ‘fails’, it could be accepted, eg. because HltLine caught an exception: a (limited!) number of such errors will/could result in an ‘accept’.

# HltLine (python)

HltLines are created by calling some dedicated python code

- Enforces uniform naming convention
  - might add some additional rules!
- Makes it easier to write HltLines
  - for HltAlgorithms, automatically ‘daisy chains’ input and output
    - TODO: improve management/specification of ‘many-to-one’ flow
  - Can ‘copy-and-modify’ entire lines to easily changes create variations:
    - eg. clone, decrease threshold and increase prescale
  - re-use pre-defined sequences (of sequences, of ... ) through ‘bindMembers’
    - usefull, as each line *MUST BE* independent, and can not rely on results of other lines
- Registers the existence of a line -- this is used to actually configure Hlt

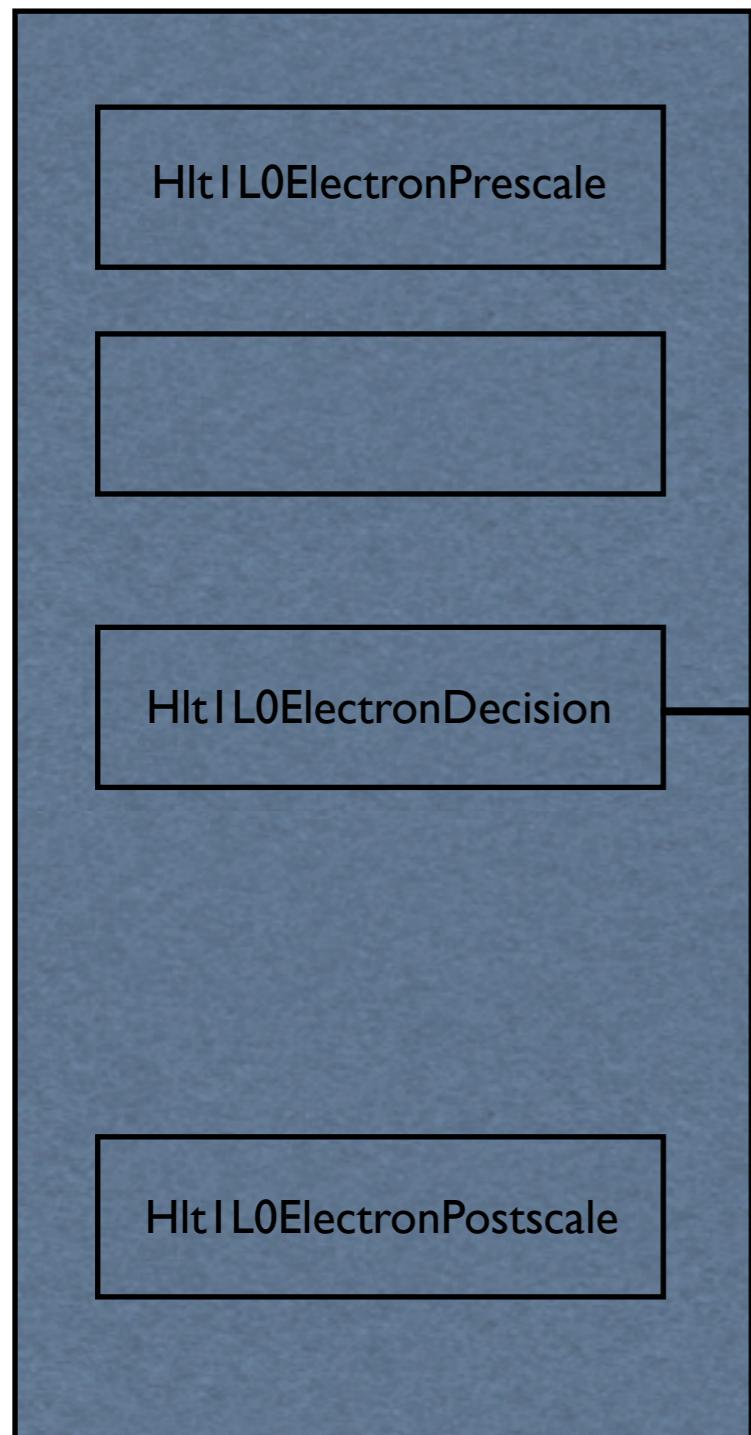
*If you want to add an Hlt decision, you **MUST** write an Hlt Line*

# Example

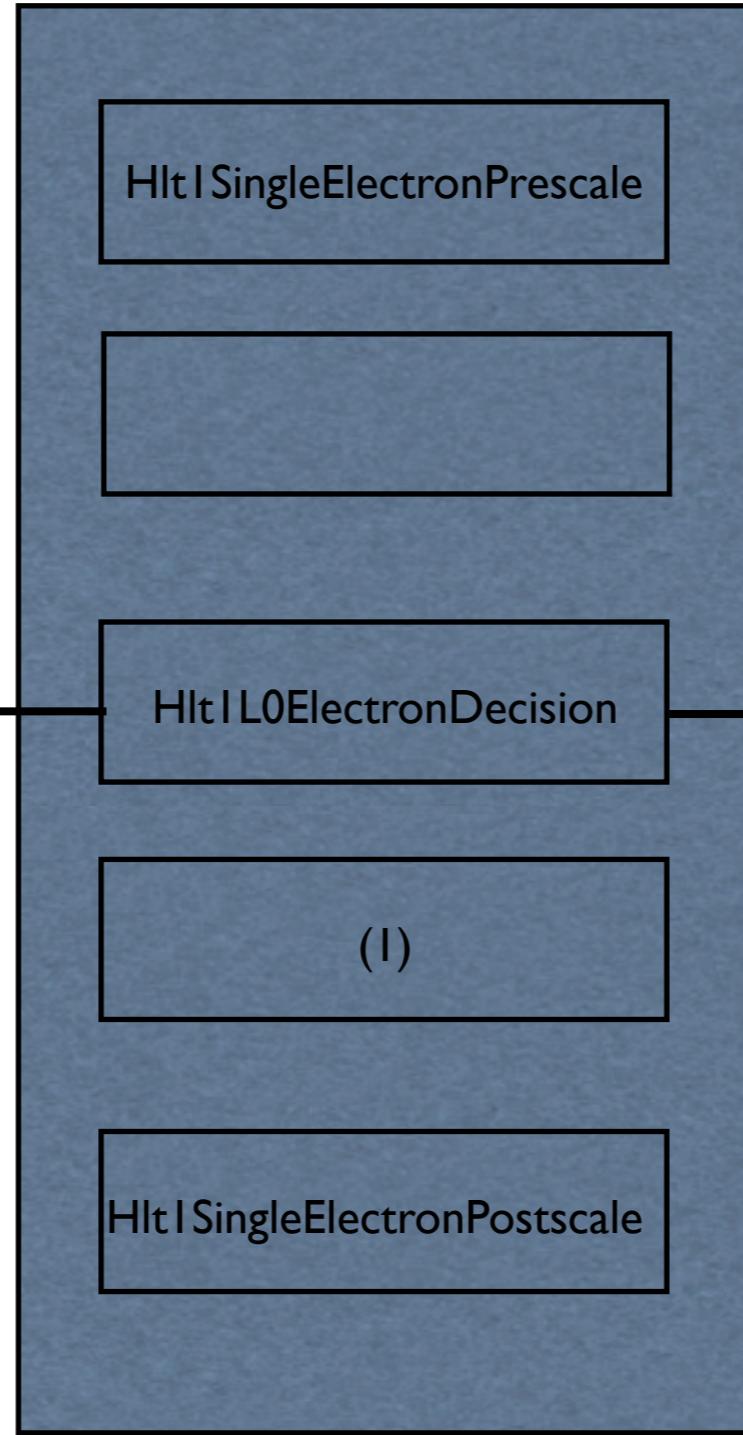
```
class HltL0LinesConf(LHCbConfigurableUser):
    __slots__ = { 'Prescale' : 1
                 , 'Postscale' : 0.000001
                 , 'L0Channels' : [] # if empty, use all pre-defined channels
                 }

def __apply_configuration__(self):
    channels = self.getProp('L0Channels')
    if not channels : channels = L0Channels() ← If an L0 TCK is explicitly specified, HltConf().L0TCK = ...
    for channel in channels :                                     goes out and finds the configured L0 channels, otherwise default to
        Line ( 'L0' + channel                                         the 'canonical' list of L0Channels
            , L0DU = "L0_CHANNEL(\""+channel+"\")"
            , prescale = self.getProp('Prescale')
            , algos = [ convertL0Candidates(channel) ]
            , postscale = self.getProp('Postscale') ← Note: HltConf().L0TCK does NOT configure the L0DU to use this
            )                                                 TCK, it is intended to be set by the configuration layer 'above'
                                                       HltConf which also actually configures L0...
                                                       It is there such that at configuration time, one can check that only
                                                       L0 channels which exist get used, and allow to check (partly) the
                                                       consistency between L0 & HLT
                                                      
                                                       Insures that all users of the L0 candidates of a given L0 Channel
                                                       use the same algorithm instance to convert them. This way, this
                                                       only gets done once, while keeping the lines independent
```

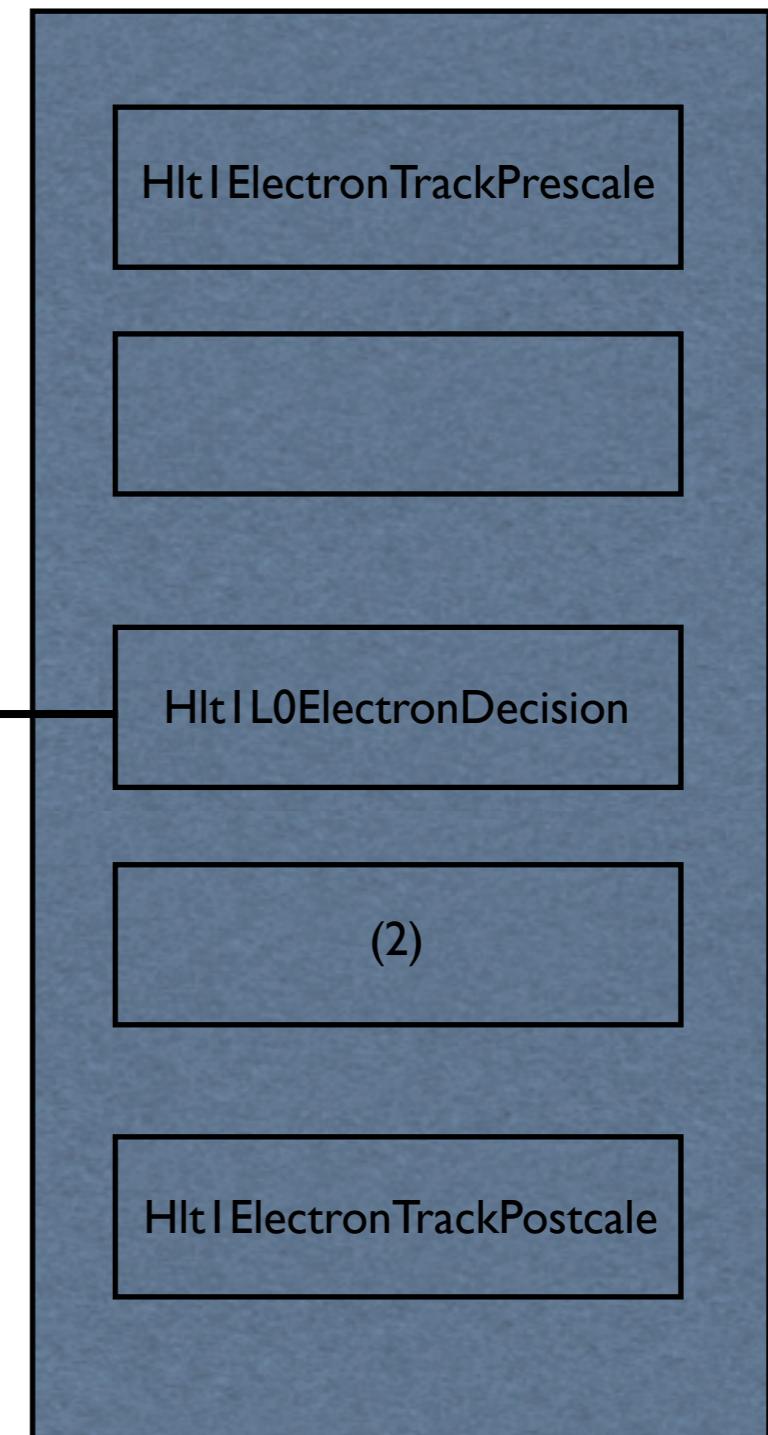
# Hlt|L0Electron



# Hlt|SingleElectron



# Hlt|ElectronTrack



Hlt|L0ElectronDecision runs *at most* once per event, and (1) and (2), when executed, can rely on it always having run before (1) or (2) is excuted, i.e. all three lines are independent by construction!

# More complicated...

```
IP_CUT = str(self.getProp('Ele_IPCut'))
PT_CUT = str(self.getProp('Compan_PtCut'))
companionTrackWithIP = [ RecoVelo
    , Member ( 'TF', 'CompanionVelo',
        FilterDescriptor = [ 'IP_PV2D,||[],'+IP_CUT+',3.', 'DOCA_%TFVeloT,<,0.2' ])
    , Member ( 'TU', 'CompanionForward', RecoName = 'Forward' )
    , Member ( 'TF', 'CompanionForward', FilterDescriptor = ['PT,>,'+PT_CUT] )
    , Member ( 'VM2', 'DiElectron',
        InputSelection1 = '%TFVeloT', InputSelection2 = '%TFCompanionForward',
        FilterDescriptor = ['DOCA,<,0.2'])
    ]
Line( 'ElectronTrackWithIP'
    , L0DU = "L0_CHANNEL('Electron')"
    , algos = [ convertL0Candidates('Electron') ] + prepareElectronWithIP + companionTrackWithIP
        + [ Member ( 'VF', 'VertexCut'
            , FilterDescriptor = [ 'VertexPointing_PV2D,<,0.5', 'VertexDz_PV2D,>,0.' ]
            , OutputSelection = '%Decision') ]
)
Line( 'ElectronFromPi0TrackWithIP'
    , L0DU = "L0_CHANNEL('LocalPi0')"
    , algos = [ convertL0Candidates('LocalPi0') ] + prepareElectronWithIP + companionTrackWithIP
        + [ Member ( 'VF', 'VertexCut'
            , FilterDescriptor = [ 'VertexPointing_PV2D,<,0.5', 'VertexDz_PV2D,>,0.' ]
            , OutputSelection = '%Decision') ]
)
```

# HltLine (python)

- HltLines are grouped together ('alleys')
  - PA(ss through), L0, VE(lo), LU(minosity), MU(ons), HA(drons), EL(ectrons), PH(otons), B(eam)G(ass)
    - mu+track is part of MU
    - One .py file per *alley* defines *all* lines in that 'alley'
- Configuration of HltLines is triggered through HltConf().hltType = ...
  - Hlt1 == L0+VE+LU+MU+HA+EL+PH
  - if Hlt2 in this + separated list, the Hlt2 configuration is invoked after Hlt1 -- so you can specify Hlt1+Hlt2 (the default)
  - HltType 'configures' lines, *then* they get 'added' to the Hlt1 sequencer
    - this is in a postconfig step (which has confused some)
    - because there is some (small) order dependence (eg. Hlt1Global and Hlt1IgnoringLumi can only be made after the rest)

# T rigger C onfiguration K ey

- The Python code is used to generate configurations (by running a careful configured Moore), which are then stored in a ‘database’
  - ‘master’ is in CVS (Hlt/TCKData), can copy to any CORAL supported database (SQLite, Oracle, ... )
  - new configurations become available for use through new releases of the Moore project (TODO: move Hlt/ TCKData to DBase so that eg. DaVinci can access this information)
- The individual configurations are identified by 128 bit configuration IDs (think of this ID as the checksum of the HLT part of the configuration)
- TCK ‘is a’ 32 bit value which labels configurations, lower 16 bits specify L0 config.
- The ‘online’ Hlt (Moore) is configured from the above database
  - initial TCK -- the one to be used during initialization -- is specified by run-control to avoid waisting time on first event
  - Moore ‘preloads’ all other configurations of the same type as the initial one, and allows configuration updates within this set during fast run change
  - before processing each event, a svc insures that Hlt configuration matches TCK in ODIN bank
- Utilities exist for querying this database

# TCKsh

- SetupProject Moore
- TCKsh
  - is just: `python -i -c 'from TCKData.utils import *'`
  - can list configurations, compare them, query for properties of algorithms, etc.

- `listConfigurations()`
  - shows all known configurations, grouped by release, and ‘`hltType`’
- `listAlgorithms(0x803b0000)`
  - given either a TCK or a configID, shows a list of configured algorithms



# getHltLines

- returns a list of HltI lines

```
>>> from pprint import pprint
>>> pprint( getHltLines(0x803b0000) )
['HltLine/HltIPhysics',
 'HltLine/HltIRandom',
 'HltLine/HltIIncident',
 'HltLine/HltIL0Muon',
 'HltLine/HltIL0DiMuon',
 'HltLine/HltIL0MuonNoGlob',
 'HltLine/HltIL0Electron',
 'HltLine/HltIL0Photon',
 'HltLine/HltIL0Hadron',
 'HltLine/HltIL0LocalPi0',
 'HltLine/HltIL0GlobalPi0',
 'HltLine/HltIVeloASide',
 'HltLine/HltIVeloCSide',
 'HltLine/HltIVeloClosing',
 'HltLine/HltILumiNoBeam',
 'HltLine/HltILumiBeamCrossing',
 'HltLine/HltILumiSingleBeamRight',
 'HltLine/HltILumiSingleBeamLeft',
 'HltLine/HltIXPress',
```

PA

L0

VE

LU

XP

'HltLine/HltISingleHadron',  
'HltLine/HltIDiHadron',

'HltLine/HltIMuonNoIPLO',  
'HltLine/HltIMuonNoIPGEC',  
'HltLine/HltIMuonIPCL0',  
'HltLine/HltIMuonIPCGEC',  
'HltLine/HltIDiMuonNoIPLODi',  
'HltLine/HltIDiMuonIPCL0Di',  
'HltLine/HltIDiMuonNoIP2L0',  
'HltLine/HltIDiMuonNoIPLOGEC',  
'HltLine/HltIDiMuonIPC2L0',  
'HltLine/HltIDiMuonIPCL0GEC',  
'HltLine/HltIDiMuonNoIPLOSeg',  
'HltLine/HltIDiMuonNoIPGECSeg',  
'HltLine/HltIDiMuonIPCL0Seg',  
'HltLine/HltIDiMuonIPCGECSeg',  
'HltLine/HltIMuTrack',  
'HltLine/HltIMuTrack4JPsi',

'HltLine/HltISingleElectron',  
'HltLine/HltISingleElectronFromPi0',  
'HltLine/HltIElectronTrackWithIP',  
'HltLine/HltIElectronFromPi0TrackWithIP',  
'HltLine/HltIElectronTrackNoIP',  
'HltLine/HltIElectronFromPi0TrackNoIP',

'HltLine/HltIPhoton',  
'HltLine/HltIPhotonFromEle',

'HltLine/HltIIgnoringLumi',  
'HltLine/HltILumi',  
'HltLine/HltIGlobal']

>>>

HA

MU

EL

PH

Autogenerated

```
>>> listAlgorithms(0x803b0000)
```

```
Hlt
Hlt1
  Hlt1Physics
    Hlt1PhysicsPreScaler
    Hlt1PhysicsODINFilter
    Hlt1PhysicsPostScaler
  Hlt1Random
    Hlt1RandomPreScaler
    Hlt1RandomODINFilter
    Hlt1RandomPostScaler
  Hlt1Incident
    Hlt1IncidentPreScaler
    Hlt1IncidentODINFilter
    Hlt1IncidentFilterSequence
      HltIncidentFilter
    Hlt1IncidentPostScaler
  Hlt1L0Muon
    Hlt1L0MuonPreScaler
    Hlt1L0MuonL0DUFILTER
    Hlt1L0MuonFilterSequence
      Hlt1L0MuonDecision
    Hlt1L0MuonPostScaler
  Hlt1L0DiMuon
    Hlt1L0DiMuonPreScaler
    Hlt1L0DiMuonL0DUFILTER
    Hlt1L0DiMuonFilterSequence
      Hlt1L0DiMuonDecision
    Hlt1L0DiMuonPostScaler
  Hlt1L0MuonNoGlob
    Hlt1L0MuonNoGlobPreScaler
    Hlt1L0MuonNoGlobL0DUFILTER
    Hlt1L0MuonNoGlobFilterSequence
      Hlt1L0MuonNoGlobDecision
    Hlt1L0MuonNoGlobPostScaler
  Hlt1L0Electron
    Hlt1L0ElectronPreScaler
    Hlt1L0ElectronL0DUFILTER
    Hlt1L0ElectronFilterSequence
      Hlt1L0ElectronDecision
    Hlt1L0ElectronPostScaler
  Hlt1L0Photon
    Hlt1L0PhotonPreScaler
    Hlt1L0PhotonL0DUFILTER
    Hlt1L0PhotonFilterSequence
      Hlt1L0PhotonDecision
    Hlt1L0PhotonPostScaler
  Hlt1L0Hadron
    Hlt1L0HadronPreScaler
    Hlt1L0HadronL0DUFILTER
    Hlt1L0HadronFilterSequence
      Hlt1L0HadronDecision
    Hlt1L0HadronPostScaler
  Hlt1L0LocalPi0
    Hlt1L0LocalPi0PreScaler
    Hlt1L0LocalPi0L0DUFILTER
    Hlt1L0LocalPi0FilterSequence
      Hlt1L0LocalPi0Decision
    Hlt1L0LocalPi0PostScaler
  Hlt1L0GlobalPi0
    Hlt1L0GlobalPi0PreScaler
    Hlt1L0GlobalPi0L0DUFILTER
    Hlt1L0GlobalPi0FilterSequence
      Hlt1L0GlobalPi0Decision
    Hlt1L0GlobalPi0PostScaler
  Hlt1SingleHadron
    Hlt1SingleHadronPreScaler
    Hlt1SingleHadronL0DUFILTER
    Hlt1SingleHadronFilterSequence
      Hlt1L0AllHadronCandidates
      Hlt1SingleHadronTFL0Hadrons
      Hlt1RecoRZVeloSequence
        HltRecoRZVeloSequence
        HltRecoRZVeloTracksSequence
        HltRecoRZVelo
      HltRecoRZPVSequence
        HltRecoPV2D
      Hlt1PrepareRZVelo
        Hlt1PreparePV2D
      Hlt1SingleHadronTFRZVelo
      Hlt1SingleHadronTUVelo
      Hlt1SingleHadronTF1Velo
      Hlt1SingleHadronTF2Velo
      Hlt1SingleHadronTMVeloCalo
```

01f182dae96749445aa469a8f032dd70  
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eda84958cf9d19fe5a01f339d1731c  
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9b7f13ee778b7319e97f6719d87e65de  
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Hlt1ElectronTrackWithIP	
Hlt1ElectronTrackWithIPPreScaler	(*)
Hlt1ElectronTrackWithIPL0DUFILTER	
Hlt1ElectronTrackWithIPFilterSequence	
Hlt1ElectronTrackWithIPTFL0Electrons	
Hlt1ElectronTrackWithIPTUTConf	
Hlt1ElectronTrackWithIPTFRZVelo	
Hlt1ElectronTrackWithIPTUVelo	
Hlt1ElectronTrackWithIPTMVeloT	
Hlt1ElectronTrackWithIPTFVeloT	
Hlt1ElectronTrackWithIPTFCompanionVelo	
Hlt1ElectronTrackWithIPTUCompanionForward	
Hlt1ElectronTrackWithIPTFCompanionForward	
Hlt1ElectronTrackWithIPVM2DiElectron	
Hlt1ElectronTrackWithIPVFVertexCut	
Hlt1ElectronTrackWithIPPostScaler	
Hlt1ElectronFromPi0TrackWithIP	
Hlt1ElectronFromPi0TrackWithIPPreScaler	(*)
Hlt1ElectronFromPi0TrackWithIPL0DUFILTER	
Hlt1ElectronFromPi0TrackWithIPFilterSequence	
Hlt1ElectronFromPi0TrackWithIPTFL0Electrons	
Hlt1ElectronFromPi0TrackWithIPTUTConf	
Hlt1ElectronFromPi0TrackWithIPTFRZVelo	
Hlt1ElectronFromPi0TrackWithIPTUVelo	
Hlt1ElectronFromPi0TrackWithIPTMVeloT	
Hlt1ElectronFromPi0TrackWithIPTFVeloT	
Hlt1ElectronFromPi0TrackWithIPTFCompanionVelo	
Hlt1ElectronFromPi0TrackWithIPTUCompanionForward	
Hlt1ElectronFromPi0TrackWithIPTFCompanionForward	
Hlt1ElectronFromPi0TrackWithIPVM2DiElectron	
Hlt1ElectronFromPi0TrackWithIPVFVertexCut	
Hlt1ElectronFromPi0TrackWithIPPostScaler	

cead6f7e56211651e369e0	
3898e4b3f80a4454ebc40a	
a6febdf2dbc02585e1b4b7	
4f5b84324604e22078f87d	
e7b69d8b0224f81f278a2a	
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8902b3e5b7362854ebcalb	
7ca16327afeb425751ea82	
36f3ba78f932775a3d0999	
d29c468c7f1722b63f7bb9	
1ea8fa57b0c8400449a866	
bf816635c3adba07f34ac3	
b3561d4d235928e36e1d15	
e7665ebbc2b09485992619	
e763b94a6870601c4686d9	
e2a1e12b027280f31ba8fe	
e6e27bc2b255629c0ecbcd	
7900220d71d1f0ca1446c7	
bd95d1fc26b3260f3b19b2	
023db24666b98c81e544e2	
8075051512c27085a76d69	
5a5d95a11d76ace1570a7c	
1becbab9b3c67d627cecd8	
d017638e1c33aa37412aa9	
a8c47440e7e653cd6fb58	
5b4889788304ded8d3f93b	
fed45a23a942f6bdfa58a0	
f1fb22268579859871abc6	
7e6ee64f269181e65b6f68	
495f950f13abf367ca7772	
1acf0394fdab6cb59d1ee1	

(\*) Where did Hlt1L0{LocalPi0,Electron}Decision, Hlt1RecoRZVeloSequence go?

see <https://savannah.cern.ch/bugs/?46919>

- `listProperties(0x803b0000,'HltI Global')`
  - returns all properties of those algorithms whose name matches the regex ‘HltI Global’
- `listProperties(0x803b0000,'HltI','Code|FilterDesc')`
  - returns the properties whose name match ‘Code|FilterDesc’ for the algorithms which match the regex HltI

```
>>> listProperties(0x803b0000,'Hlt1ElectronFromPi0TrackWithIP','Code|FilterDesc|AcceptFraction')

Requested Properties for DeterministicPrescaler/Hlt1ElectronFromPi0TrackWithIPPreScaler
'AcceptFraction':1

Requested Properties for LoKi::L0Filter/Hlt1ElectronFromPi0TrackWithIPL0DUFILTER
'Code':L0_CHANNEL('LocalPi0')

Requested Properties for HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFL0Electrons
'FilterDescriptor':[ 'L0ET,>,2600.' ]

Requested Properties for HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFRZVelo
'FilterDescriptor':[ 'RZVeloTMatch_Hlt1ElectronFromPi0TrackWithIPTUTConf,||<,60.' ]

Requested Properties for HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFVeloT
'FilterDescriptor':[ 'IP_PV2D,||[],0.1,3.' ]

Requested Properties for HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFCompanionVelo
'FilterDescriptor':[ 'IP_PV2D,||[],0.1,3.' , 'DOCA_Hlt1ElectronFromPi0TrackWithIPTFVeloT,<,0.2' ]

Requested Properties for HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFCompanionForward
'FilterDescriptor':[ 'PT,>,1000.0' ]

Requested Properties for HltVertexMaker2/Hlt1ElectronFromPi0TrackWithIPVM2DiElectron
'FilterDescriptor':[ 'DOCA,<,0.2' ]

Requested Properties for HltVertexFilter/Hlt1ElectronFromPi0TrackWithIPVFVertexCut
'FilterDescriptor':[ 'VertexPointing_PV2D,<,0.5' , 'VertexDz_PV2D,>,0.' ]

Requested Properties for DeterministicPrescaler/Hlt1ElectronFromPi0TrackWithIPPostScaler
'AcceptFraction':1
```

```
>>> listProperties(0x803b0000, 'Hlt1ElectronTrackWithIPFilterSequence', 'Members')
```

    Requested Properties for GaudiSequencer/Hlt1ElectronTrackWithIPFilterSequence  
    'Members': [ 'HltL0CaloCandidates/Hlt1L0ElectronDecision' ,  
    'HltTrackFilter/Hlt1ElectronTrackWithIPTFL0Electrons' ,  
    'HltTrackUpgrade/Hlt1ElectronTrackWithIPTUTConf' ,  
    '[GaudiSequencer/Hlt1RecoRZVeloSequence](#)' ,  
    'HltTrackFilter/Hlt1ElectronTrackWithIPTFRZVelo' ,  
    'HltTrackUpgrade/Hlt1ElectronTrackWithIPTUVelo' ,  
    'HltTrackMatch/Hlt1ElectronTrackWithIPTMVeloT' ,  
    'HltTrackFilter/Hlt1ElectronTrackWithIPTFVeloT' ,  
    '[HltTrackUpgrade/Hlt1RecoVelo](#)' ,  
    'HltTrackFilter/Hlt1ElectronTrackWithIPTFCompanionVelo' ,  
    'HltTrackUpgrade/Hlt1ElectronTrackWithIPTUCompanionForward' ,  
    'HltTrackFilter/Hlt1ElectronTrackWithIPTFCompanionForward' ,  
    'HltVertexMaker2/Hlt1ElectronTrackWithIPVM2DiElectron' ,  
    'HltVertexFilter/Hlt1ElectronTrackWithIPVFVertexCut' ]

```
>>> listProperties(0x803b0000, 'Hlt1ElectronFromPi0TrackWithIPFilterSequence', 'Members')
```

    Requested Properties for GaudiSequencer/Hlt1ElectronFromPi0TrackWithIPFilterSequence  
    'Members': [ 'HltL0CaloCandidates/Hlt1L0LocalPi0Decision' ,  
    'HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFL0Electrons' ,  
    'HltTrackUpgrade/Hlt1ElectronFromPi0TrackWithIPTUTConf' ,  
    '[GaudiSequencer/Hlt1RecoRZVeloSequence](#)' ,  
    'HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFRZVelo' ,  
    'HltTrackUpgrade/Hlt1ElectronFromPi0TrackWithIPTUVelo' ,  
    'HltTrackMatch/Hlt1ElectronFromPi0TrackWithIPTMVeloT' ,  
    'HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFVeloT' ,  
    '[HltTrackUpgrade/Hlt1RecoVelo](#)' ,  
    'HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFCompanionVelo' ,  
    'HltTrackUpgrade/Hlt1ElectronFromPi0TrackWithIPTUCompanionForward' ,  
    'HltTrackFilter/Hlt1ElectronFromPi0TrackWithIPTFCompanionForward' ,  
    'HltVertexMaker2/Hlt1ElectronFromPi0TrackWithIPVM2DiElectron' ,  
    'HltVertexFilter/Hlt1ElectronFromPi0TrackWithIPVFVertexCut' ]

```
>>>
```

- `getRoutingBits(0x803b0000)` / `listRoutingBits(0x803b0000)`
  - returns / list a (python) list of Routing Bit definitions, given either a TCK or a configID

```
>>> listRoutingBits(0x801b0000) ← Moore v5r5 configuration
{32: 'Hlt|Global', 33: 'Hlt|LumiDecision', 34: 'Hlt|PhysicsDecision|Hlt|RandomDecision|Hlt|VeloASideDecision|Hlt|VeloCSideDecision|
Hlt|SingleMuonNoIPDecision|Hlt|SingleMuonPandPTDecision|Hlt|DiMuonFromL0DiMuNoIPDecision|
Hlt|DiMuonFromL0DiMuonIPDecision|Hlt|DiMuonFrom2L0IPDecision|Hlt|DiMuonFrom2L0NoIPDecision|
Hlt|DiMuonFromMuonSegIPDecision|Hlt|DiMuonFromMuonSegNoIPDecision|Hlt|MuonHadronDecision|Hlt|SingleHadronDecision|
Hlt|DiHadronDecision|Hlt|PhotonDecision|Hlt|SingleElectronDecision|Hlt|ElectronTrackDecision', 35: 'Hlt|VeloCSideDecision|
Hlt|VeloASideDecision', 36: 'Hlt|RandomDecision', 37: 'Hlt|PhysicsDecision'}
```

>>>

>>>

```
>>> listRoutingBits(0x803b0000) ← Moore v6r1 configuration
{32: "HLT_PASS('Hlt|Global')", 33: "HLT_PASS('Hlt|LumiDecision')", 34: "HLT_PASS('Hlt|IgnoringLumiDecision')", 35:
"HLT_PASS('Hlt|VeloClosingDecision')", 36: "HLT_PASS('Hlt|XPressDecision')"}
```

>>>

**TODO** : make `HLT_PASS` and `HLT_PASSIGNORING` accept wildcards, so we can write the above as:

```
{32: "HLT_PASS('Hlt|Global')", 33: "HLT_PASS('Hlt|Lumi.*Decision')", 34: "HLT_PASSIGNORING('Hlt|Lumi.*Decision')", 35:
"HLT_PASS('Hlt|Velo.*Decision')", 36: "HLT_PASS('Hlt|XPressDecision')"}
```

which will get rid of the need to create dedicated lines which are the ‘or’ of others (and thus introduce ordering constraints!) just to keep this legible...

- `diff(0x80350000,0x803b0000)`
  - shows items only in left, only in right, and differences of items in both
  - (*still!*) *TODO*: *should* be used internally before allowing the addition of a configuration to an existing hltType -- if ‘only in’ appears, should be forbidden (as it implies a change in the ‘flow’)
- `updateProperties( ... )`
  - allows to copy-and-modify existing configurations (eg. change prescales)
- `createTCKentries( { ..:..,..... } )`
  - creates entries in mapping from TCK to configID
  - eg. update to new L0 TCK, but keep same HLT config

# The configuration mistakes fixed during the 2nd FEST week...

```
>>> diff(0x80350000,0x803b0000)
--- DeterministicPrescaler/Hlt1VeloASidePreScaler (IAlgorithm) d6d84499f22b9d49b170690ed272be40
+++ DeterministicPrescaler/Hlt1VeloASidePreScaler (IAlgorithm) 7843022e9c4f5b88678b790f7318b3a0
@@ -28,1 +28,1 @@
-'AcceptFraction':0.0001
+'AcceptFraction':1

--- DeterministicPrescaler/Hlt1VeloCSidePreScaler (IAlgorithm) d6d84499f22b9d49b170690ed272be40
+++ DeterministicPrescaler/Hlt1VeloCSidePreScaler (IAlgorithm) 7843022e9c4f5b88678b790f7318b3a0
@@ -28,1 +28,1 @@
-'AcceptFraction':0.0001
+'AcceptFraction':1

--- DeterministicPrescaler/LumiStripperPrescaler (IAlgorithm) d6d84499f22b9d49b170690ed272be40
+++ DeterministicPrescaler/LumiStripperPrescaler (IAlgorithm) 7843022e9c4f5b88678b790f7318b3a0
@@ -28,1 +28,1 @@
-'AcceptFraction':0
+'AcceptFraction':0.9999

--- HltRoutingBitsWriter/HltRoutingBitsWriter (IAlgorithm) d6d84499f22b9d49b170690ed272be40
+++ HltRoutingBitsWriter/HltRoutingBitsWriter (IAlgorithm) 7843022e9c4f5b88678b790f7318b3a0
@@ -16,1 +16,1 @@
-'RoutingBits':{ 32 : "HLT_PASS('Hlt1Global')", 33 : "HLT_PASS('Hlt1LumiDecision')", 34 :
"HLT_PASS('Hlt1IgnoringLumiDecision')", 35 : "HLT_PASS('Hlt1VeloClosingDecision')", 36 :
"HLT_PASS('Hlt1ExpressDecision')"}
+'RoutingBits':{ 32 : "HLT_PASS('Hlt1Global')", 33 : "HLT_PASS('Hlt1LumiDecision')", 34 :
"HLT_PASS('Hlt1IgnoringLumiDecision')", 35 : "HLT_PASS('Hlt1VeloClosingDecision')", 36 :
"HLT_PASS('Hlt1XPressDecision')"}

>>>
```

First three now solved by overruling HltConf defaults in dedicated 'FEST' python file in Moore

# Other items (still) on the list...

- move online Moore setup over to start Moore as python, not .opts....
  - more and more options files are disappearing, and being replaced by .py
  - eg. \$STDOPTS/DecodeRawEvent.opts
  - Need to remove any dependencies on ‘external’ .opts
  - have an example from lumi to follow
- verify independence of HltLines
  - run ‘everything together’, record result for each line, then run ‘one line at a time’ job, compare output, fix discrepancies
    - can now run ‘one at a time’ easily, but need to automate *which* one to run (and loop over them)..
- Move Hlt2 to use HltLine, and additional pythonization of Hlt2
- Allow slightly selective disable histogramming

# Other items on the list...

- Switch to use DB snapshots (ready in principle, but needs testing)
- Conditions updates at fast-run change
- verify TCK changes at fast-run:
  - not all algorithms support updating of their properties after initialize
    - missing appropriate updateHandler, or (complex cases only) explicit ‘restart’
  - verify by using the ‘wrong’ TCK during initialize, causes restart on first event, compare with using the ‘right’ TCK during init
  - need to ‘sign off’ / whitelist those that are known to work
    - DeterministicPrescaler probably the one we need first, it is known to work properly ;-)
    - LoKi based filters also OK (used as ODIN filter, L0DU filter and HltDecReports filter in HltI)
    - The above probably cover virtually everything really needed in practice...