



Contribution ID: 148

Type: **Poster**

## D4: Grid on QPACE 4

*Wednesday 28 July 2021 15:40 (20 minutes)*

In 2020 we deployed QPACE 4, which features 64 Fujitsu A64FX model FX700 CPUs interconnected by Infini-Band EDR. QPACE 4 runs an open-source software stack. For LQCD simulations we ported the Grid LQCD framework to support the Arm Scalable Vector Extension (SVE). In this contribution we discuss our SVE port of Grid, the status of SVE compilers and the performance of Grid. We also present the benefits of an alternative data layout of complex numbers for the Domain Wall operator.

**Authors:** MEYER, Nils (University of Regensburg); GEORG, Peter (University of Regensburg); SOLBRIG, Stefan (University of Regensburg); WETTIG, Tilo (University of Regensburg)

**Presenter:** MEYER, Nils (University of Regensburg)

**Session Classification:** Poster

**Track Classification:** Software development and Machines