



# TWEPP 2021 Topical Workshop on Electronics for Particle Physics

## Thursday, September 23, 2021

### Programmable Logic, Design Tools and Methods (12:20 PM - 1:00 PM)

-Conveners: Johan Alme

time	[id] title	presenter
12:20 PM	[42] Hog: handling HDL repositories on git	CIERI, Davide
12:40 PM	[109] PAM-4 implementation study for future high-speed links	WANOTAYAROJ, Chaowaroj

### Programmable Logic, Design Tools and Methods (2:00 PM - 3:00 PM)

-Conveners: Johan Alme

time	[id] title	presenter
2:00 PM	[18] Hough-transform-based FPGA track processor for the ATLAS experiment at CERN	XU, Tong
2:20 PM	[32] Automated firmware generation and continuous testing for the CMS HGCAL trigger primitive generator	SAUVAN, Jean-Baptiste
2:40 PM	[12] FPGA implementation of RDMA for ATLAS Readout with FELIX in High Luminosity LHC	VASILE, Matei