

TWEPP 2021 Topical Workshop on Electronics for Particle Physics

Thursday, 23 September 2021

Programmable Logic, Design Tools and Methods (12:20 - 13:00)

-Conveners: Johan Alme

time	[id] title	presenter
12:20	[42] Hog: handling HDL repositories on git	CIERI, Davide
12:40	[109] PAM-4 implementation study for future high-speed links	WANOTAYAROJ, Chaowaroj

Programmable Logic, Design Tools and Methods (14:00 - 15:00)

-Conveners: Johan Alme

time	[id] title	presenter
14:00	[18] Hough-transform-based FPGA track processor for the ATLAS experiment at CERN	XU, Tong
14:20	[32] Automated firmware generation and continuous testing for the CMS HGCal trigger primitive generator	SAUVAN, Jean-Baptiste
14:40	[12] FPGA implementation of RDMA for ATLAS Readout with FELIX in High Luminosity LHC	VASILE, Matei