

TWEPP 2021 Topical Workshop on Electronics for Particle Physics

Tuesday 21 September 2021

Posters ASIC (17:20 - 18:40)

| [id] title | presenter | board |
|--|---|-------|
| [9] Monolithic pixel sensor with 25 μ m x 35 μ m pixel size and high time resolution implemented in 180nm technology | ZHANG, Hui | |
| [19] 2 ps rms Jitter Analog PLL Running at 2.56 GHz in 130nm CMOS Technology | Mr MATHEZ, Herve | |
| [21] Radiation hardness of the ITkPixV1 and RD53A chips | MIRONOVA, Maria | |
| [87] Event driven readout system with non-priority arbitration for radiation detectors | GÓRNI, Dominik | |
| [100] Certification of the amplifier-shaper-discriminator ASICs produced for the ATLAS MDT chambers at the HL-LHC | KORTNER, Oliver | |
| [122] FastIC: A Fast Integrated Circuit for the Readout of High Performance Detectors | GOMEZ FERNANDEZ, Sergio | |
| [123] A Dual-mode NRZ/PAM4 Transmitter IP Purposed for Advanced CMOS Technology Nodes | GARDINER, Thomas | |
| [128] Verification methodology of a multi-mode radiation-hard high-speed transceiver ASIC | Mr PULLI, Adithya | |
| [136] QTIA, a 2.5 or 10 Gbps 4-Channel Array Optical Receiver ASIC in a 65 nm CMOS Technology | Ms SUN, Hanhan | |
| [137] Development of a 20 Gbps PAM4 Data Transmitter ASIC for Particle Physics Experiments | Ms ZHANG, Li | |
| [138] A radiation-tolerant clock generator for the CMS Endcap Timing Layer readout chip | GONG, Datao | |
| [149] Low Noise Charge Sensitive Amplifier - ASIC with Adaptive Gain Setting and Active Reset | WIECZOREK, Peter | |
| [167] A low power clock generator 400-1800 MHz for ADPLL | IVANOV, Pavel NORMANOV, Dmitry | |
| [173] A Four-Channels Front End Electronics for ATLAS Muon-Drift-Tubes Detectors in 65nm CMOS Technology | SYED ALI SHAH, Adeel | |
| [157] DIAMASIC: A multichannel front-end electronics for high-accuracy time measurements for diamond detectors | Mr GHIMOUZ, Abderrahmane Mr RARBI, Fatah Ellah RARBI, Fatah Ellah | |
| [148] 2.56 Gbps CML transceiver for the data concentrator ASIC | SERAZETDINOV, Arthur | |
| [145] Onchip digital calibrated 2mW 12b SAR ADC with reduced input capacitance | HERNANDEZ HERRERA, Hugo | |
| [144] The Front End and Trigger Unit for an Analogue Transient Recorder ASIC | FLEMMING, Holger | |
| [143] A Family of Transient Recorder ASICs for Detector Readout | FLEMMING, Holger | |
| [124] Experimental characterisation of the RD50-MPW2 High Voltage-CMOS sensor chip | POWELL, Samuel | |

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| [15] CIC2: a radiation tolerant 65nm data aggregation ASIC for the future CMS tracking detector at LHC | VIRET, Sebastien | |
| [75] Towards a Software-Adaptable Receiver Chain for Particle Detectors | KRYSTOFIAK, Lukas RÖSSING, Florian | |