



# 3D n-on-n Detectors

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*O. Ferrer, G. Pellegrini, N. Moffat,  
G. Kramberger, M. Manna,  
D. Quirion, J. Villegas, S. Hidalgo*



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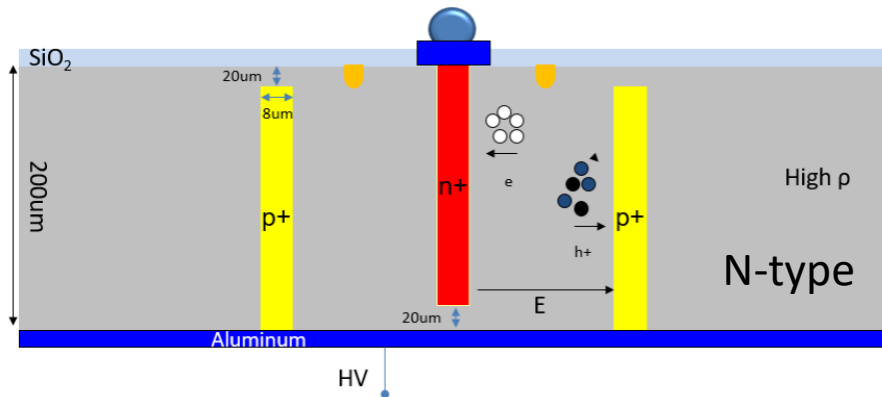


Jožef Stefan  
Institute  
Ljubljana, Slovenia

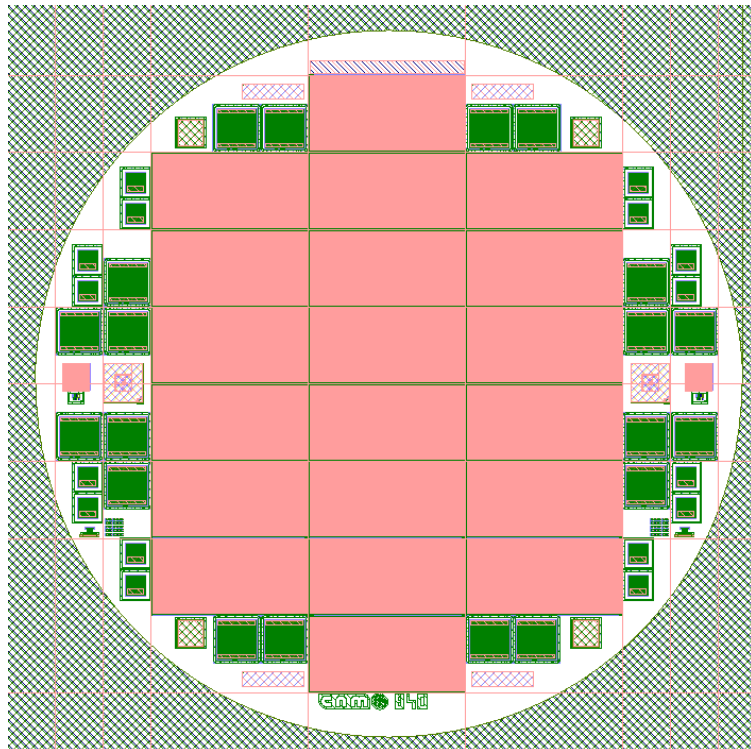
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## Outline

- Introduction
- I-V measurements
- TCT measurements
- Conclusions and future work



- 200µm-thick, high resistivity, n-type wafers
- 180µm-deep columns
- 8µm of diameter for columns



List of devices:

- 9 RD53 50x50µm<sup>2</sup> (1-x)
- 9 RD53 25x100µm<sup>2</sup> 2E (2-x)
- 2 RD53 25x100µm<sup>2</sup> 1E (3-x)
- 9 Diodes 50x50µm<sup>2</sup> (5-x)
- 16 Diodes (small pad) 50x50µm<sup>2</sup> (6-x)
- 6 Diodes 25x50µm (7-x)
- 6 Diodes 25x100µm<sup>2</sup> (8-x)
- 4 MOS (9-x)

N- type wafer makes p-n junction to happen in the backside → n+ column is shorted: High current expected

After irradiation, electron traps act as acceptors → substrate type inversion: normal current expected

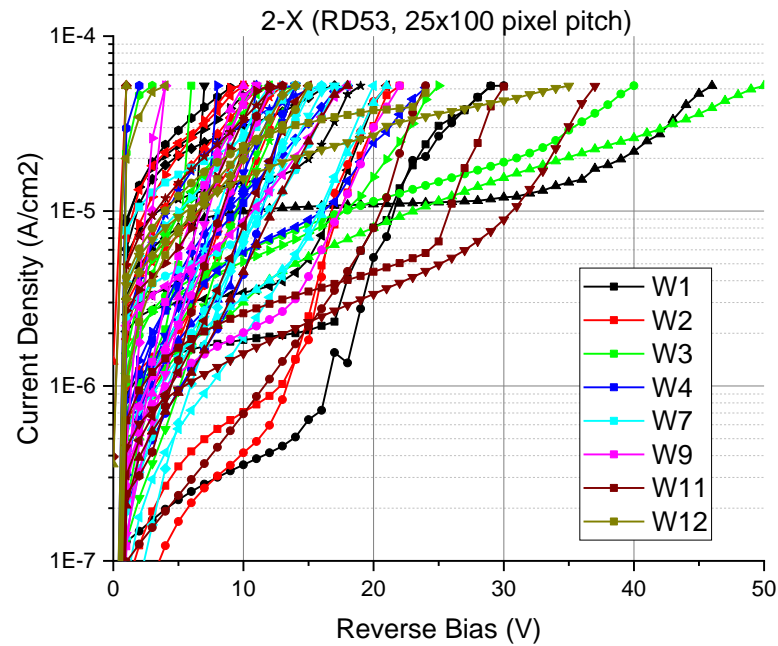
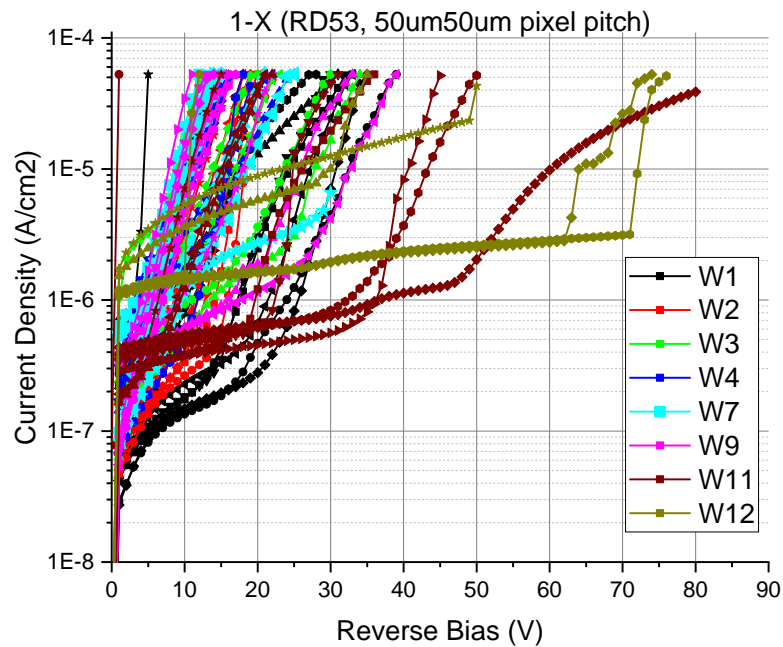
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## Irradiation

- Devices have been irradiated at Jožef Stefan Institute at the fluences of:  $1e14cm^{-2}$ ,  $1e15cm^{-2}$ ,  $5e15cm^{-2}$ ,  $1e16cm^{-2}$ ,  $5e16cm^{-2}$ ,  $1e17cm^{-2}$ .
- Devices chosen for irradiation:
  - RD53  $50\mu m \times 50\mu m$
  - RD53  $25\mu m \times 100\mu m$
  - Diodes  $50\mu m \times 50\mu m \rightarrow 50 \times 50$  pixel array
  - Diodes  $25\mu m \times 100\mu m \rightarrow 100 \times 100$  pixel array
  - Small  $50\mu m \times 50\mu m$  test structure diodes for timing measurements

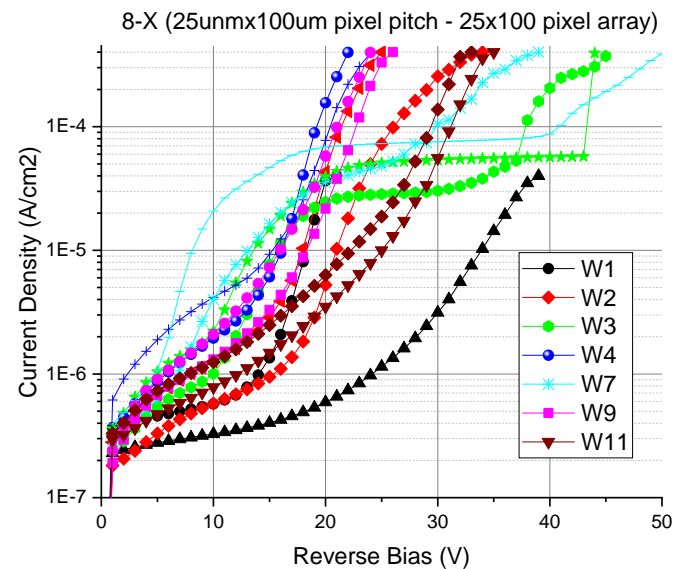
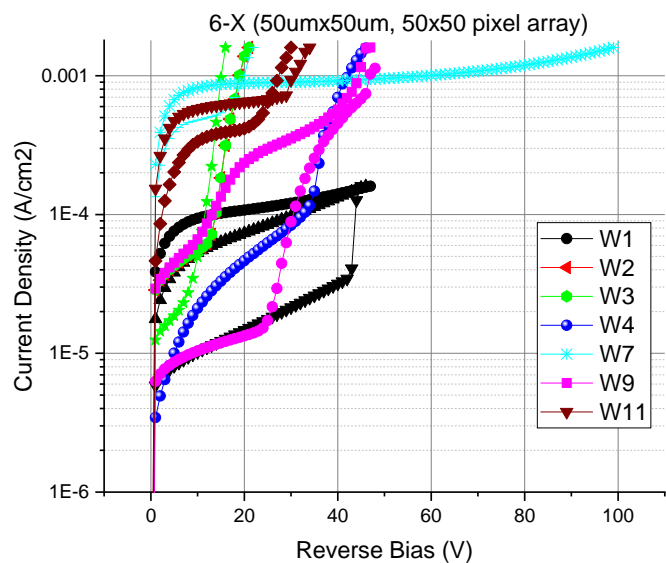
# I-V measurements

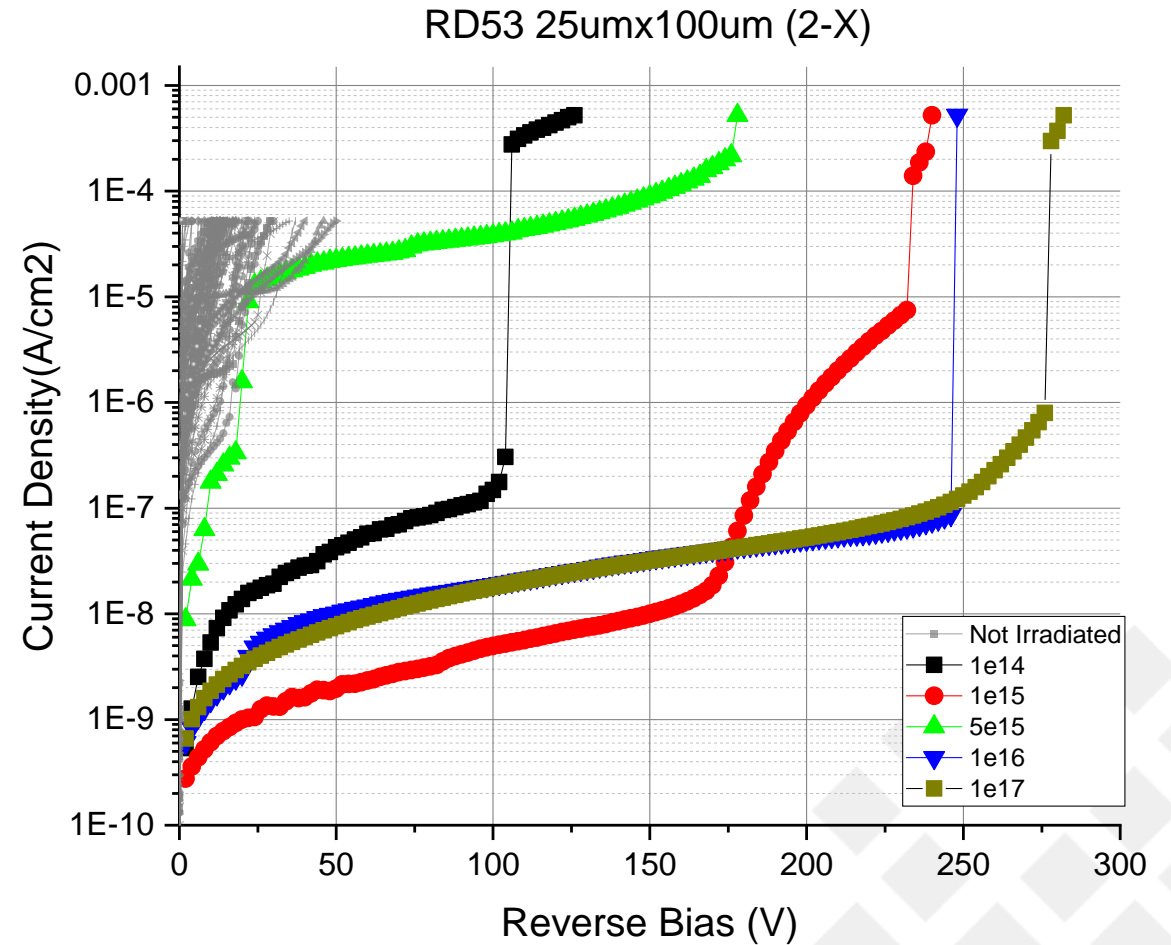
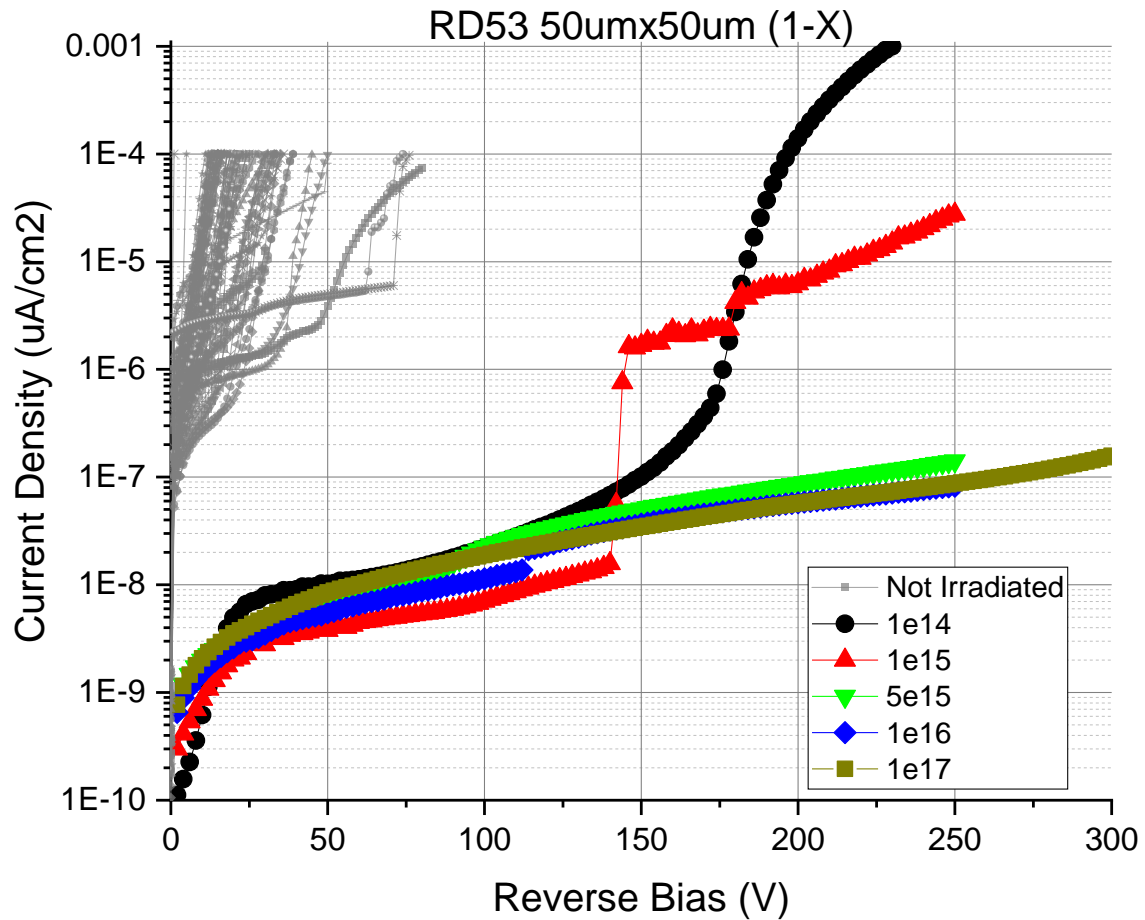
The background of the slide is a solid blue color. On the left side, there is a faint, semi-transparent image of a digital multimeter. On the right side, there is a faint, semi-transparent grid pattern consisting of light blue squares.

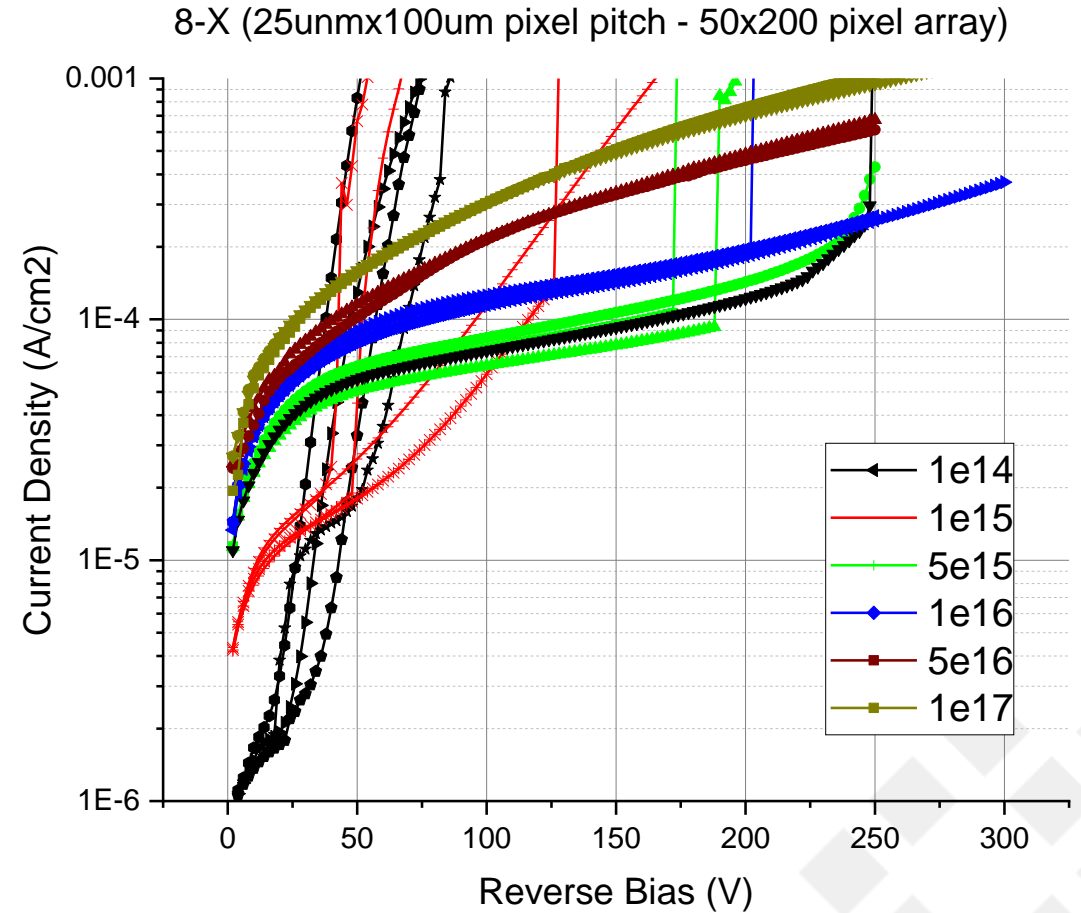
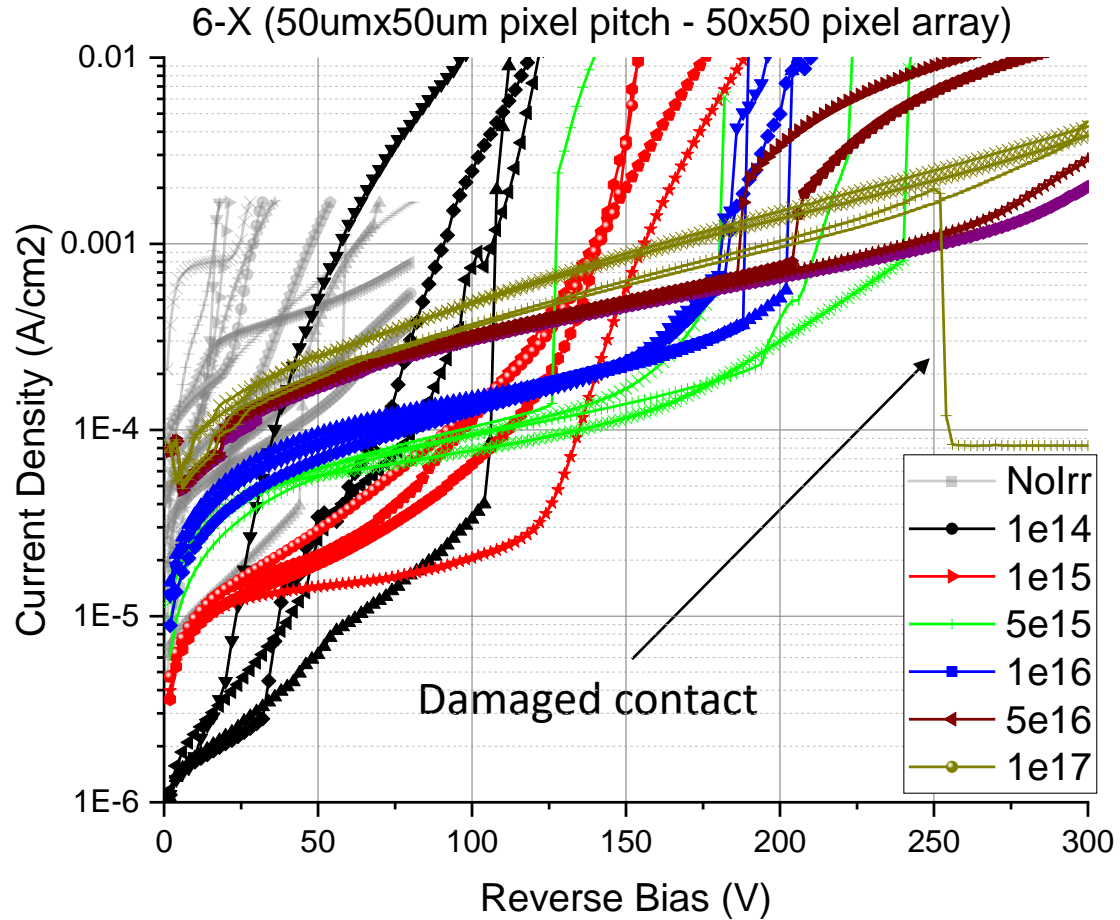


I-V measurements on wafer, T = 20°C

High leakage current and low Vbd



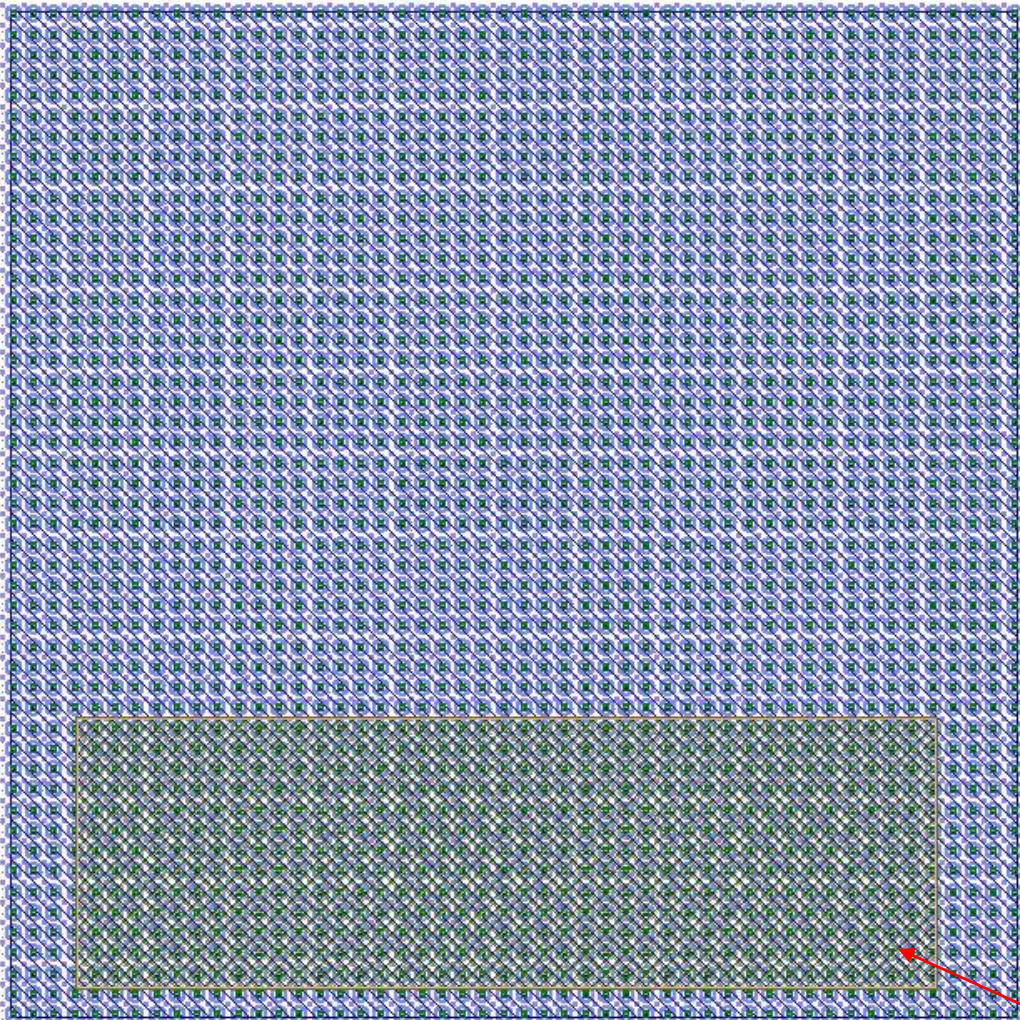






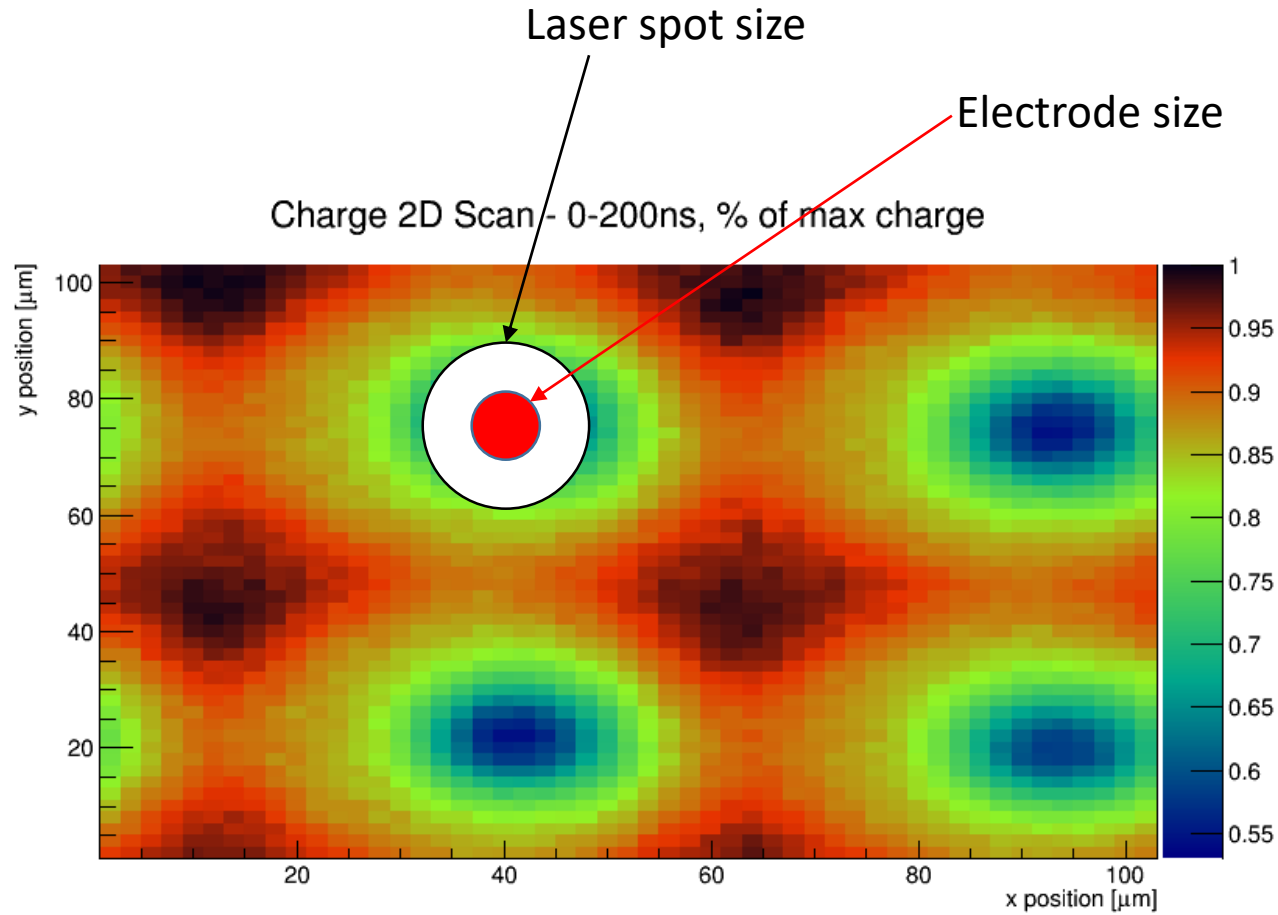
# TCT measurements

The background of the slide is a solid blue color. On the left side, there is a faint, semi-transparent image of a dental handpiece. On the right side, there is a faint, semi-transparent grid pattern of diamonds.

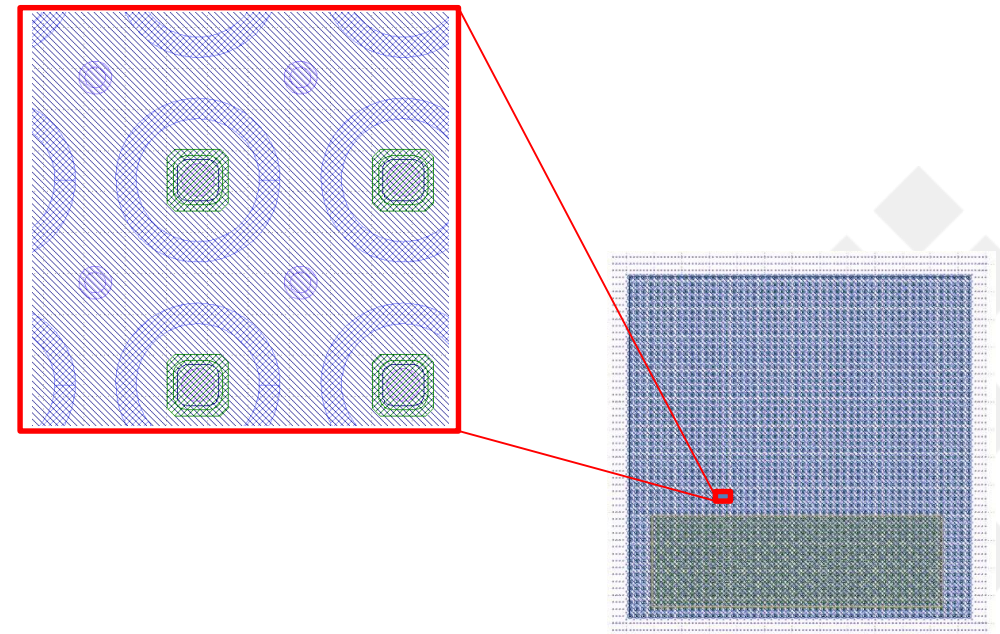


- 50x50 pixel array with 50µm x 50µm pixel pitch
- Diodes shorted with highly doped polysilicon on top
- Pad made of metal (for wire bonding)
- Reverse bias voltage = 20V
- T = 20°C
- Laser spot size ~30 µm

Metal pad

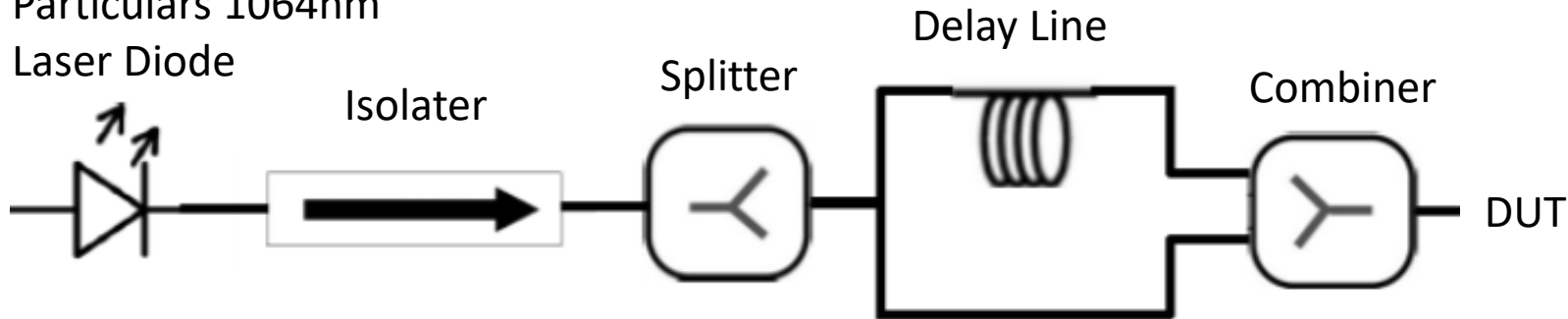


- On the electrodes, no charge should be collected
- Laser spot size  $>$  electrodes  $\rightarrow$  no region with no charge collection

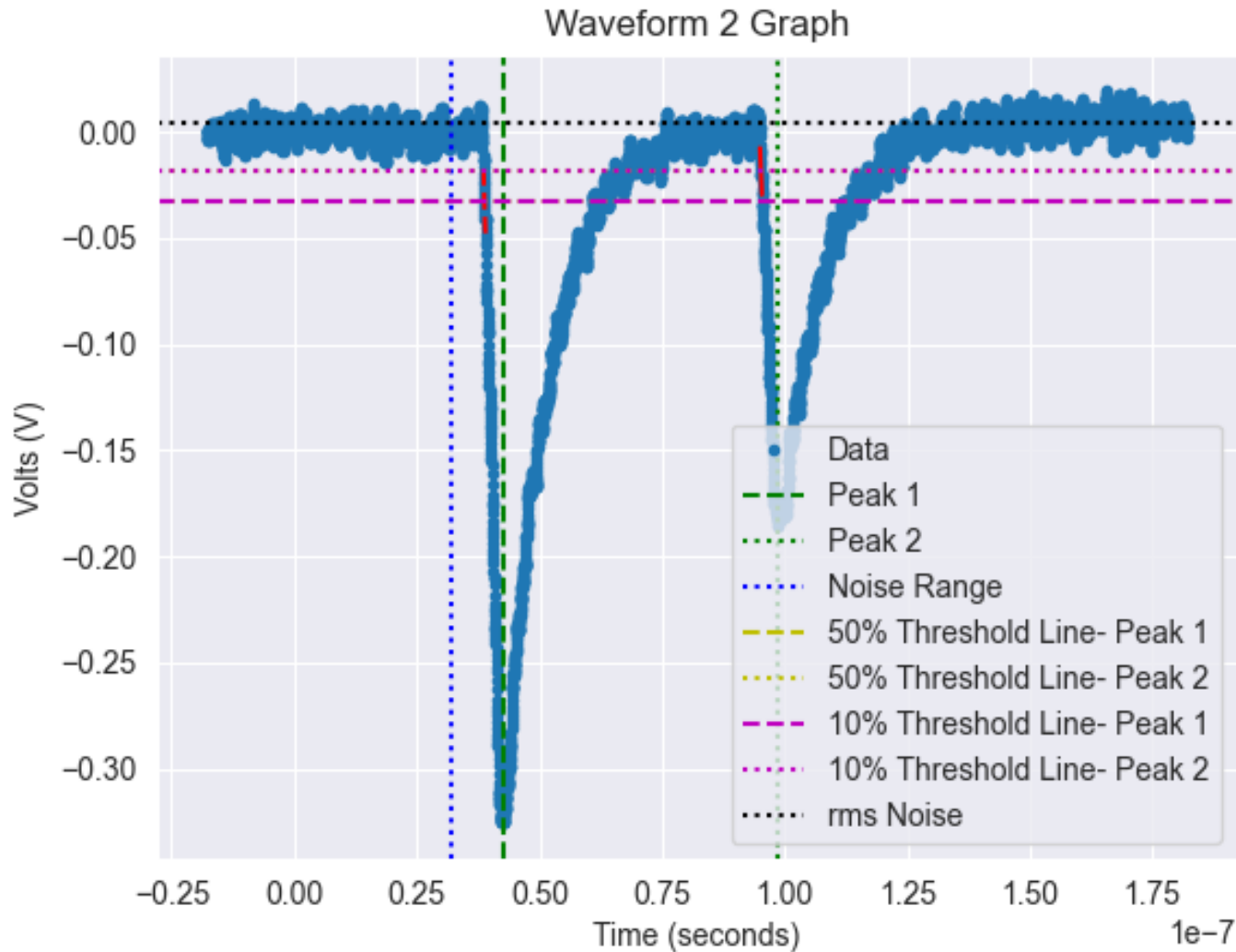


## Timing setup

Particulars 1064nm  
Laser Diode



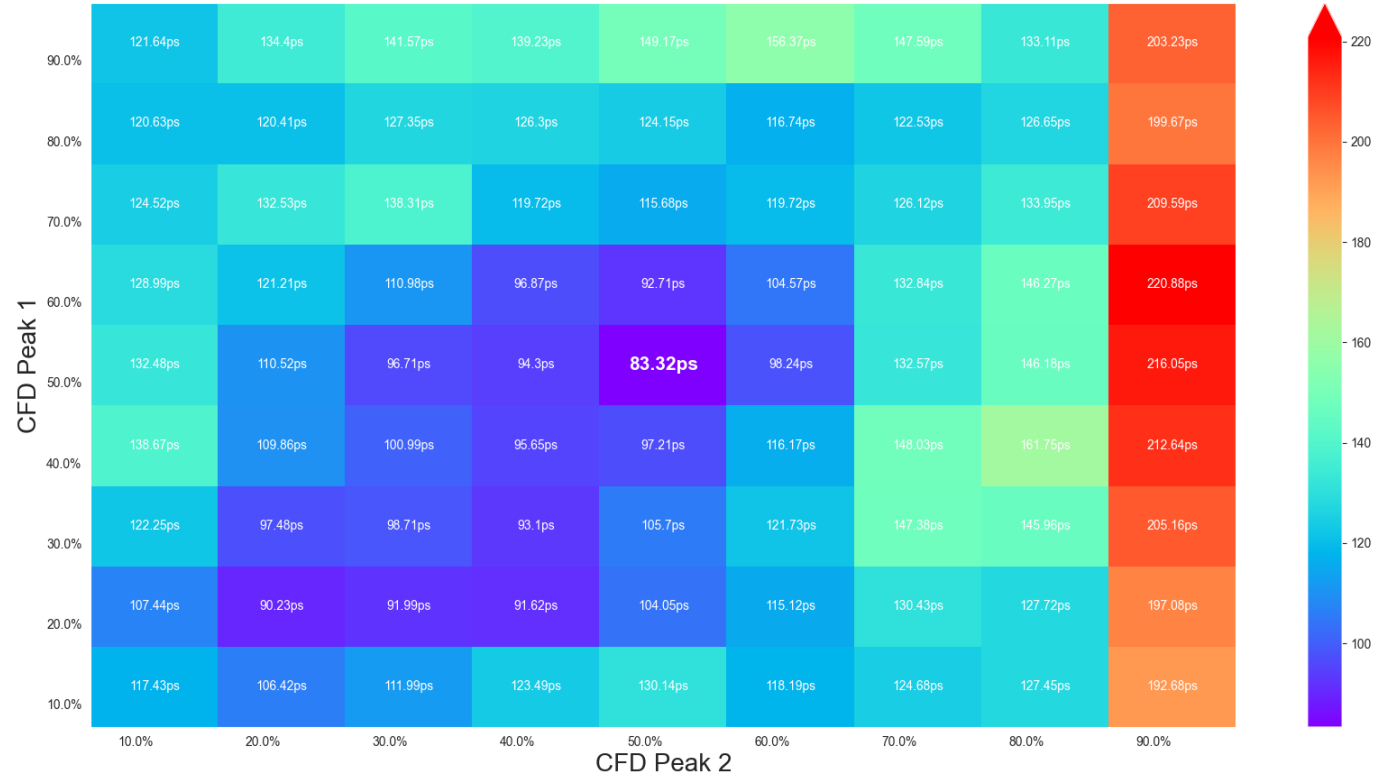
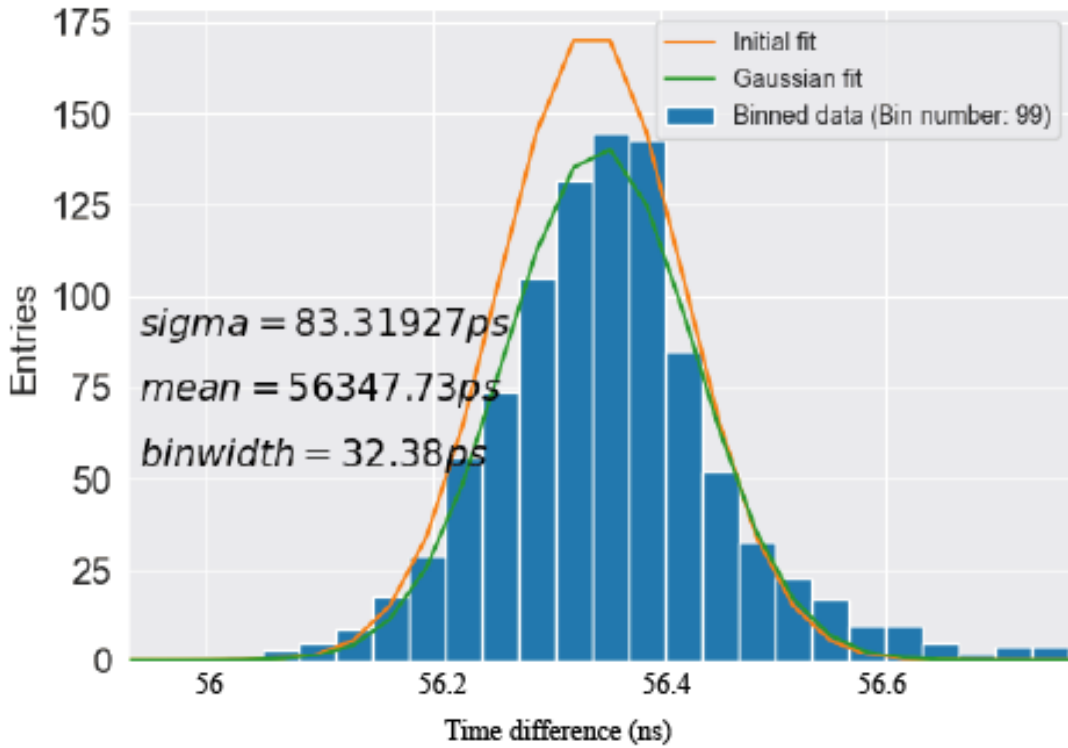
- IR laser Split into two paths
- One path, delays the laser pulse roughly 50ns. This pulse is used as the reference signal
- The second is not affected
- The two pulses are recombined and shined on the DUT



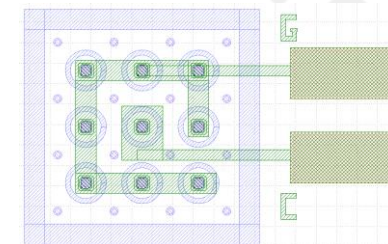
- 1<sup>st</sup> peak almost double the large as the 2<sup>nd</sup> → in future measurements, corrected with an attenuator
- First 1000 points used for evaluating noise and baseline correction.
- Peaks fitted with a 2 degree polynomial with 4 points
- Linear fit using 9 points for the CFD
- $T = 20^{\circ}\text{C}$
- Bias voltage = 20V

## Timing results

Time difference at CFD Frac of 50% of Peak 1 at 50% of Peak



- N-on-n 3D detectors have high leakage current and low  $V_{bd}$  before irradiation
- I-V measurements show that they can work after irradiations of fluences up to  $1e17N_{eq}/cm^{-2}$ .
- TCT measurements show potentially good CCE and timing performance, but better measurements have to be performed (attenuator, low temperature...)
- Irradiated devices are to be tested with the TCT.
- Smaller diode arrays (3x3) will be used for timing measurements (less noise due to capacitance). Also, TCT will be calibrated as a MIP.



# Thank you for your attention