Session Program

8–14 Jun 2011

TIPP 2011 - 2nd International Conference on Technology and Instrumentation in Particle Physics

Front-end Electronics

Chicago, Sheraton Hotel
301 East Water Street Chicago, IL 60611

Thursday 9 June

16:00

Front-end Electronics

Session | **Location:** Sheraton Hotel, Erie | **Conveners:** Hans-Günther Moser, Zheng Wang

16:00–16:30 **Recent developments of HEP pixel detector readout chips**

Speaker
Dr Lea Michaela Caminada

16:30–17:00 **Advanced pixel sensors and readout electronics based on 3D integration for the SuperB Silicon Vertex Tracker**

Speaker
Valerio Re

17:00–17:20 **Continuous Acquisition Pixel 12: Hexagonal Pixels in SOI Technology**

Speaker
Michael Cooney

17:20–17:40 **The TDCpix readout ASIC: a 75 ps resolution timing front-end for the Gigatracker of the NA62 experiment**

Speaker
Dr Gianluca Aglieri Rinella

17:40–18:00 **A High-speed Adaptively-biased Current-to-current Front-end for SSPM Arrays**

Speaker
Bob Zheng

18:00

Friday 10 June

14:00

Front-end Electronics

Session | **Location:** Sheraton Hotel, Erie | **Convener:** J.P. Walder

14:00–14:30

HIPPO, a Flexible Front-End Signal Processor for High-Speed Image Sensor Readout

Speaker
Dr Carl Grace

14:30–14:50

Deeper Sampling CMOS Transient Waveform Recording ASICs

Speaker
Prof. Gary Varner

14:50–15:10

A 4-Channel Waveform Sampling ASIC using 130nm CMOS technology

Speakers
Eric Oberla, Herve Grabas

15:10–15:30

SPACIROC: A Front-End Readout ASIC for JEM-EUSO cosmic ray observatory

Speaker
Mr Salleh Ahmad

15:30

16:00

Front-end Electronics

Session | **Location:** Sheraton Hotel, Erie | **Convener:** Yasuo Arai

16:00–16:30

Readout Electronics for the ATLAS LAr Calorimeter at HL-LHC

Speaker
Hucheng Chen

16:30–16:50

A correlation-based timing calibration and diagnostic technique for fast digitizing ASICs

Speaker
Dr Kurtis Nishimura

16:50–17:10

Multipurpose Test Structures and Process Characterization using 0.13µm CMOS: The CHAMP ASIC.

Speaker
Michael Cooney

17:10–17:30

Upgrade Design of TileCal Front-end Readout Electronics and Radiation Hardness Studies

Speaker
Fukun Tang

17:30

Saturday 11 June

08:30

Front-end Electronics

Session | **Location:** Sheraton Hotel, Erie | **Conveners:** Valerio Re, gustavo cancelo

08:30–09:00

A Gigabit transceiver for data transmission in future high energy physics experiments

Speaker
Dr Ken Wyllie

09:00–09:30

A Versatile Link for high-speed, radiation resistant optical transmission in LHC upgrades

Speaker
Dr Annie Xiang

09:30–09:50

RADIATION-HARD ASICS FOR OPTICAL DATA TRANSMISSION

Speaker
Dr Mike Strang

09:50–10:10

A high speed serializer ASIC for ATLAS Liquid Argon calorimeter upgrade

Speaker
Tiankuan Liu

10:10–10:30

Design and verification of an FPGA based bit error rate tester

Speaker
Dr Annie Xiang

10:30

14:00

Front-end Electronics

Session | **Location:** Sheraton Hotel, Erie | **Convener:** Carl Grace

14:00–14:30

The Front-end Electronics for the Daya Bay Reactor Neutrino Experiment

Speaker
Prof. Zheng Wang

14:30–14:50

EASIROC, an easy & versatile readout device for SiPM

Speaker
Mr Stéphane Callier

14:50–15:10

The cryogenic performances of specific optical and electrical components for a liquid argon time projection chamber

Speaker
Dr Tiankuan Liu

15:10–15:30

A concept for power cycling the electronics of CALICE-AHCAL with the train structure of ILC

Speaker
Dr Peter Goettlicher

15:30