

Brief update on the LHCb benchmark

- Discussed this with the LHCb Simulation WG on Feb 9th
 - After the Task Force talk on Feb 3rd (and a previous talk in LHCb on Jan 28th)
- The plan is what I presented to the TF – use MC Simulation (80-90% of LHCb Grid CPU usage)
 - I will provide a new LHCb container using single-threaded MC simulation (Gauss)
 - The LHCb Simulation WG will provide the final details
 - Version of the software (one of the latest Gauss releases that we are already using for Run3)
 - Version of the O/S (was SLC6, will be CC7) and of the compiler (one of the latest ones we use)
 - Using SIM only without 1st event (currently using GEN-SIM including 1st event overhead)
 - Representative event type to be chosen by the simulation WG
 - Architecture will be x86_64 (no production yet on ARM or Power)
 - The goal is to keep this stable for 3+ years during Run3
- Timescales?
 - Everyone (me, LHCb Simulation WG) busy with other things (e.g. vCHEP)
 - Once I get the final details from LHCb it should be relatively quick to prepare the container

(Not discussing here other issues beyond the production HEP-SCORE21, e.g. MT simulation, MJF etc..)