## Request to remove HW interlock



- o As MD device, the TECS had a dedicated BIC entry in LSS2: device had to be out for normal operation
- Moving to operational device, this was supposed to be reversed to ensure device in beam
- o The interlock only checks if crystal at the end switches (crystal could be almost out and interlock still be OK...)
- o HW interlock not possible for position or angle at the moment  $\rightarrow$  done via SIS
- o No risk for the machine or ZS (see previous talk at the MPP)
- Propose to completely remove the HW interlock and rely only on SIS for the moment
- o For the future, investigate possibility to HW interlock on position and angle with sufficient accuracy for smooth and safe operation

4D> 4A> 4B> 4B> B 990

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