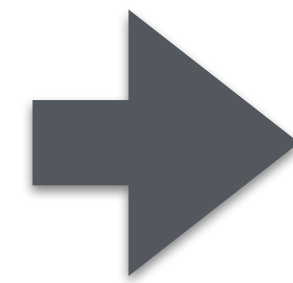
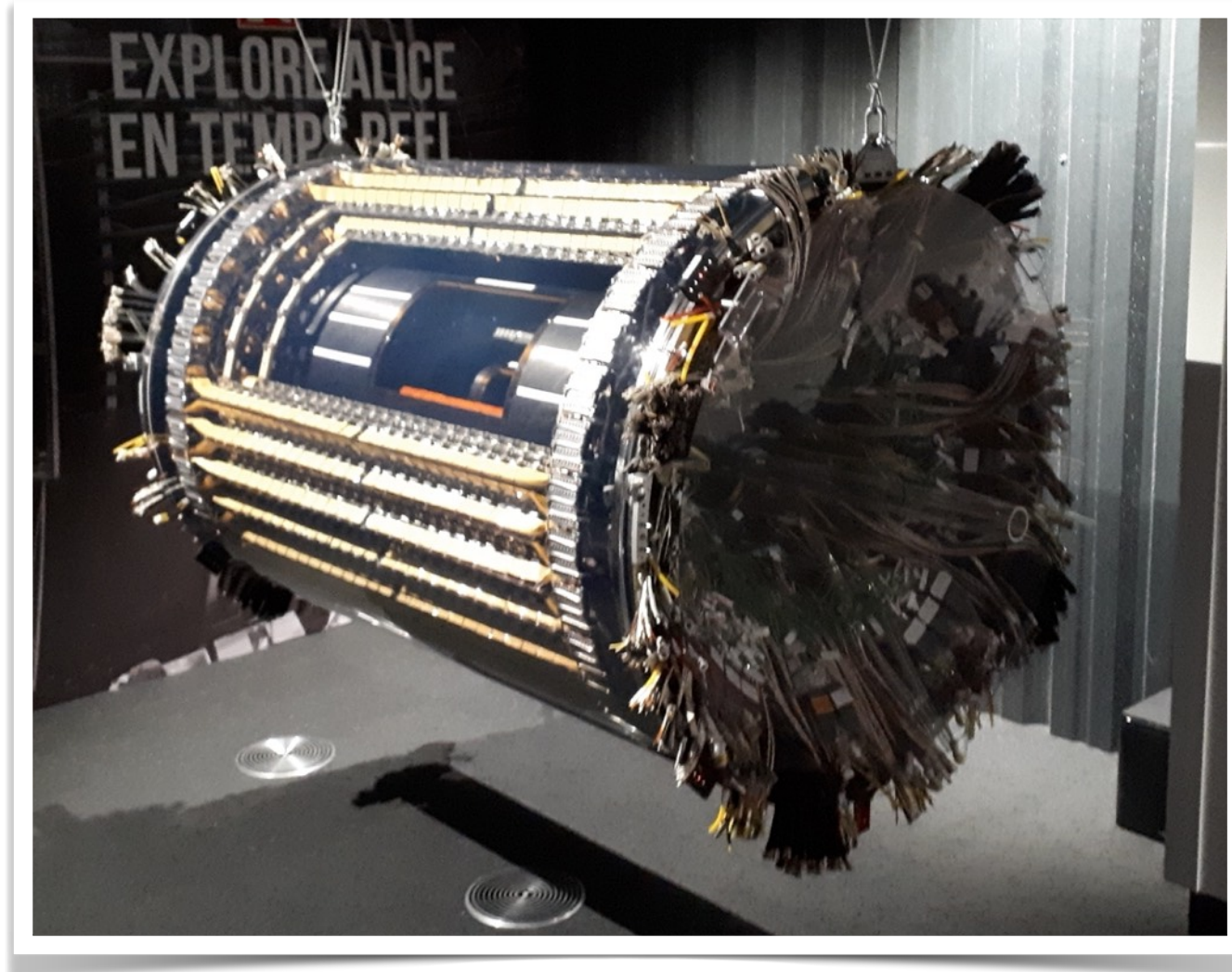


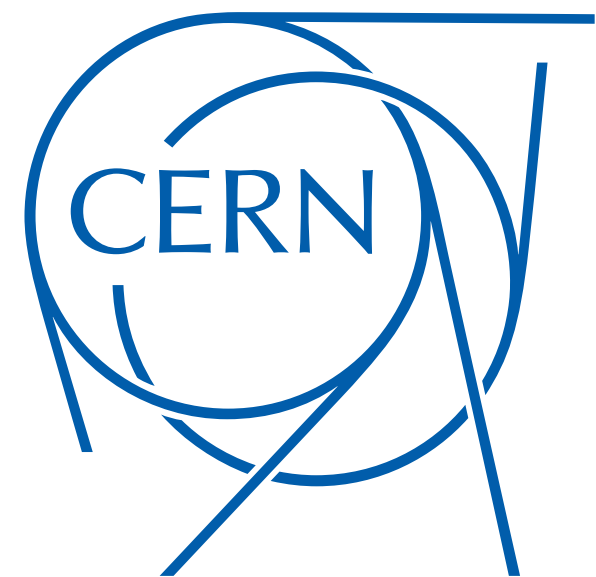
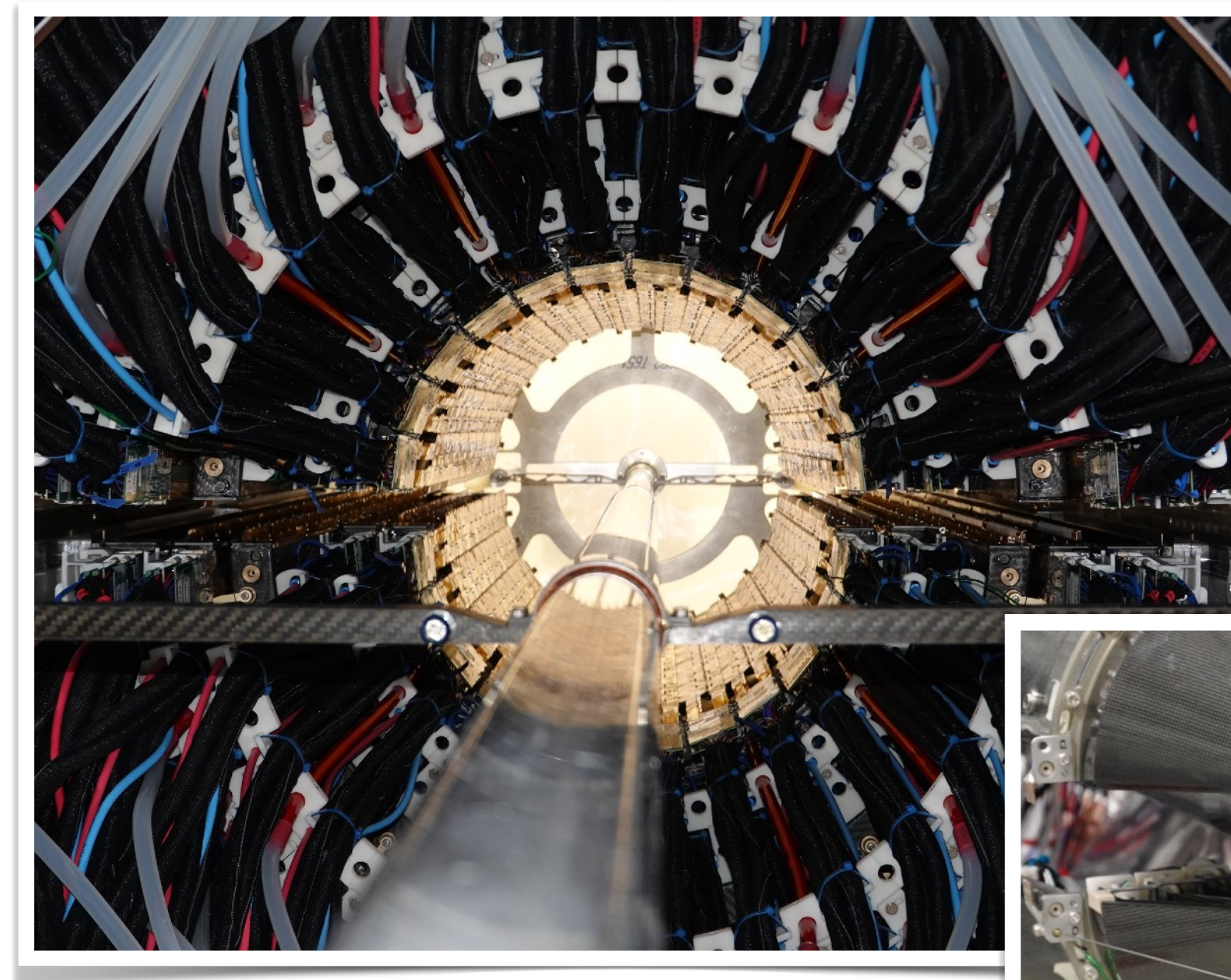


ALICE

ITS1



ITS2



Upgrade of the ALICE ITS detector

Felix Reidt
felix.reidt@cern.ch

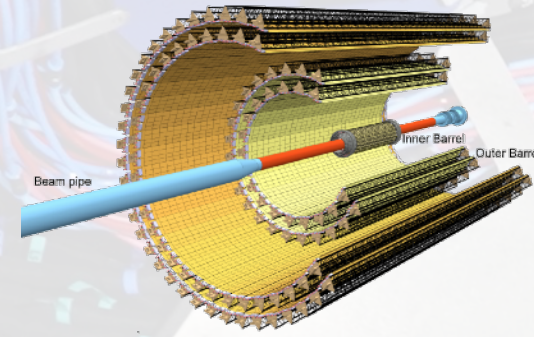
on behalf of the ALICE collaboration



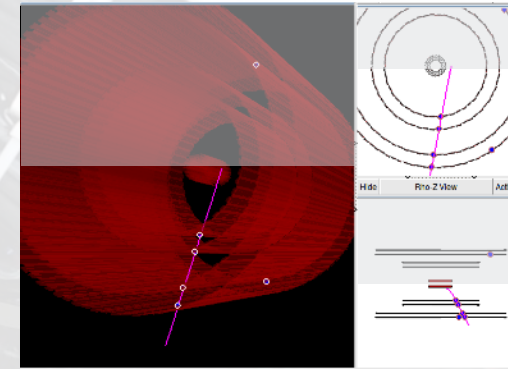
ALICE

Outline

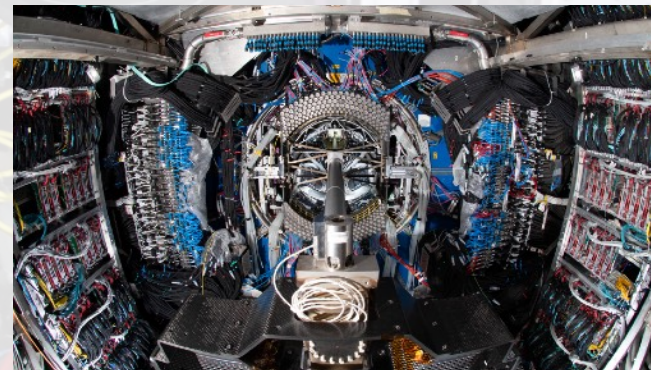
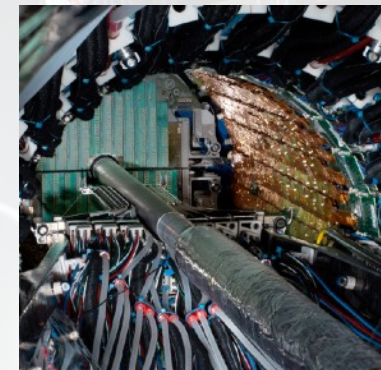
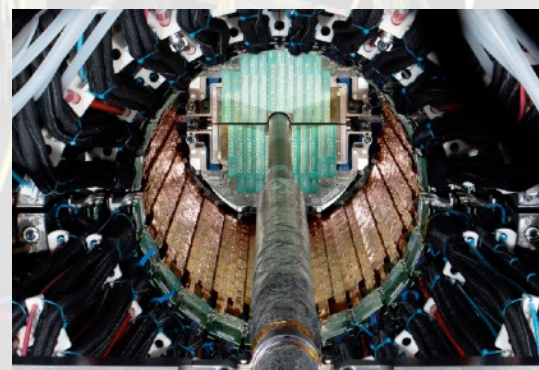
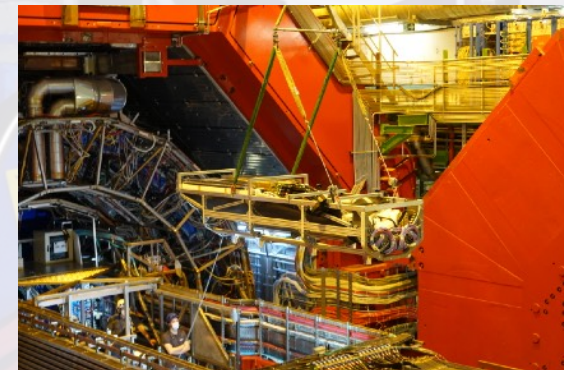
- Introduction



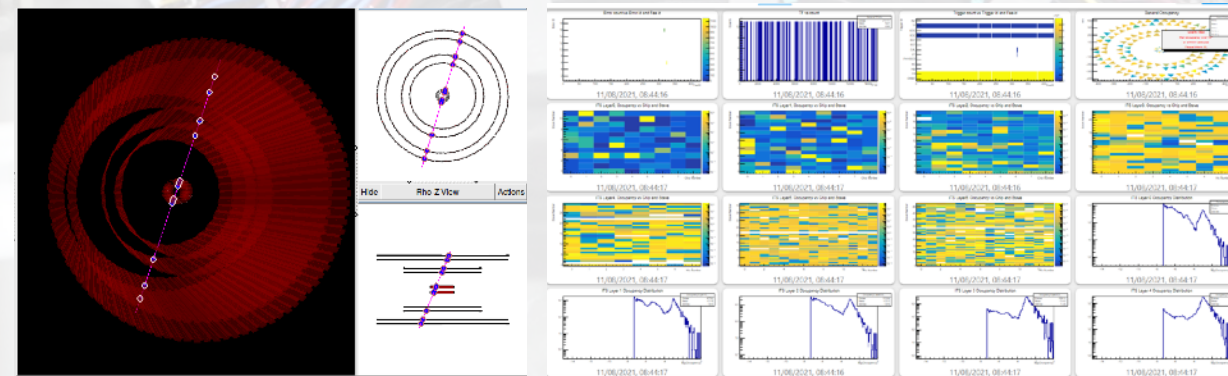
- On-surface commissioning



- Detector Installation



- Commissioning in the experimental apparatus





ALICE

Upgrade Strategy for LS2 (2019 - 2021)

Motivation:

- High-precision measurements of rare probes at low p_T
- Cannot be selected by hardware triggers
 - Need to record large minimum-bias data sample
⇒ read out all Pb-Pb interactions up to the maximum LHC Pb-Pb collision rate of 50 kHz

Goal:

- Pb-Pb recorded luminosity: 10 nb^{-1} (plus pp, p-A and O-O data)
→ gain factor 100 in statistics for minimum-bias trigger
- Improved vertex reconstruction and tracking capabilities

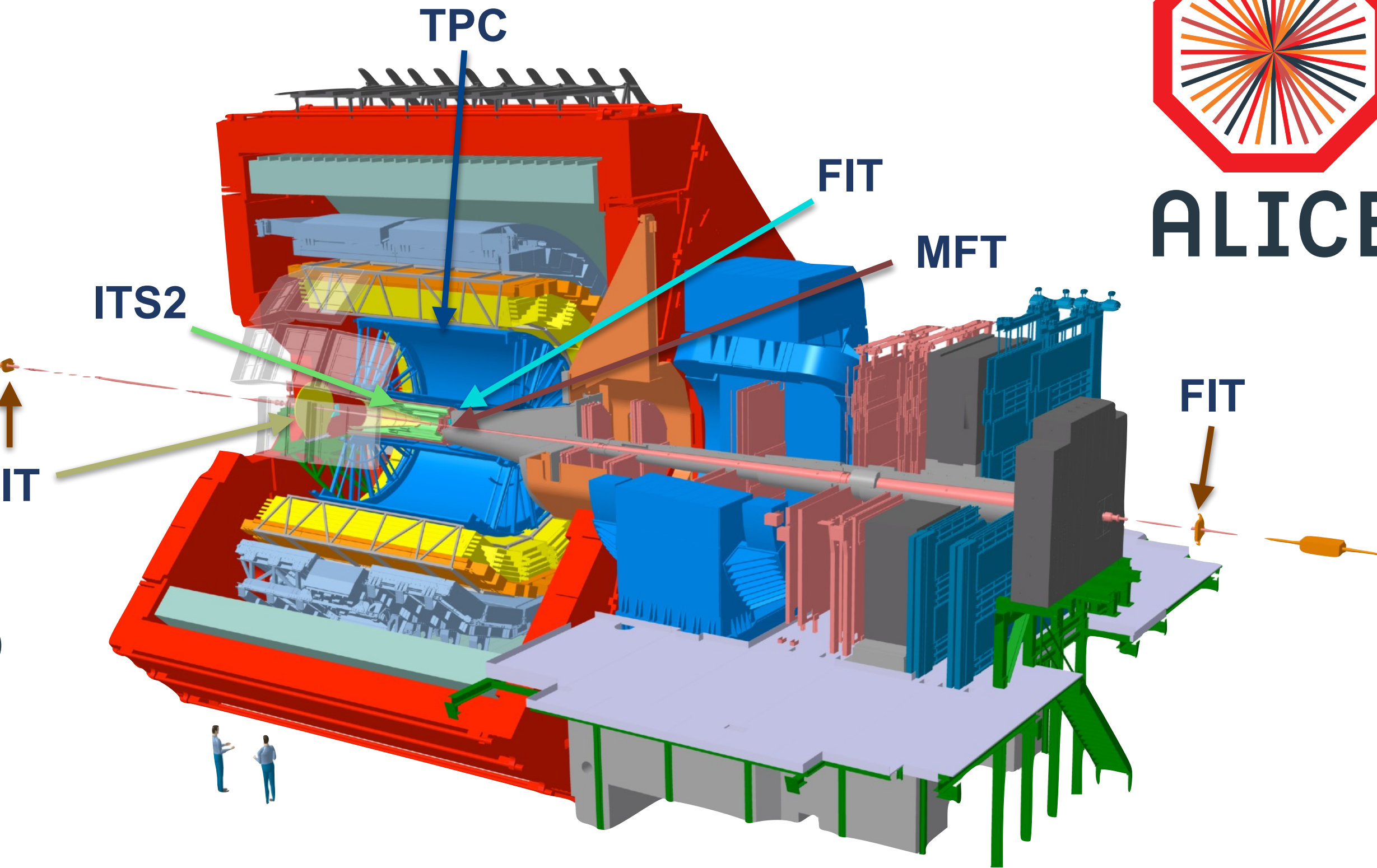
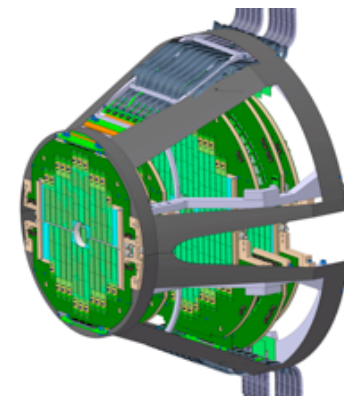
Strategy:



New Inner Tracking System (ITS2)

New Muon Forward Tracker (MFT)

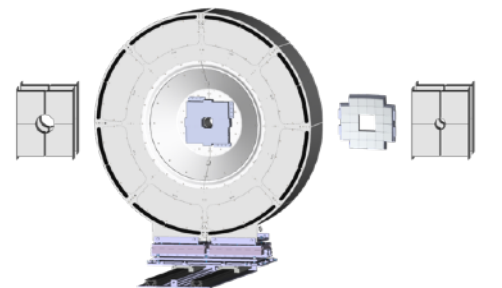
New TPC Readout Chambers (ROCs)



New Fast Interaction Trigger (FIT) Detector

Readout upgrade

Integrated Online-Offline system (O²)



ITS2 Design Objectives

Improve impact parameter resolution by a factor ~ 3 in $r\phi$ and ~ 5 in z at $p_T=500\text{MeV}/c$

- get closer to IP: 39mm \rightarrow 23mm (innermost layer)
- reduce material budget: $\sim 1.14\% X_0 \rightarrow \sim 0.35\% X_0$ (inner layers)
- reduce pixel size: $50 \times 425 \mu\text{m}^2 \rightarrow O(30 \times 30 \mu\text{m}^2)$

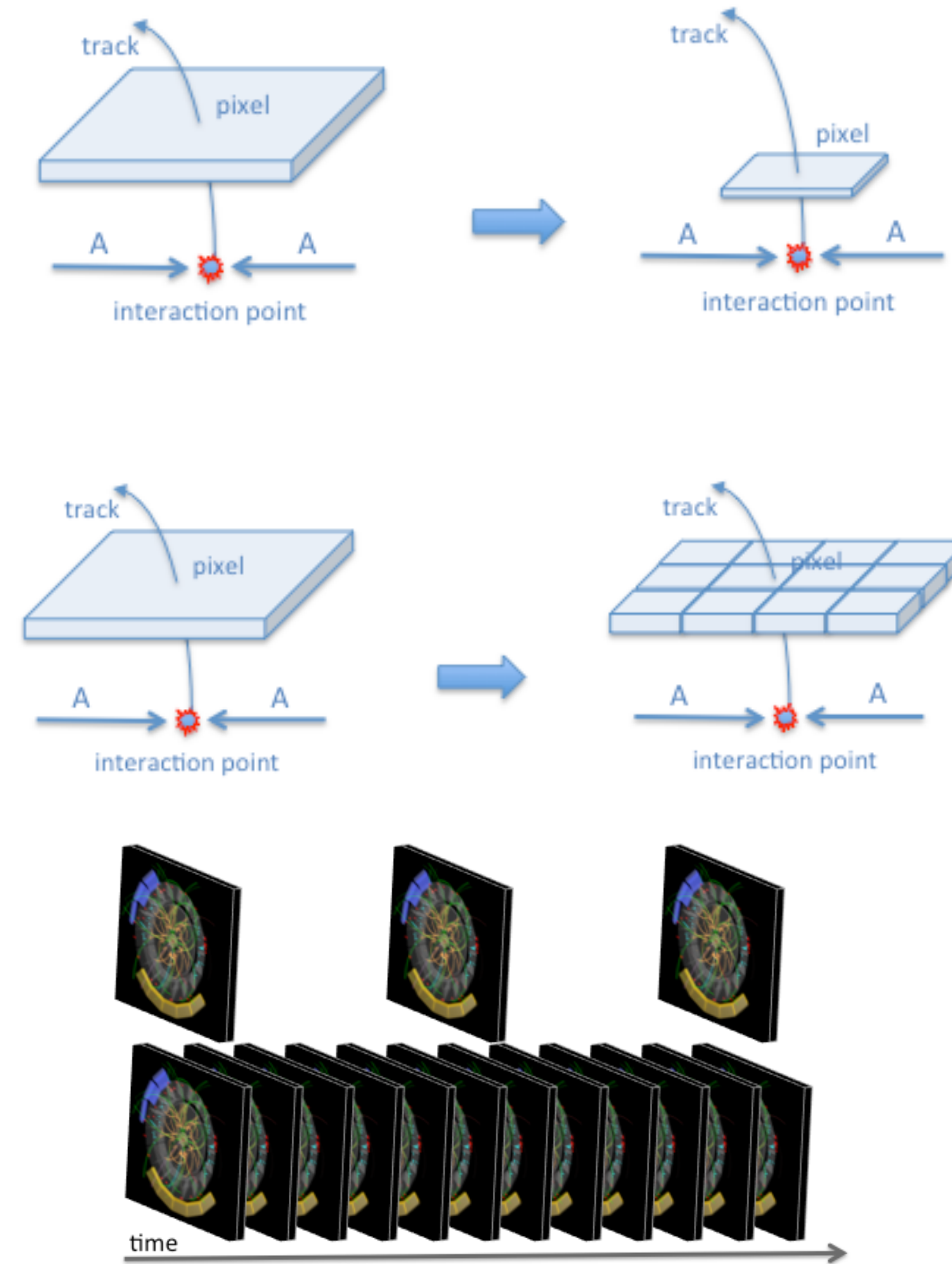
Improve tracking efficiency and p_T resolution at low p_T

- increase granularity: 6 layers \rightarrow 7 pixel layers

Fast readout

- readout of Pb-Pb at up to 100 kHz (presently 1 kHz) and 400 kHz for pp

\rightarrow ITS1 will be fully replaced!



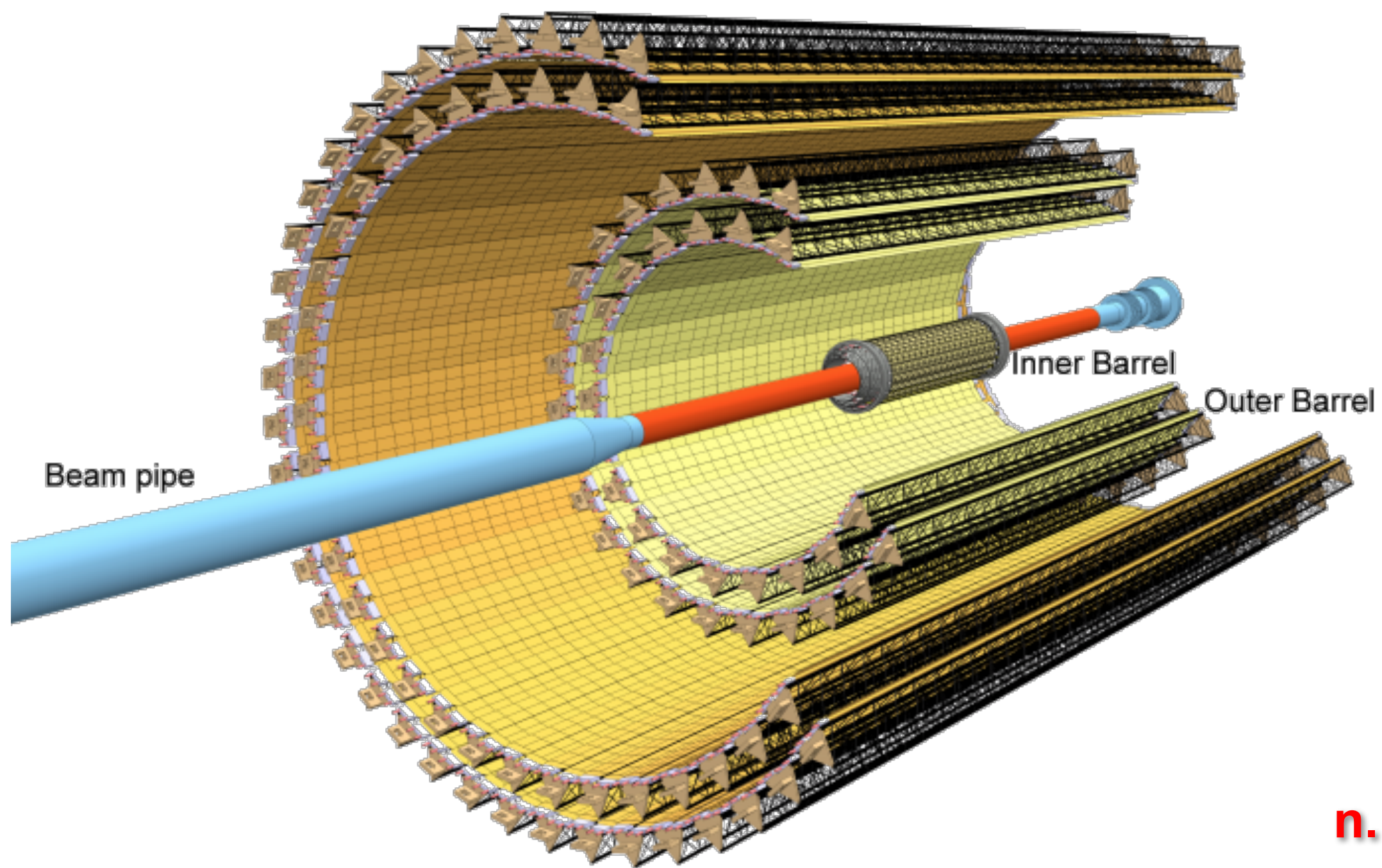


ALICE

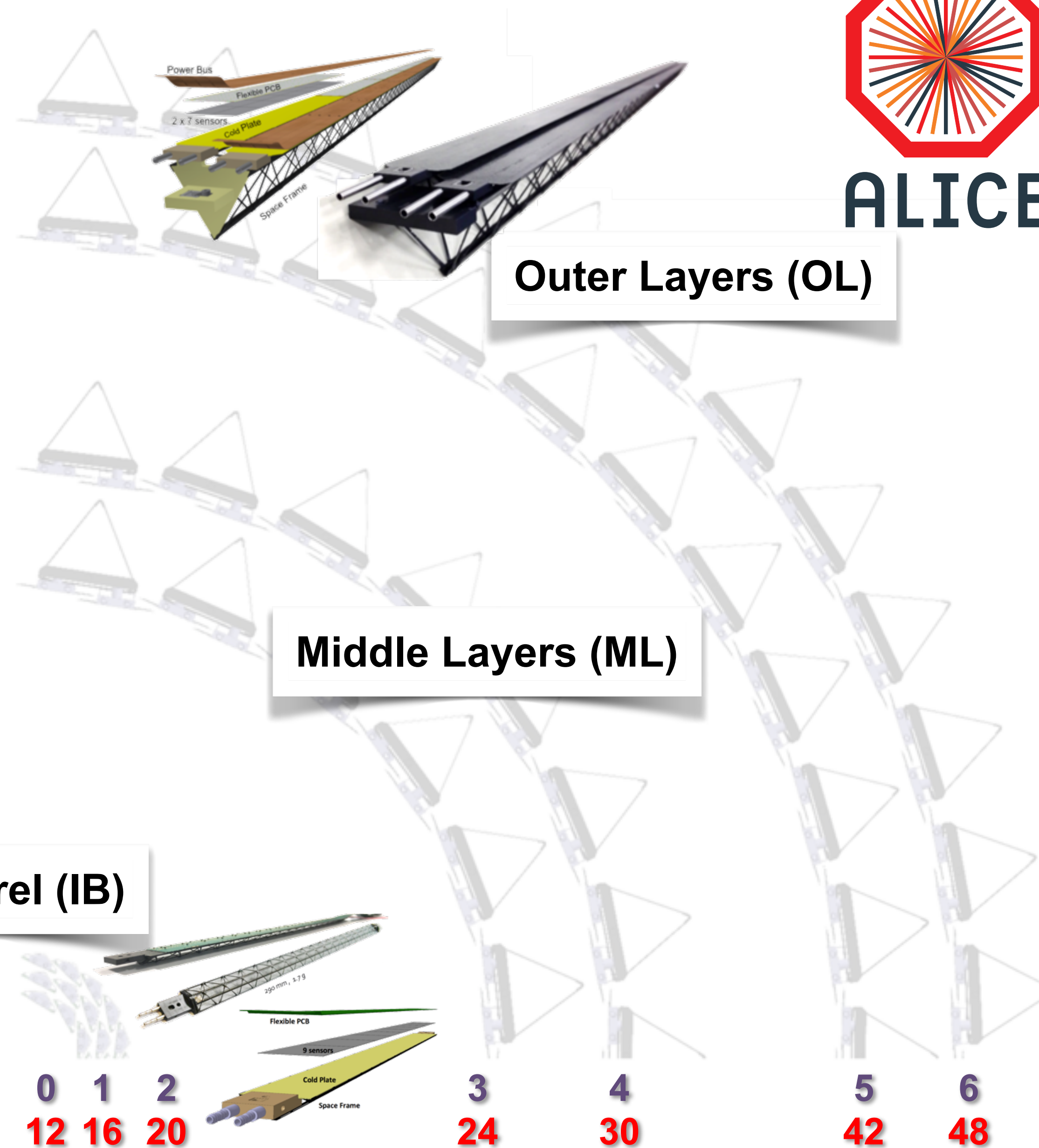
ITS2 Layout

- 7 layers (inner/middle/outer): **3/2/2** from R = 22 mm to R = 400 mm
- 192 staves (IL/ML/OL): **48/54/90**
- Ultra-lightweight support structure and cooling

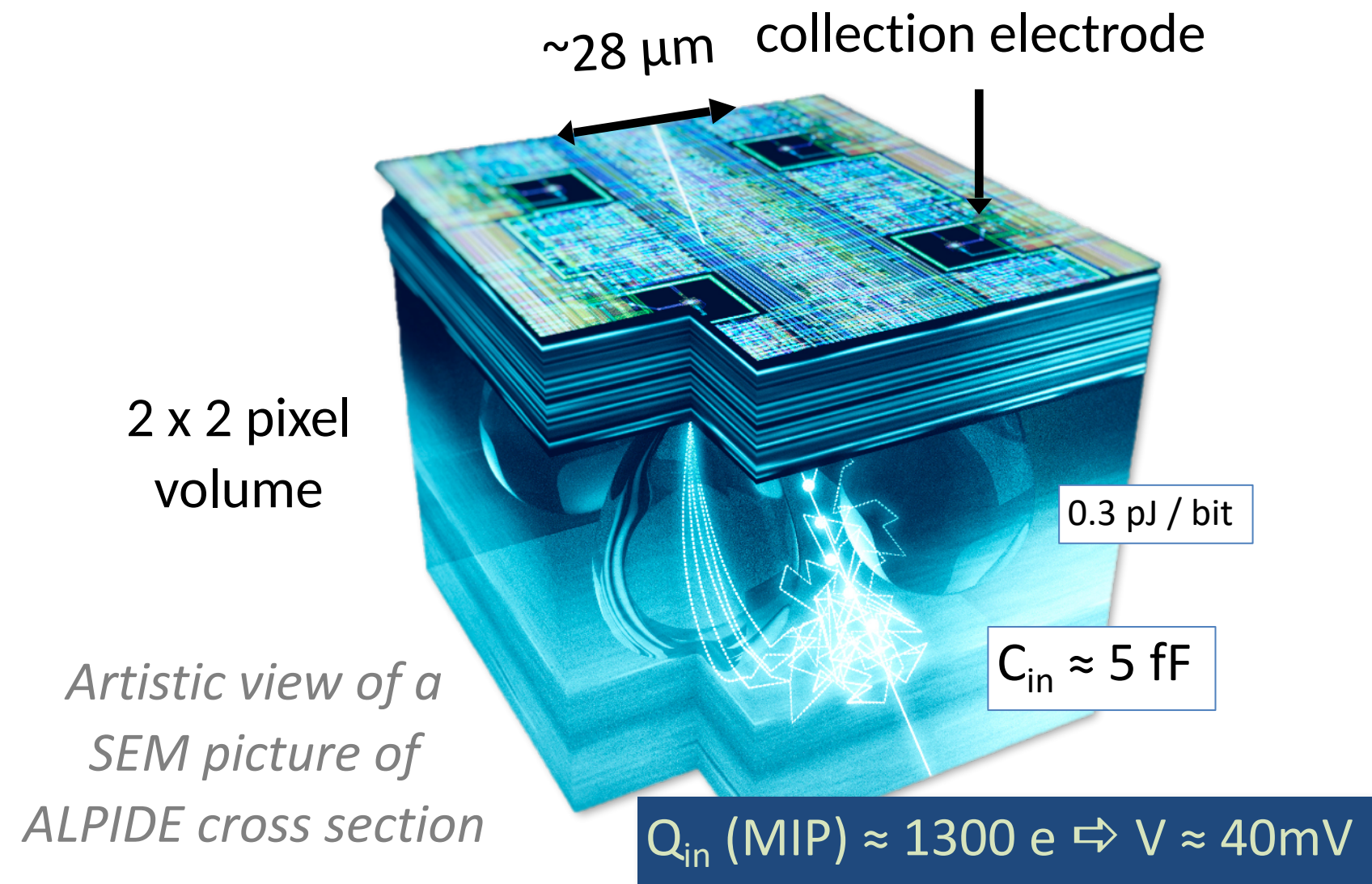
10 m² active silicon area, 12.5×10⁹ pixels



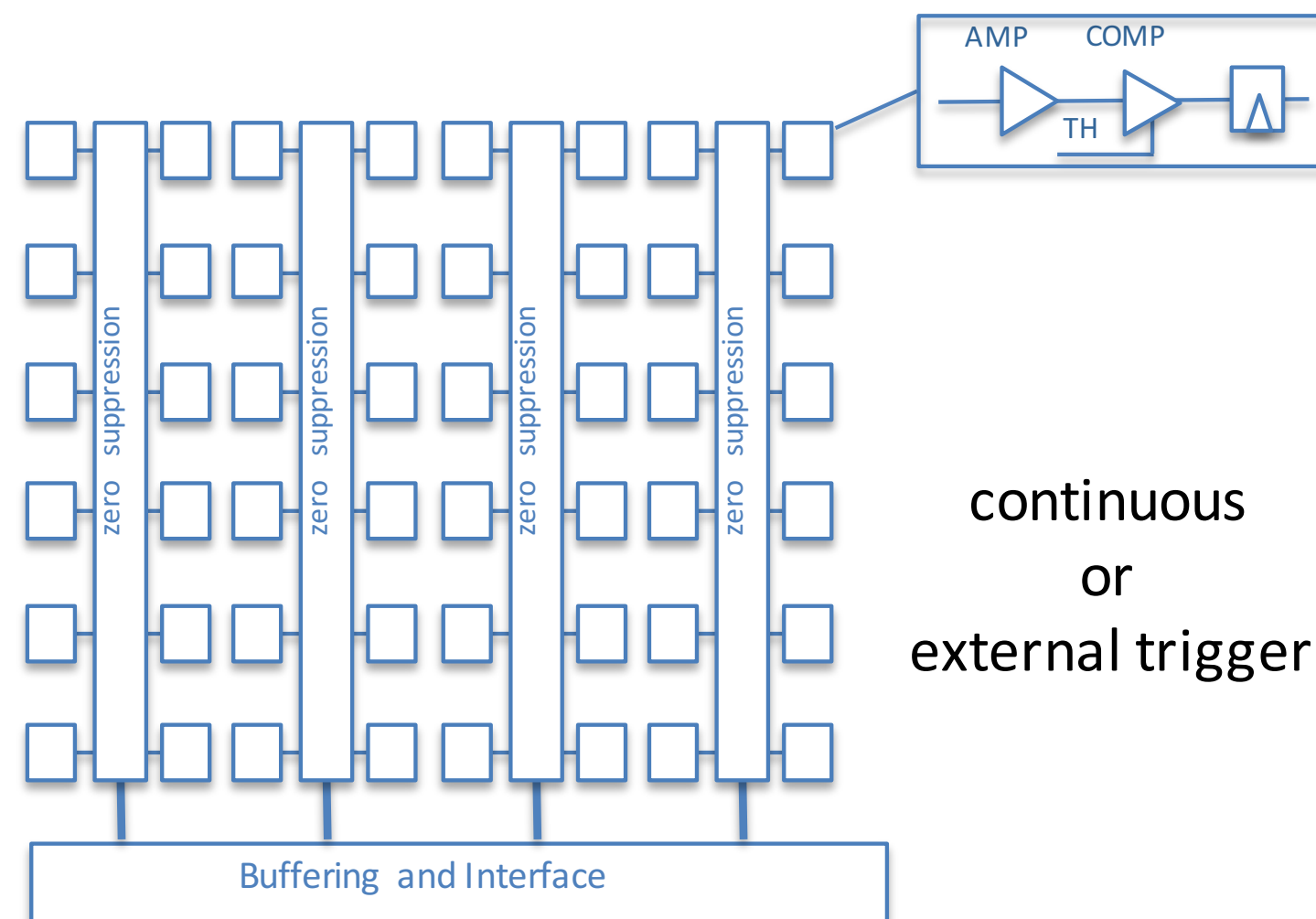
**Outer Barrel (OB)
= ML + OL**



Pixel chip characteristics



- Based on the **ALPIDE Monolithic Active Pixel Sensor**
 - **In-pixel** amplification, shaping, discrimination and Multiple-Event Buffers (MEB)
 - **In-matrix** data sparsification
 - High detection efficiency: **> 99%** and low fake-hit rate: **$\ll 10^{-6}/\text{pixel}/\text{event}$**
 - Radiation tolerant: **> 270 krad Total Ionising Dose (TID)**,
> 1.7×10^{12} 1 MeV/n_{eq} Non-Ionising Energy Loss (NIEL)
 - Low power: **< 40mW / cm²**

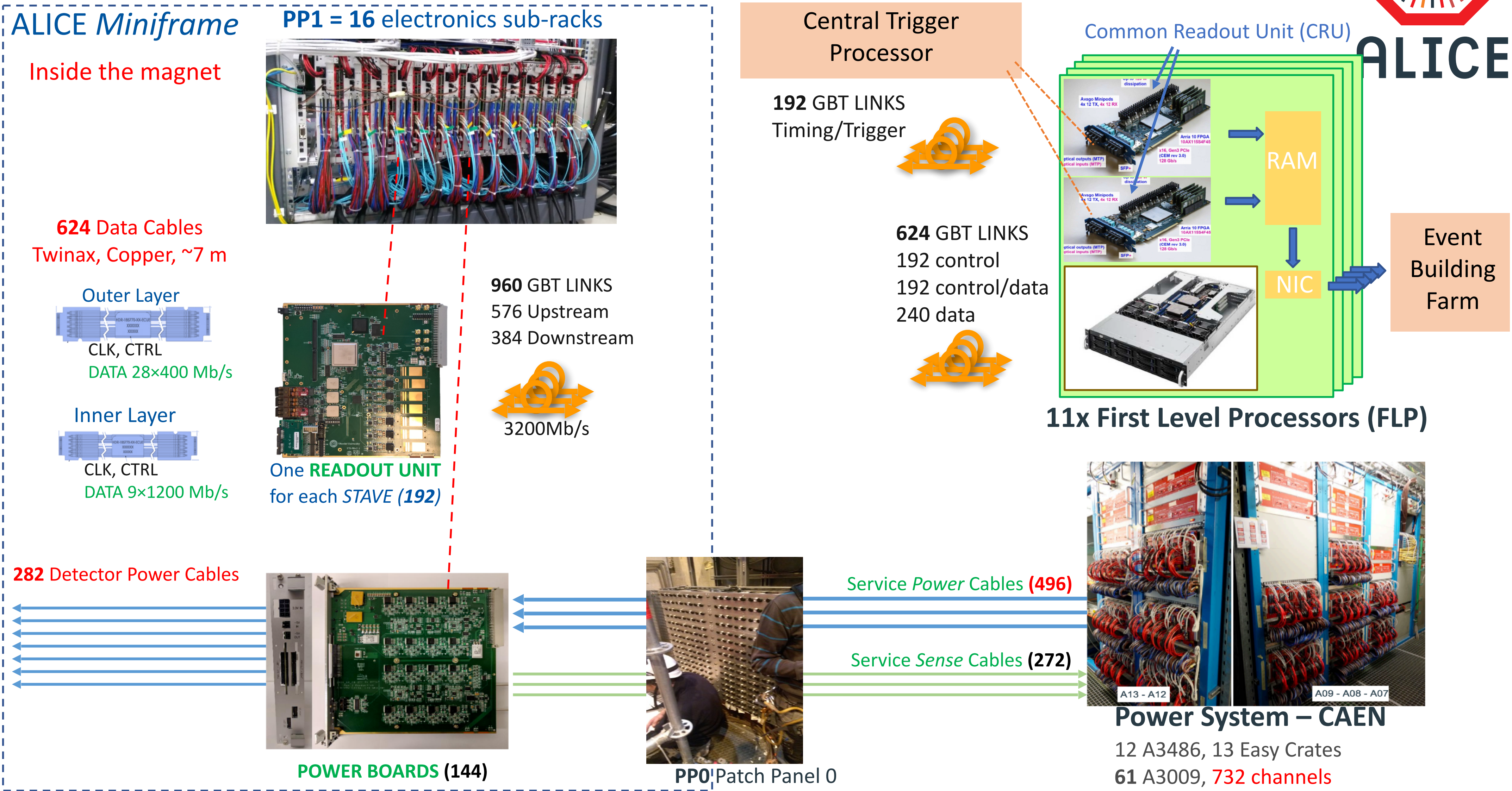


	Previous ITS	New ITS2
Distance to IP (mm)	39	22
X_0 (innermost layer) (%)	~ 1.14	~ 0.35
Pixel pitch (μm^2)	50 x 425	27 x 29
Readout rate (kHz)	1	100
Spatial resolution ($r_\phi \times z$) (μm^2)	11 x 100	5 x 5

**Improved resolution,
less material,
faster readout**



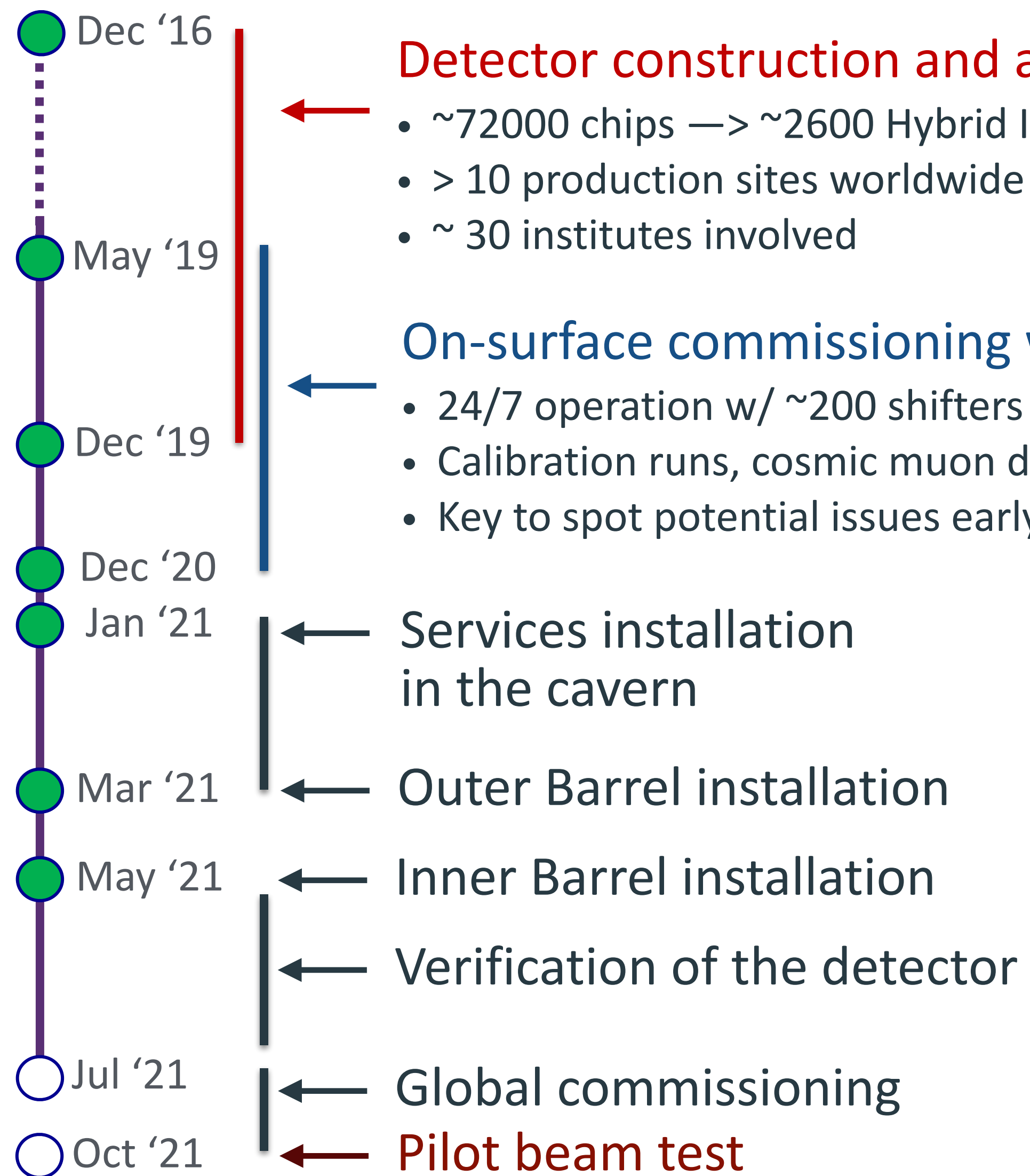
ITS2 System Overview





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Construction installation and commissioning timeline*



Detector construction and assembly

- ~72000 chips → ~2600 Hybrid Integrated Circuits (HICs) → ~280 Staves
- > 10 production sites worldwide
- ~ 30 institutes involved

On-surface commissioning w/ final services

- 24/7 operation w/ ~200 shifters from ~30 institutes
- Calibration runs, cosmic muon data taking
- Key to spot potential issues early

Services installation in the cavern

Outer Barrel installation

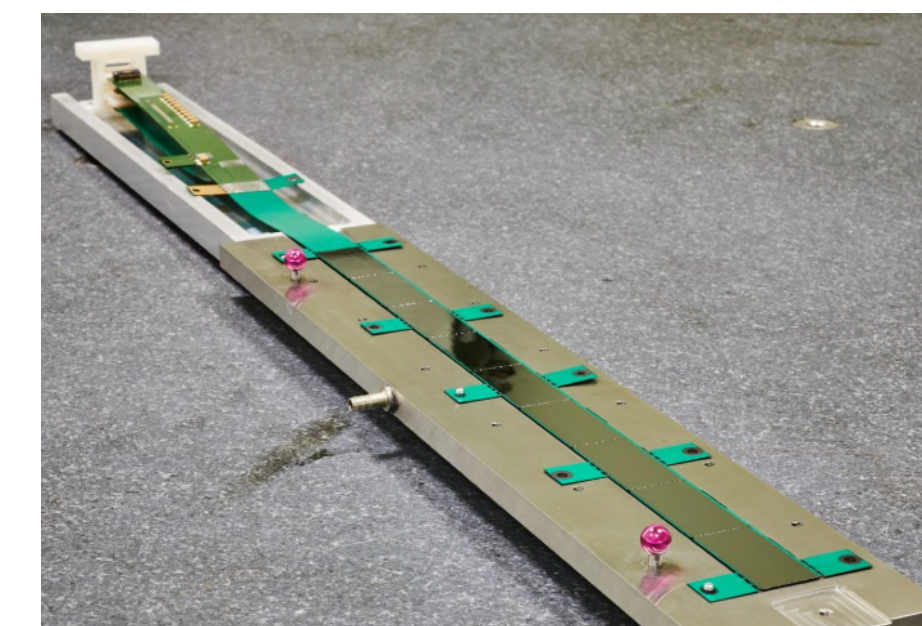
Inner Barrel installation

Verification of the detector

Global commissioning

Pilot beam test

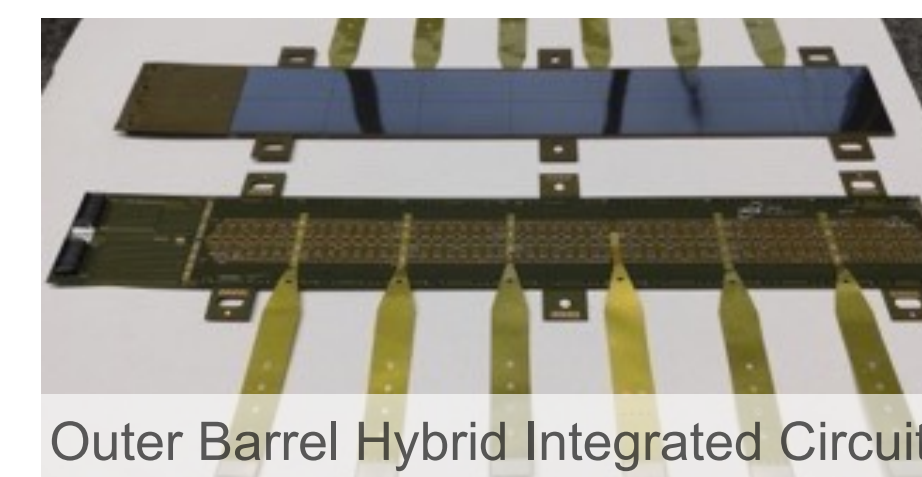
* Schedule was changed significantly due to COVID-19; installation in the cavern was carried out with a significantly reduced crew



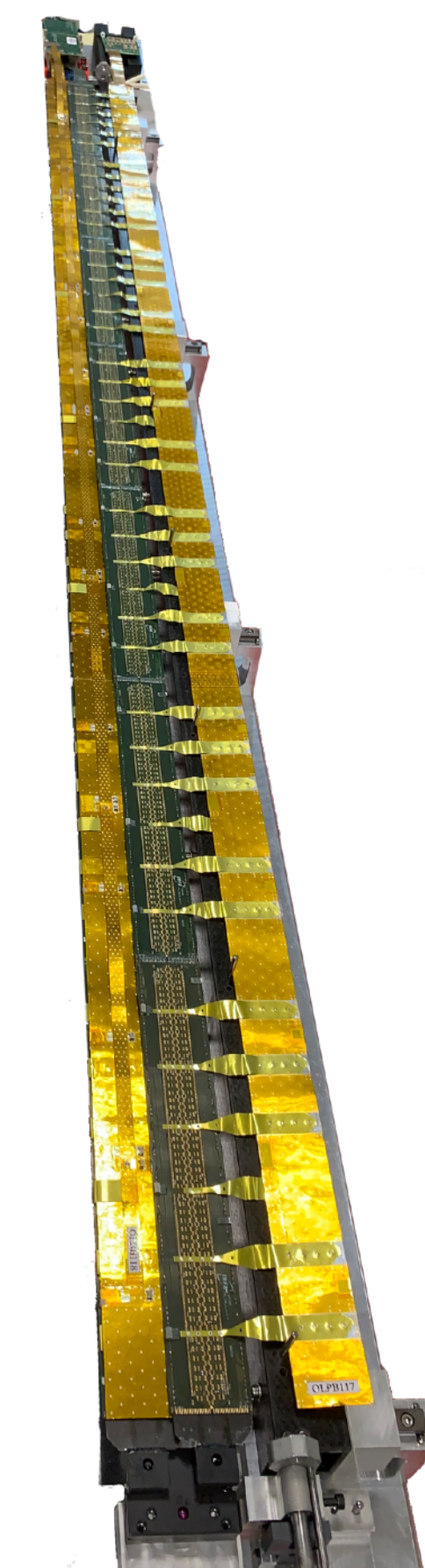
Inner Barrel Hybrid Integrated Circuit



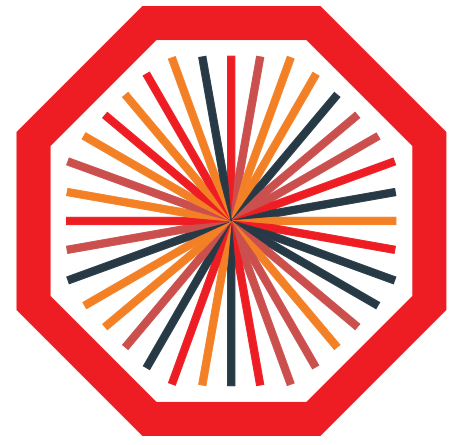
Inner Barrel Stave



Outer Barrel Hybrid Integrated Circuit



Outer Barrel Stave



ALICE

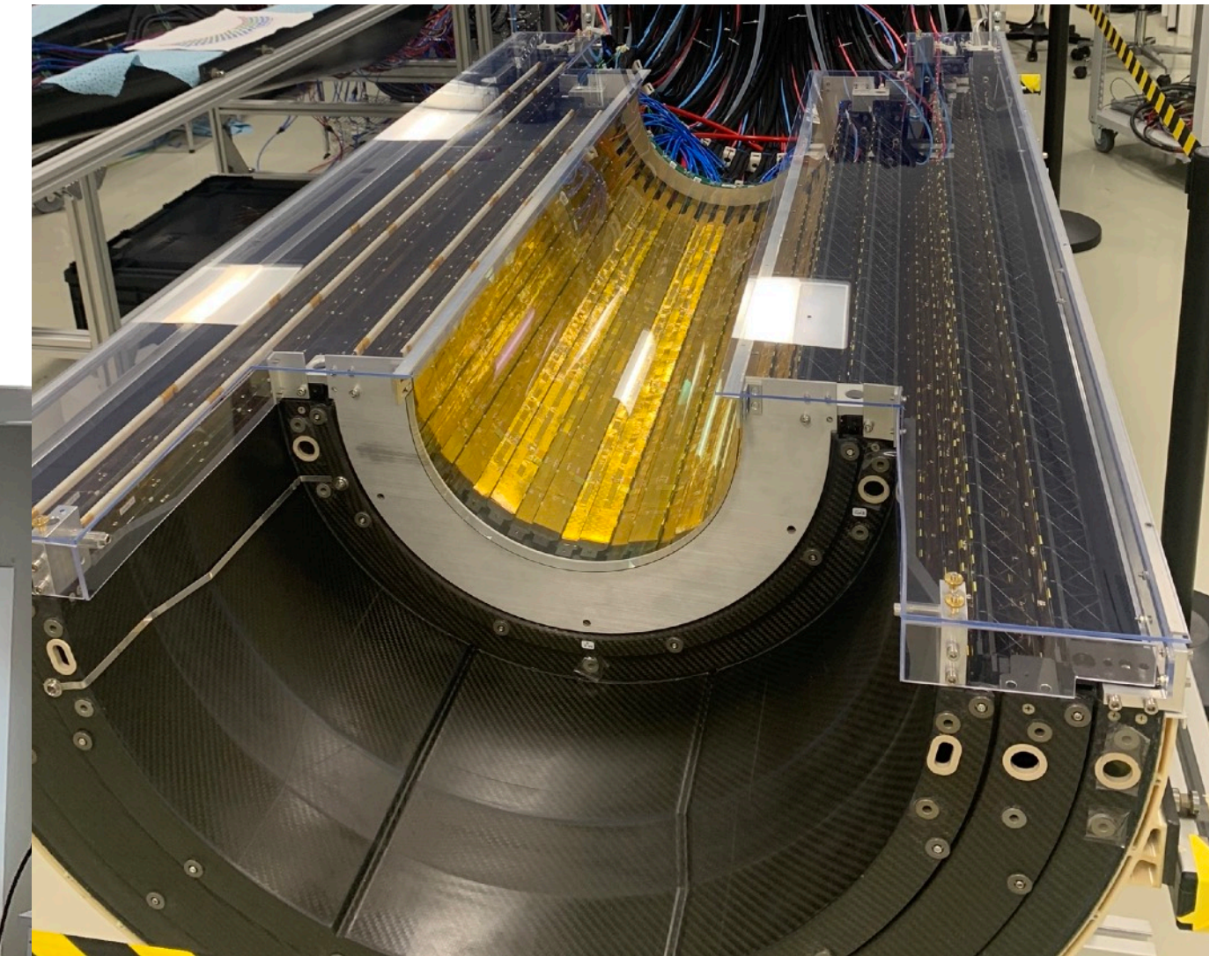
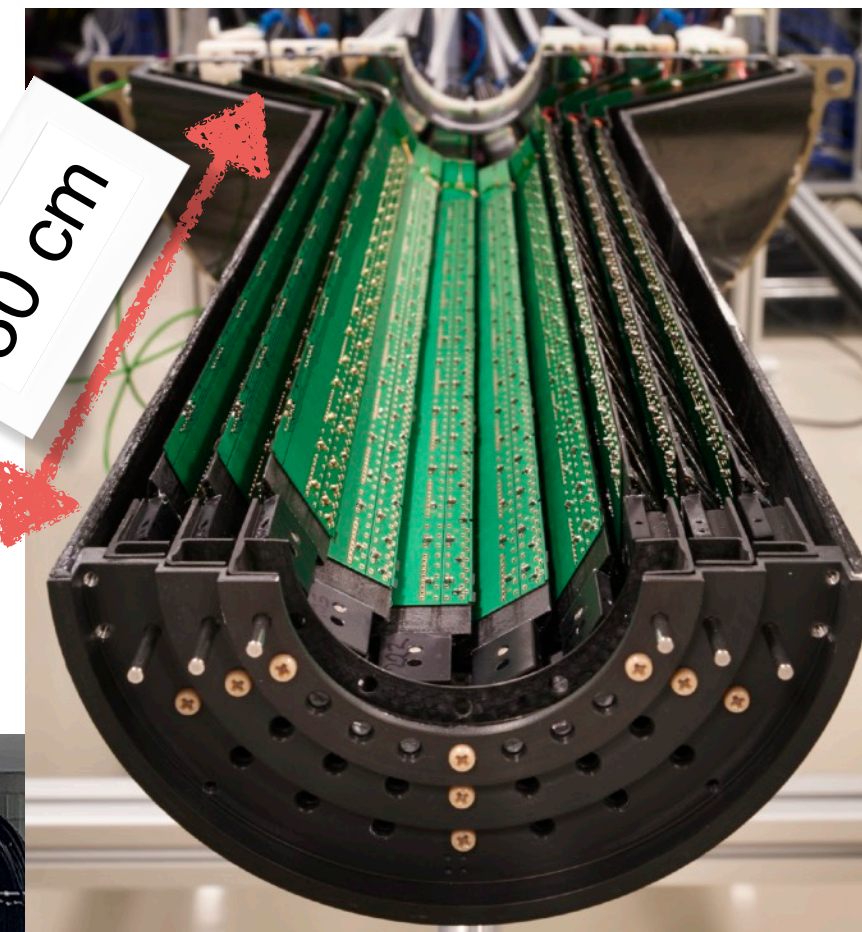
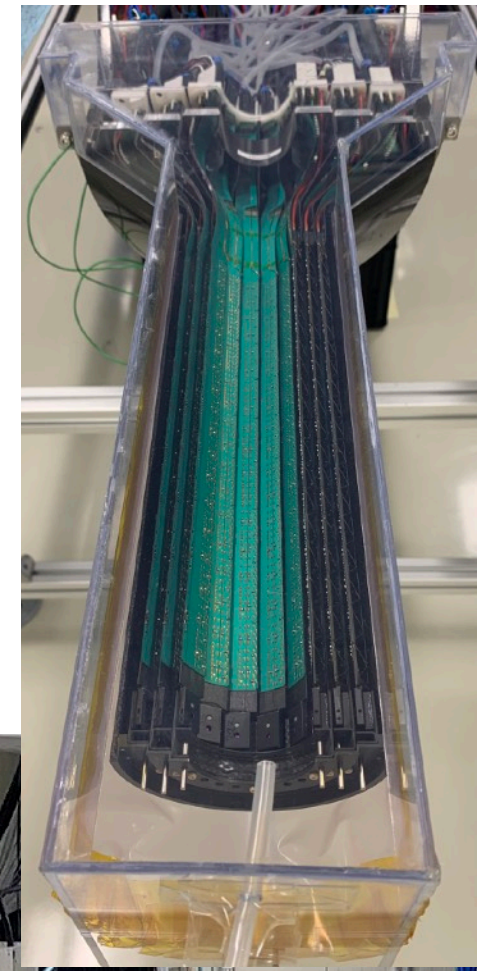
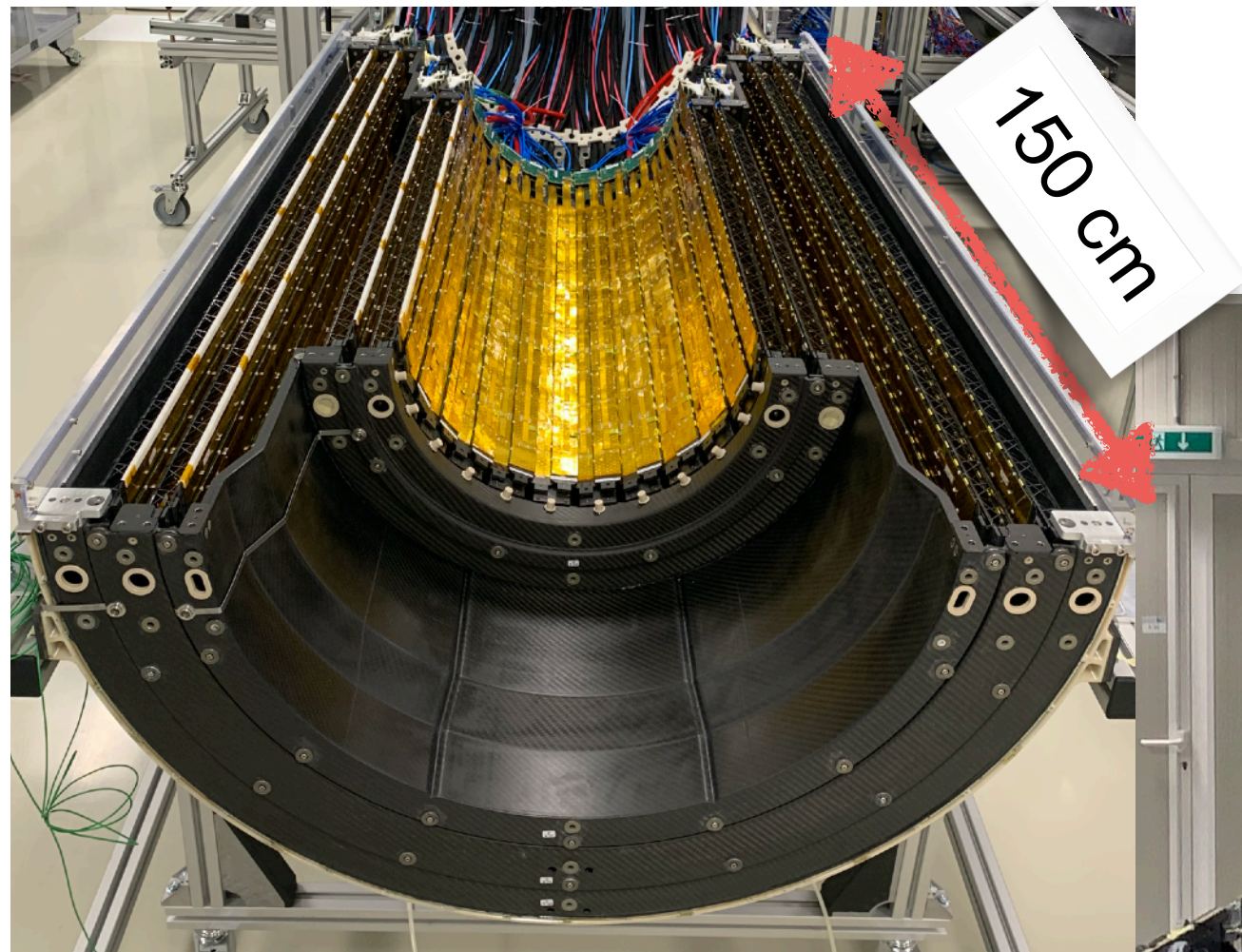
On-surface commissioning - clean room

Inner Barrel Top

Inner Barrel Bottom

Outer Barrel Top

Outer Barrel Bottom

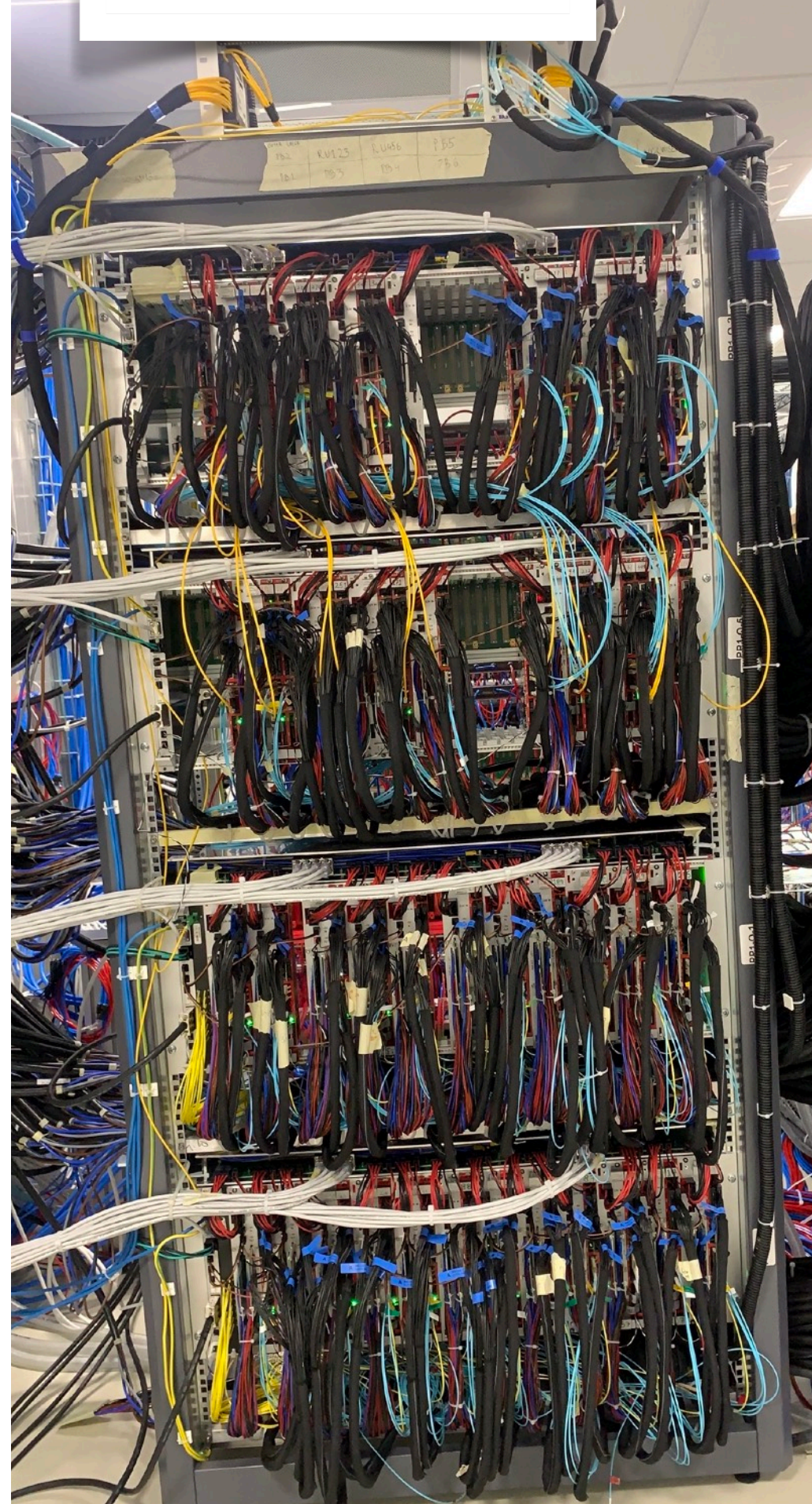




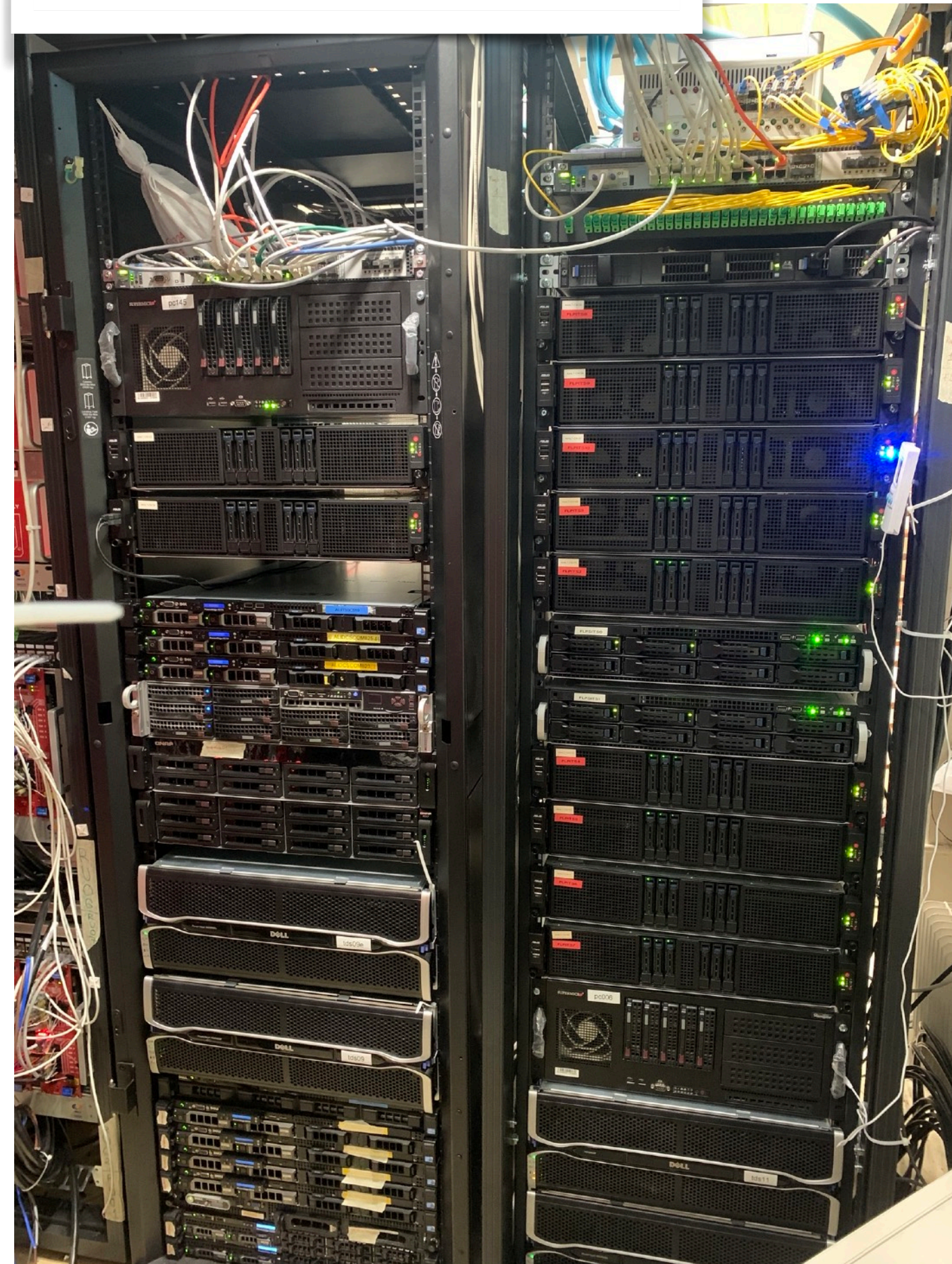
ALICE

On-surface commissioning - services

Readout Units / Power Boards



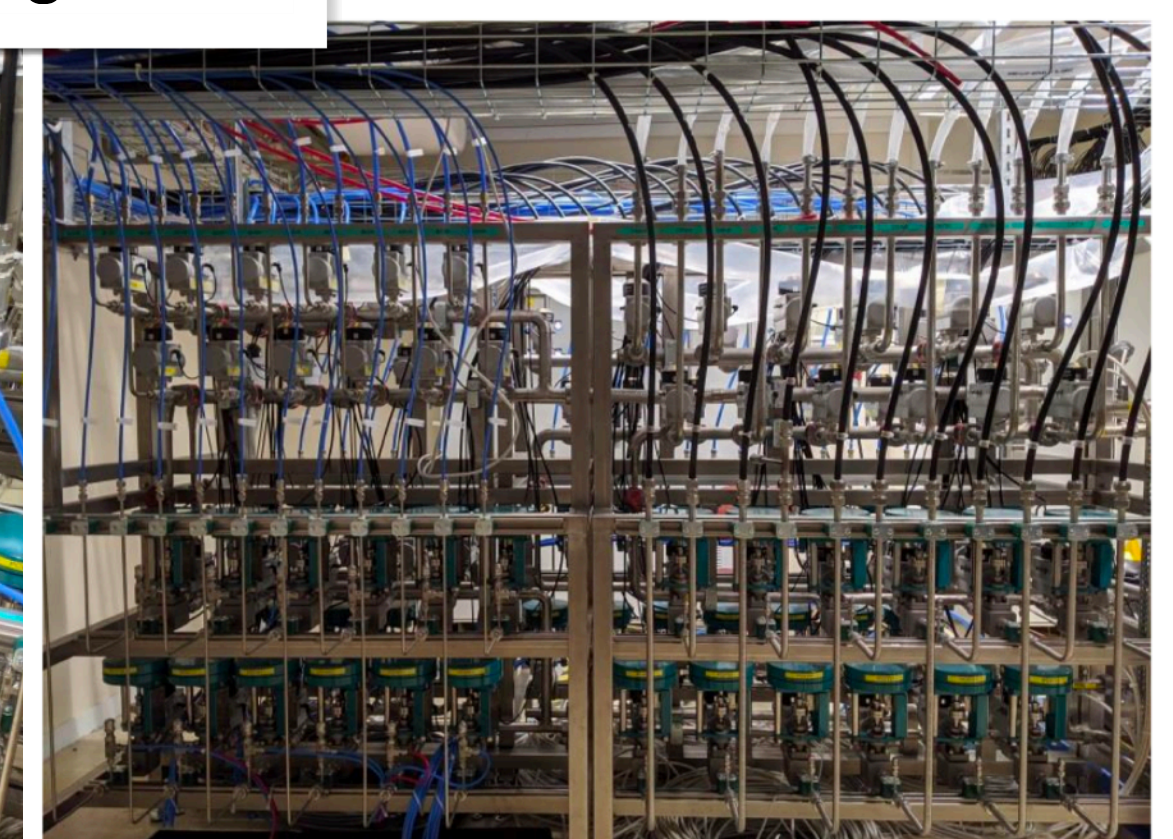
Readout / Control Servers



Power supplies



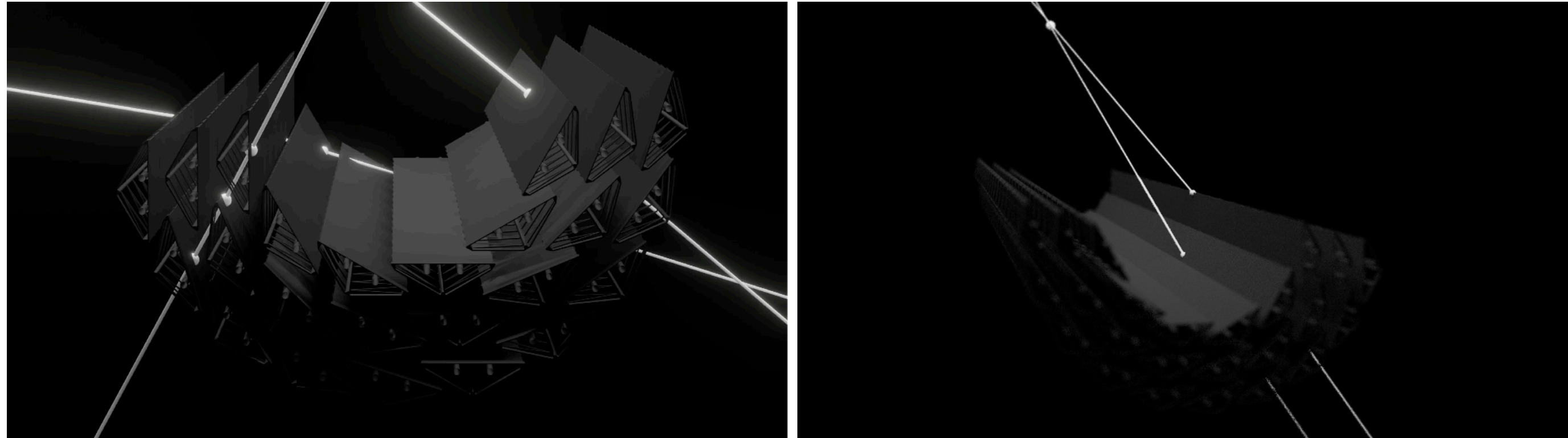
Cooling Plant





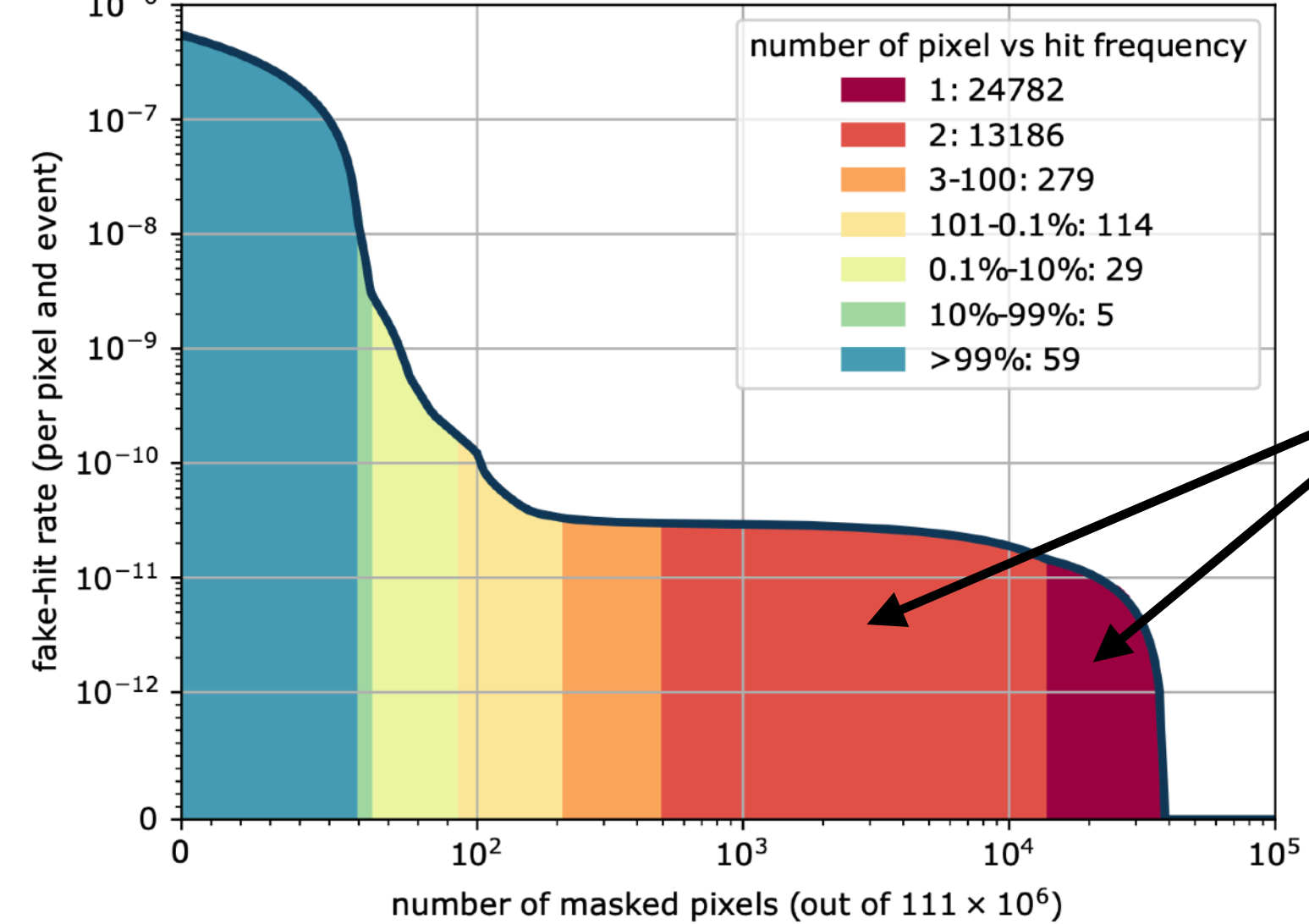
ALICE

On-surface commissioning - Inner Barrel - highlights



- Tracks and vertices reconstructed
- Excellent fake-hit rate of 10^{-10} / pixel / event
 - Achieved by masking a fraction of 10^{-6} pixels
- Bit error free data transmission:
 - Large operation margin in terms of occupancy and readout rate
 - Error free for several tens of hours at nominal operating conditions
 - Even regular errors $O(1 / s / 432 \text{ chips})$ for extreme combinations trigger rate and occupancy of lead only to negligible inefficiency

Run 101699 (15×10^6 events @ 50 kHz, VBB = 0 V, THR = 100 e)



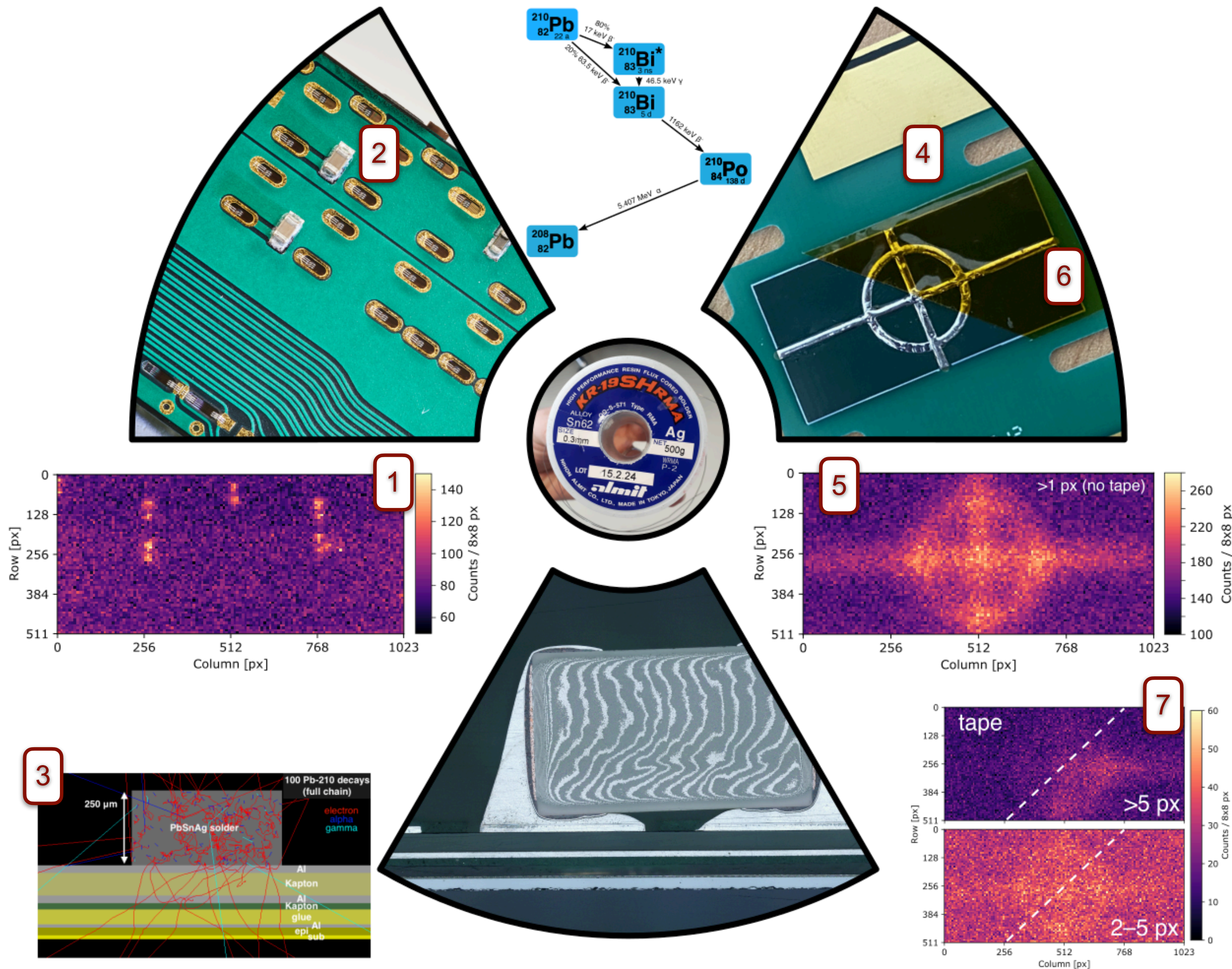
Pb-Pb most central equivalent occupancy

	44.9 kHz	67.3 kHz	101 kHz	123 kHz	202 kHz	247 kHz
32 clusters	Green	Green	Green	Green	Green	Green
64 clusters	Green	Green	Green	Green	Green	Green
128 clusters	Green	Green	Green	Green	Orange	Green
256 clusters	Green	Green	Orange	Green	Green	Green
512 clusters	Green	Green	Green	Green	Green	Green

High-speed link Bit Error Rate (BER)

- No errors measured (BER < 10^{-16} for the full system)
- Regular errors measured (BER > 10^{-12} , ~ 1 corrupt event/s)

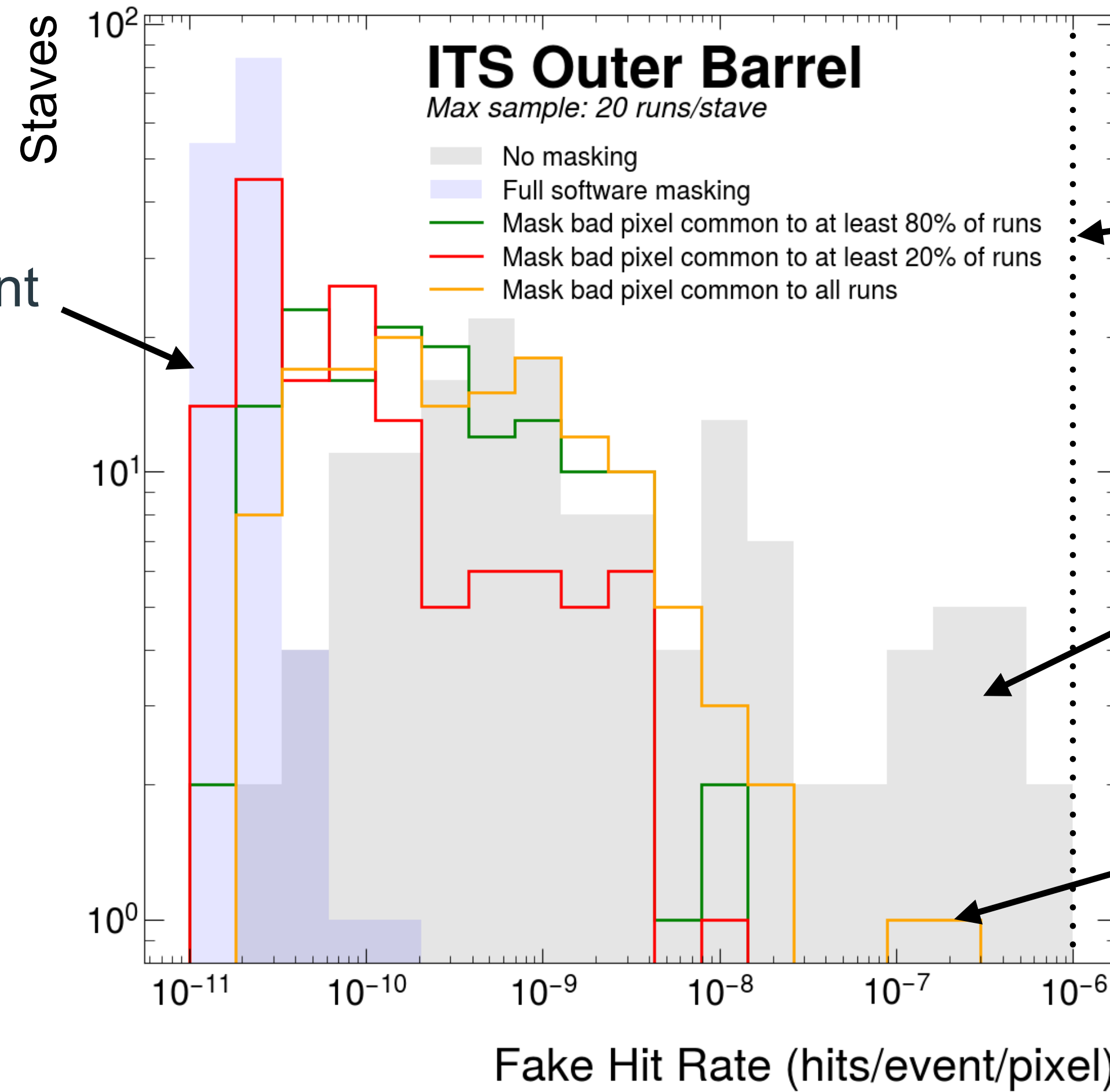
On-surface commissioning - Inner Barrel - closer look into the noise



- Excluded hits clusters assigned to tracks to investigate residual noise
- Superimposed hit maps of 216 ALPIDEs → effective **exposure time of 204 days**
- **Pattern 1** correlating with capacitors on the FPC **2**
- Components loaded using **lead solder tin**
- **Geant-4 simulation 3** to confirm ^{210}Pb decay chain as potential source → control measurement with tin sample **4**
- **Tin crosshair** well visible in the control measurement **5**
- Used Polyamide tape on a part of the sample to shield Po-210 (α) while letting Bi-210 (β) through **6**
- Cluster size analysis confirmed hypothesis: noise pattern originates from Bi-210 (β) **7**

On-surface commissioning - Outer Barrel - fake-hit rate

Offline masking:
FHR 10^{-10} / pixel / event



Requirement:
 10^{-6} / pixel / event

Exclusion of broken double-columns

Masking noisy pixels common to all runs
→ realistic estimate for hardware masking

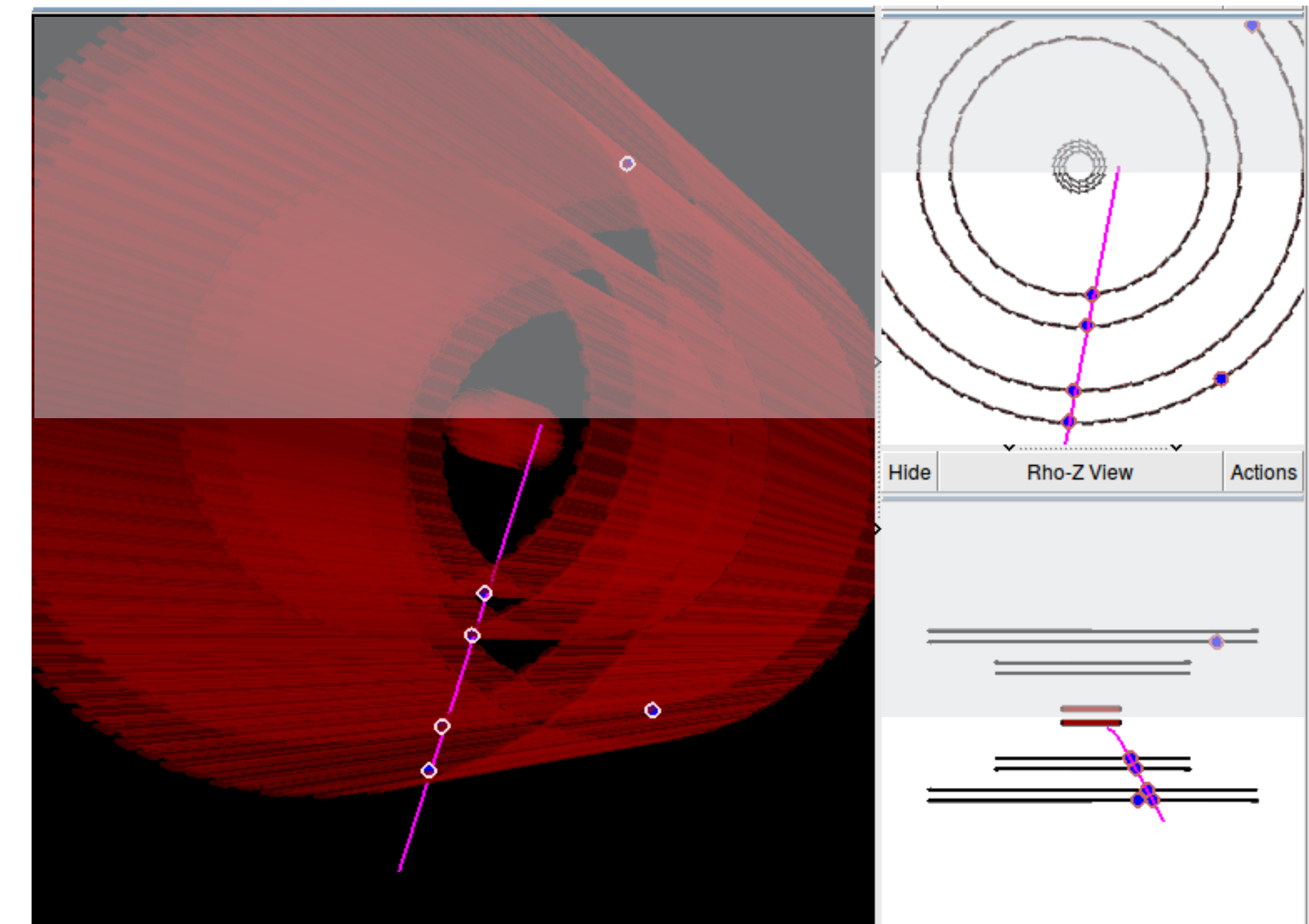
Excellent Fake-Hit Rate (FHR) in the entire OB

On-surface commissioning - Outer Barrel - tracking efficiency

Layer	TOP	BOT
3	98.92 + 0.02 - 0.02	98.95 + 0.02 - 0.02
4	99.30 + 0.01 - 0.01	99.64 + 0.01 - 0.01
5	99.30 + 0.01 - 0.01	98.54 + 0.02 - 0.02
6	99.20 + 0.01 - 0.01	99.38 + 0.02 - 0.02



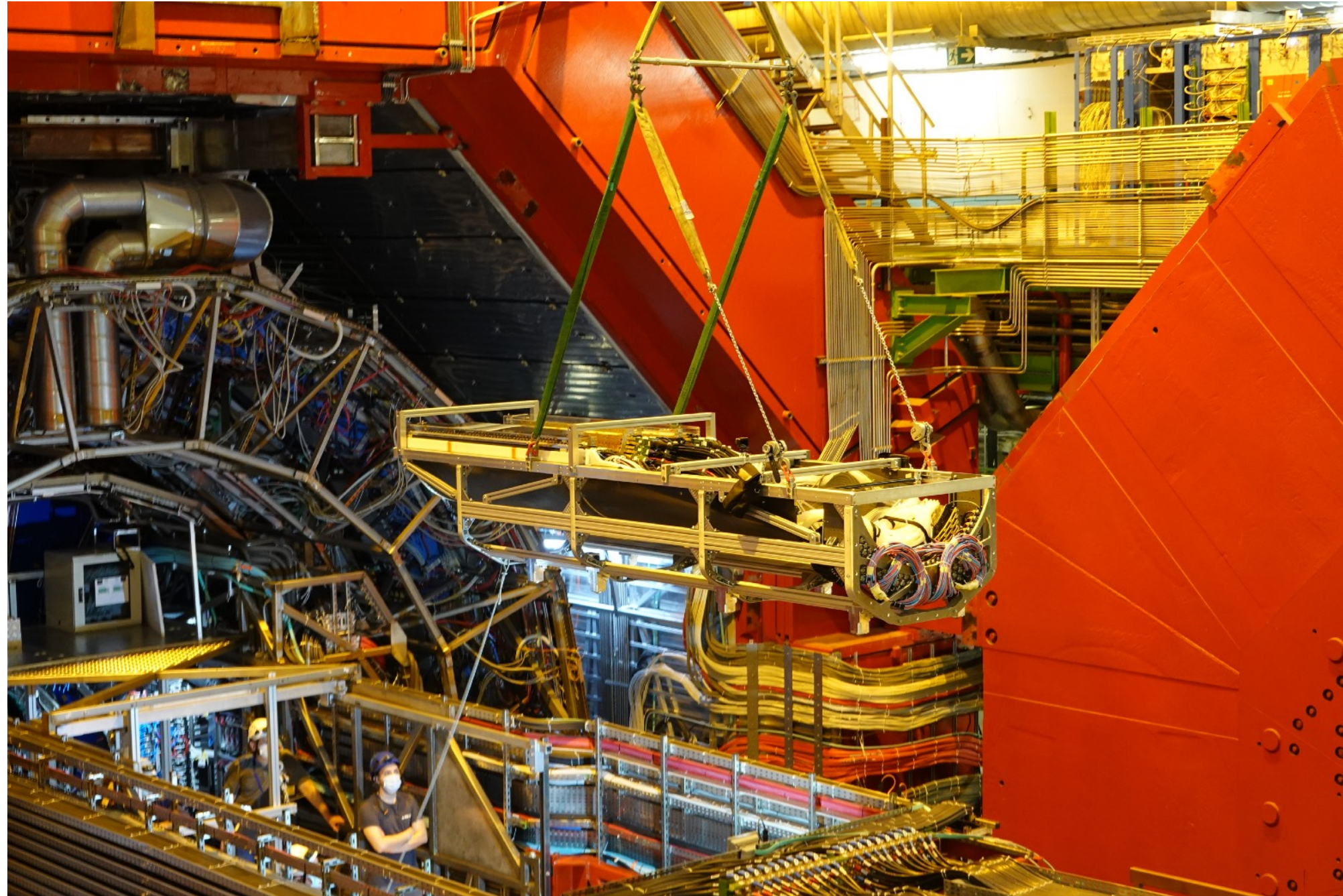
- Analysis prior to software alignment
- Hardware alignment sufficient for first studies
- Based on straight line fits through 3 out of 4 layers
- **Preliminary results** close to **99%**
- Study continues to understand efficiency loss



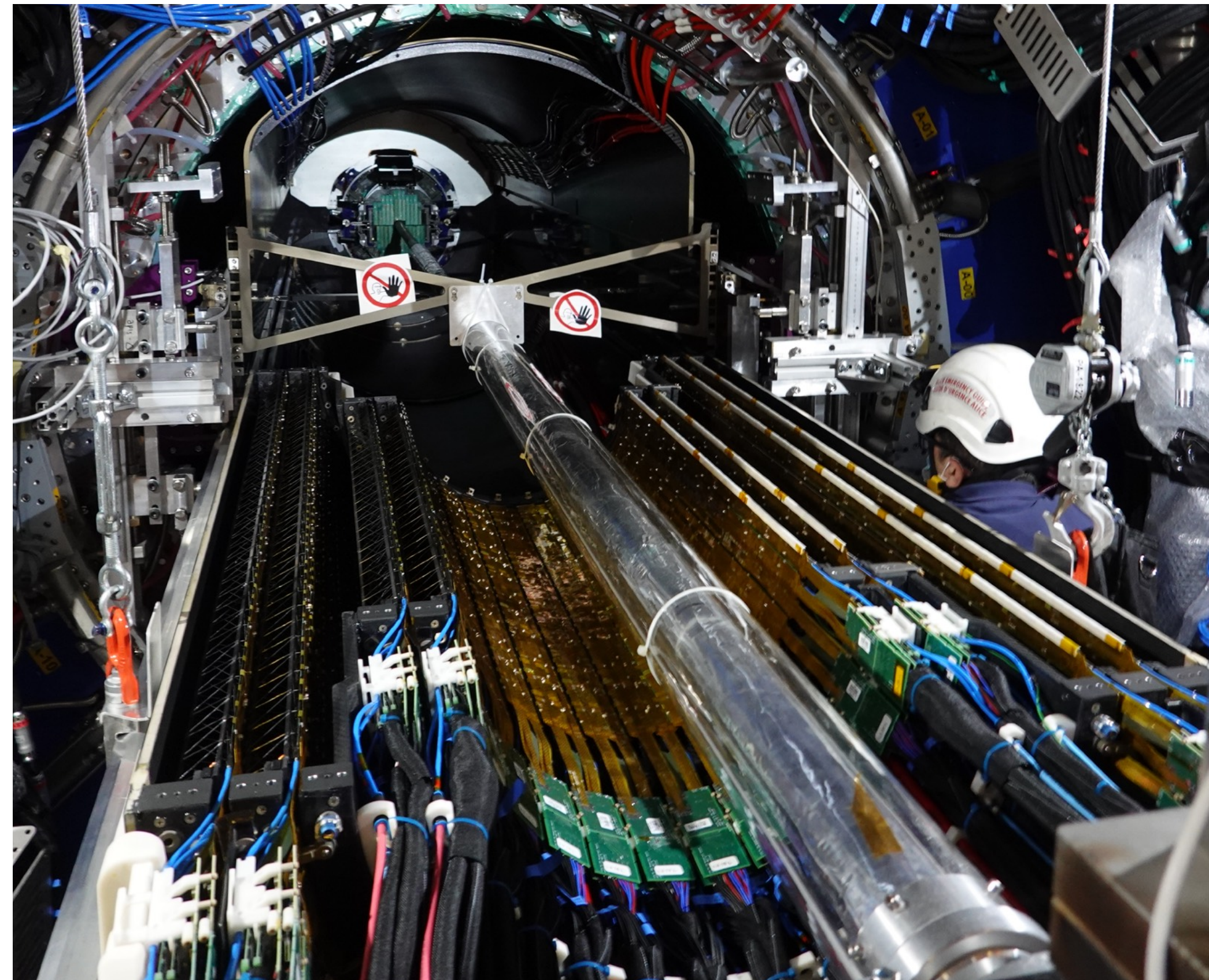
Track candidate in OB Bottom Half-Barrel



Detector installation



Outer Barrel Bottom lifted to the Mini-Frame by crane



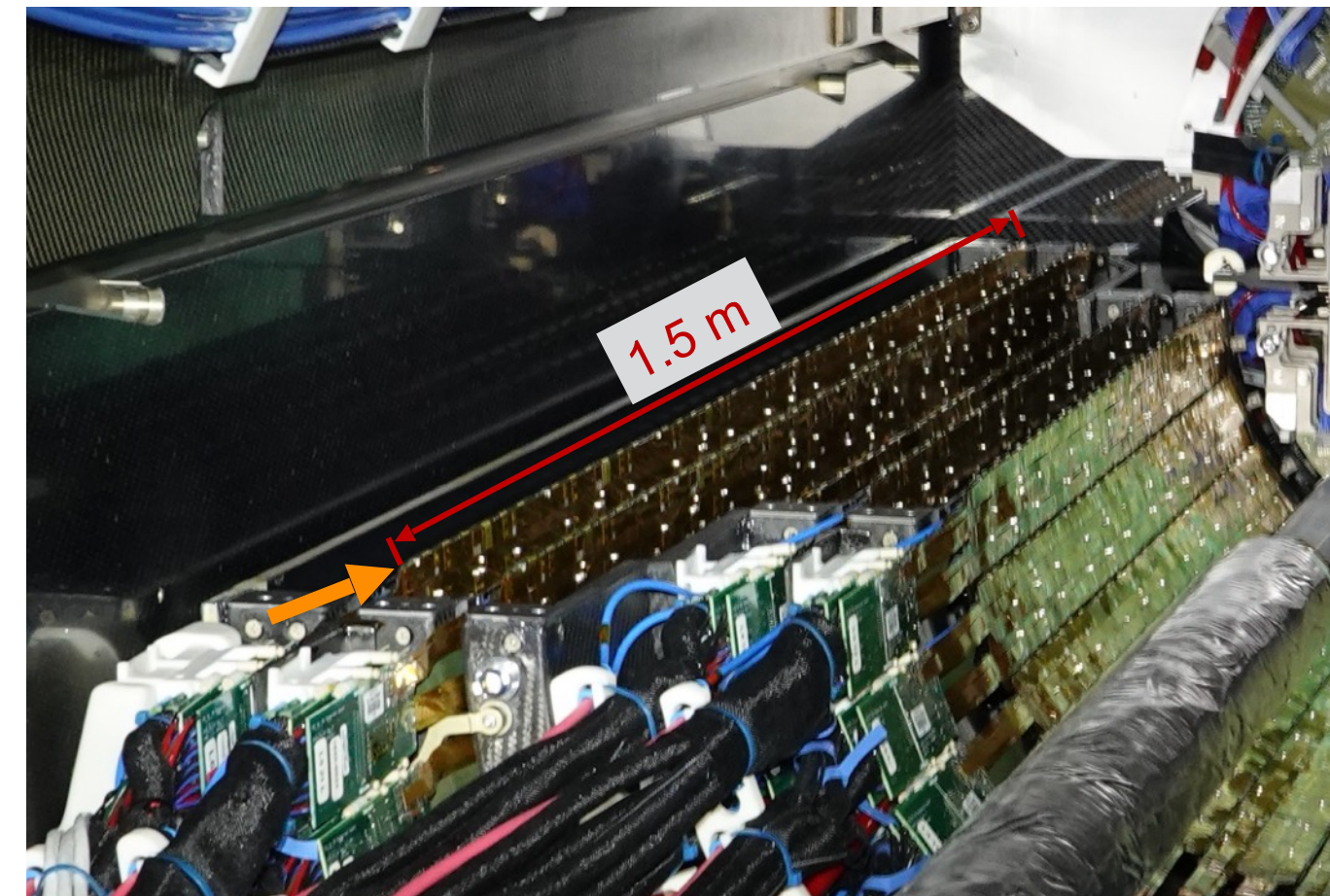
Outer Barrel Bottom being inserted on the rails inside the TPC



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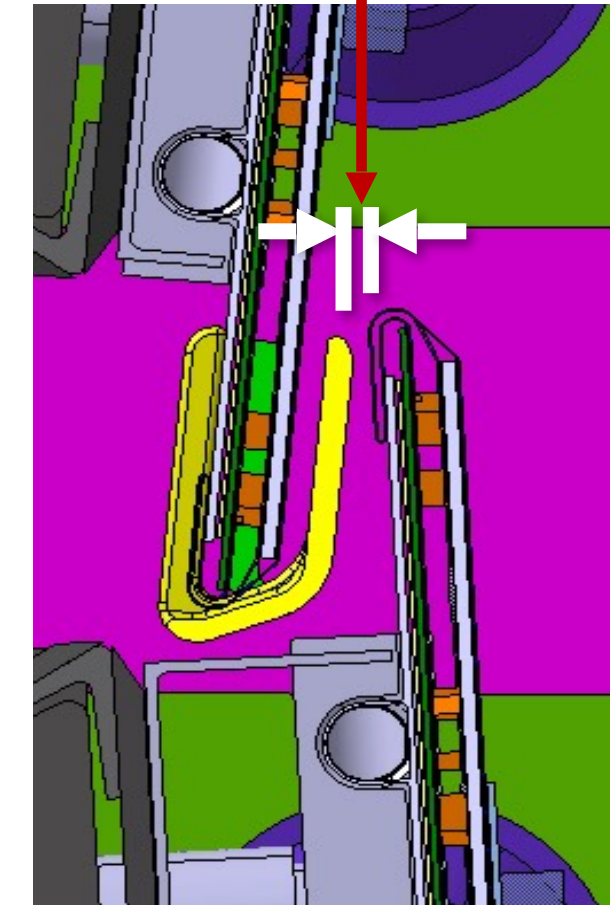
Installation - challenges

- Precise positioning of fragile objects inside the TPC bore
 - manipulating from a few meters distances
 - difficult to actually see the position by eye
- Dry installation tests on-surface to exercise and test the procures
- Use of 3d scans, surveys and cameras

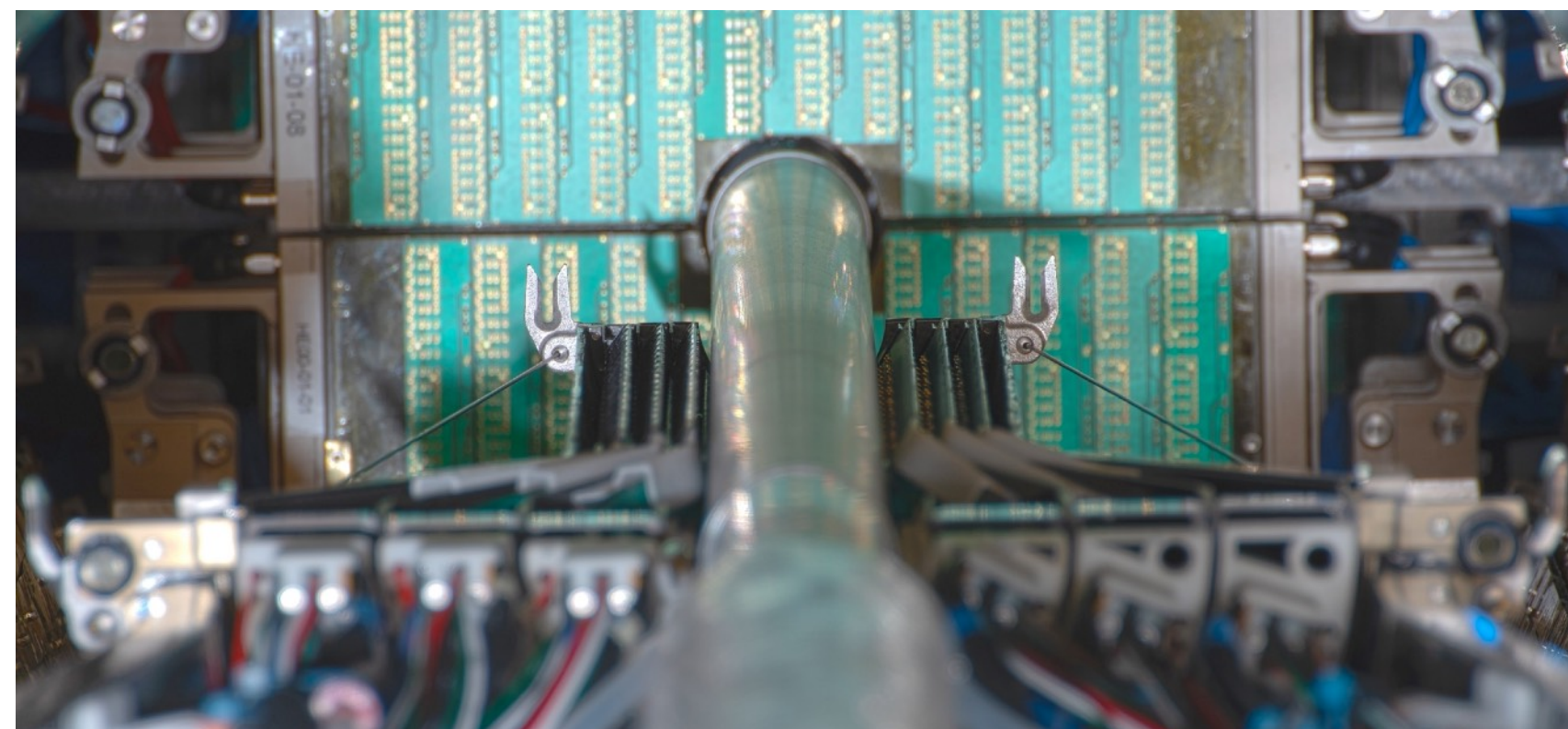


OB stave edge

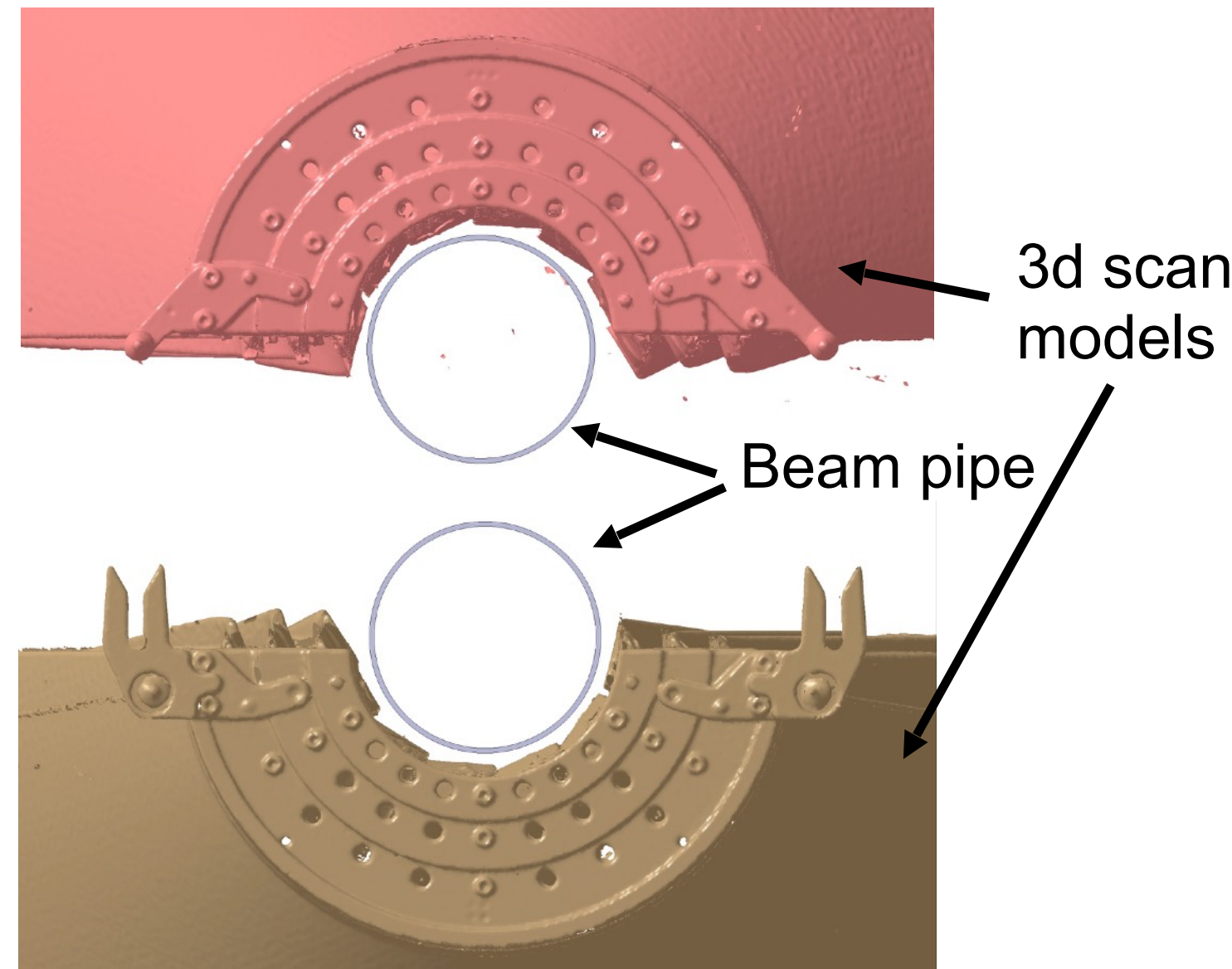
1.2 mm nominal clearance



OB stave edge clearance when fully mated

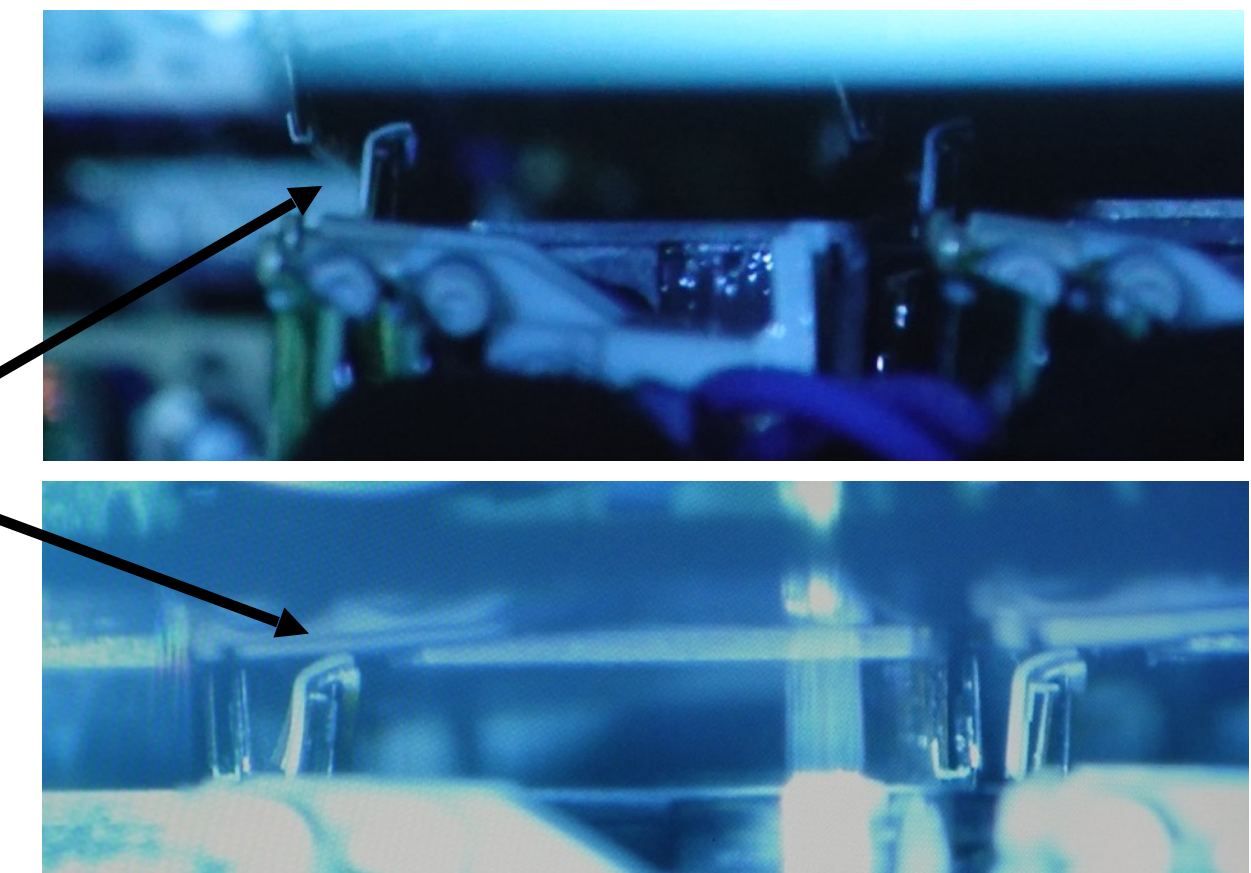


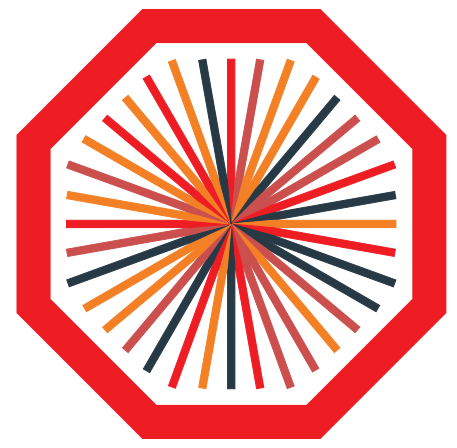
IB Bottom in the final position



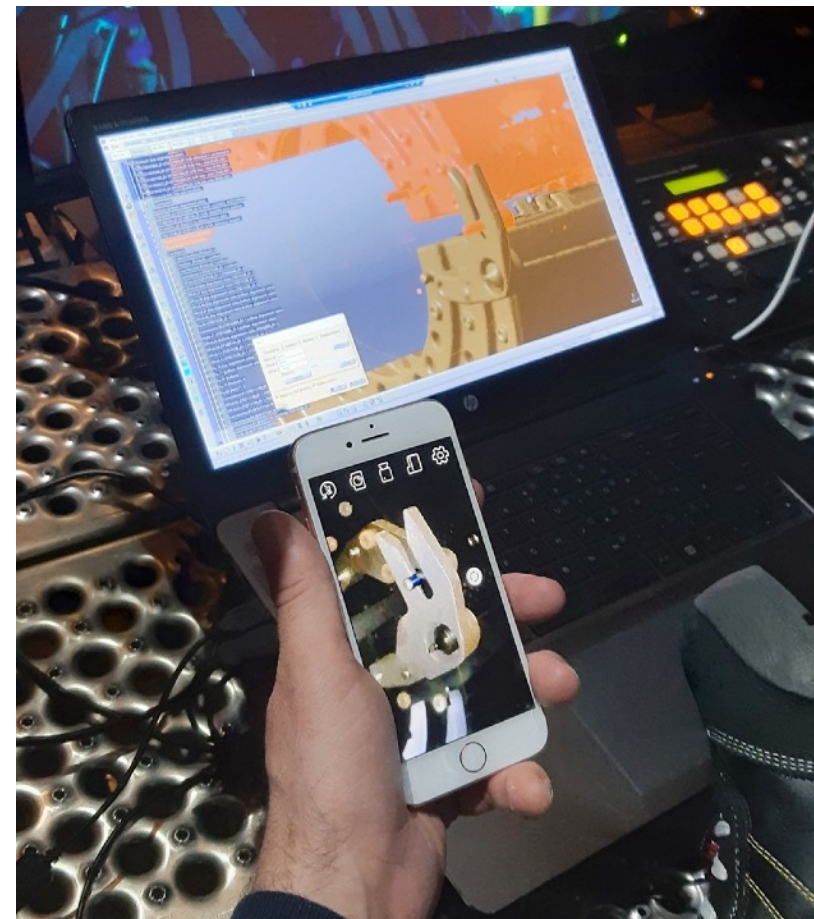
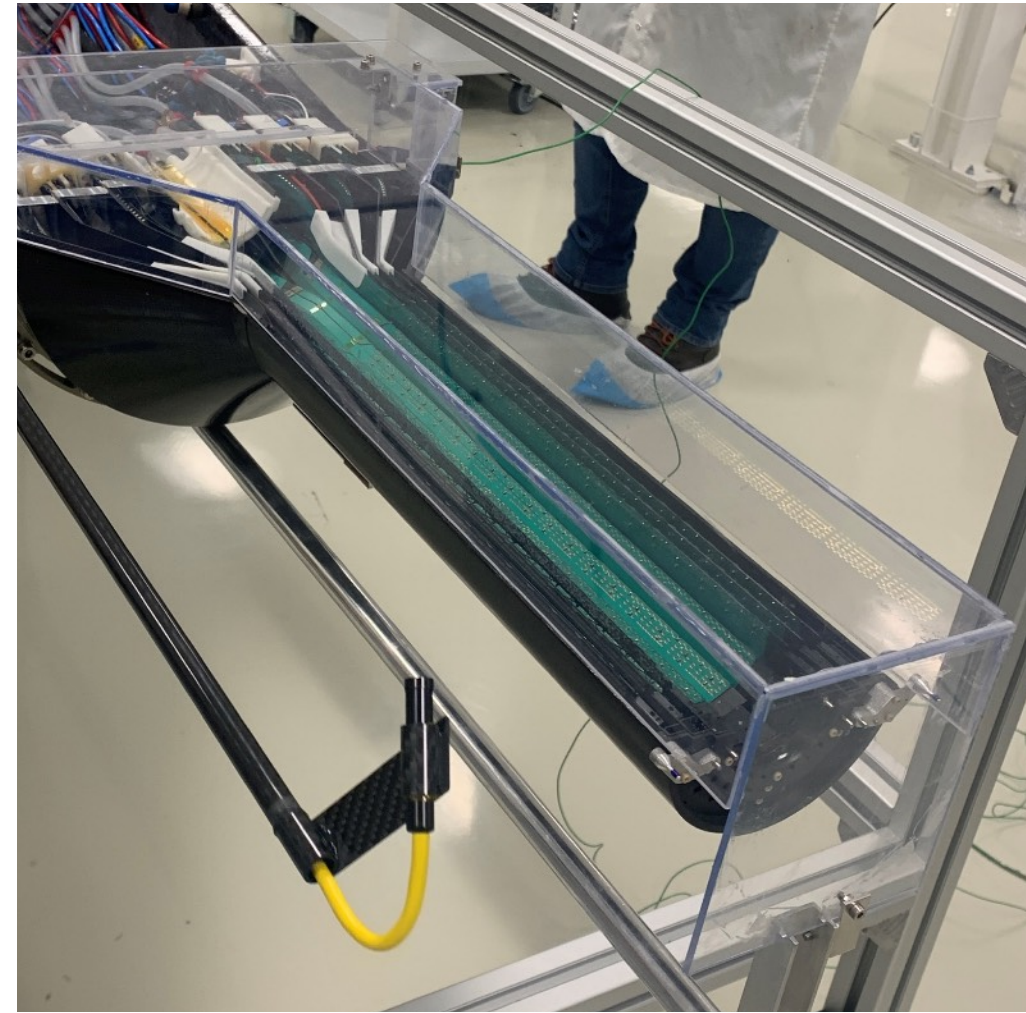
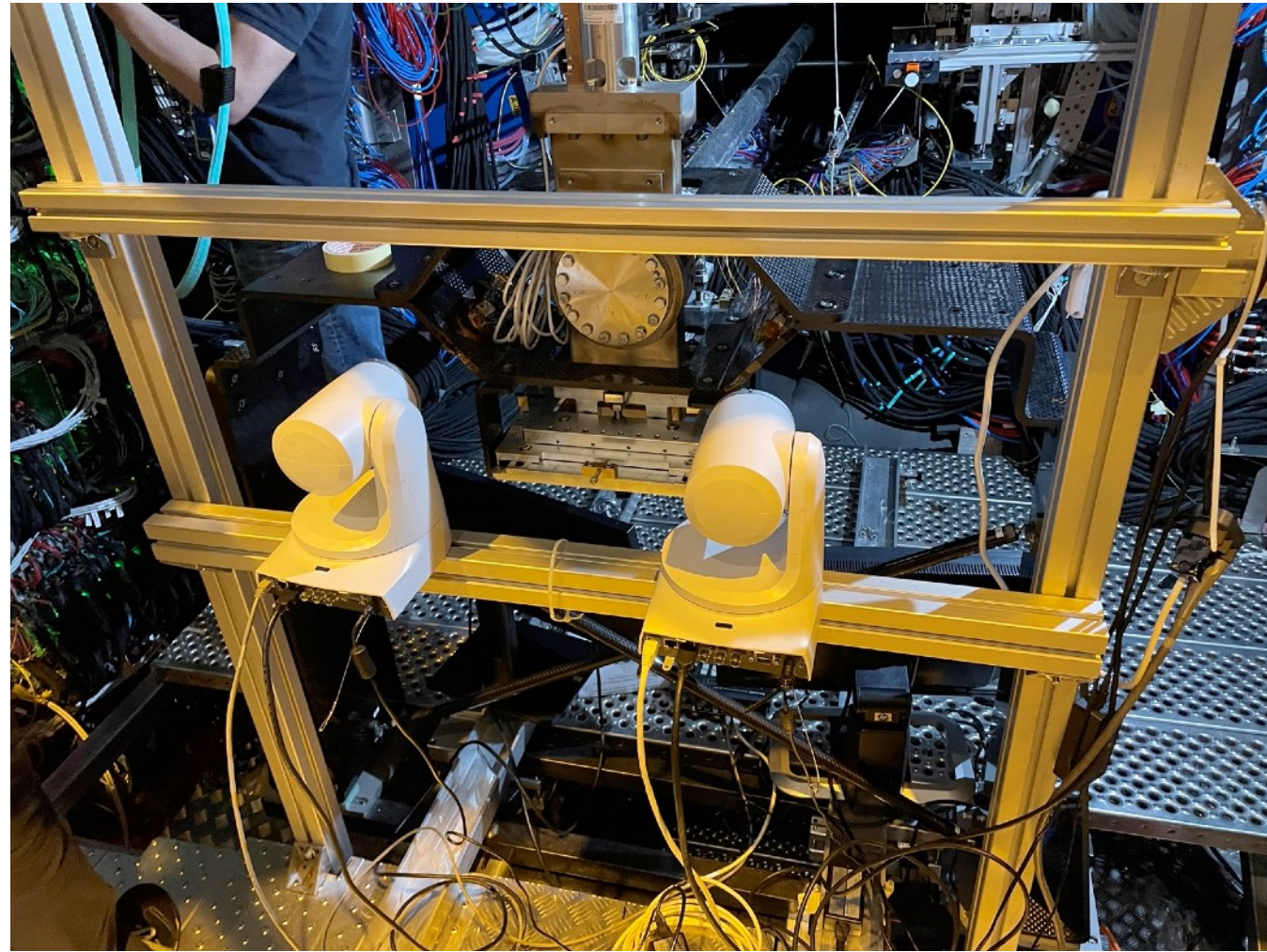
3d scans of IB Half-Barrels next to beam pipe, based on survey

OB stave edges





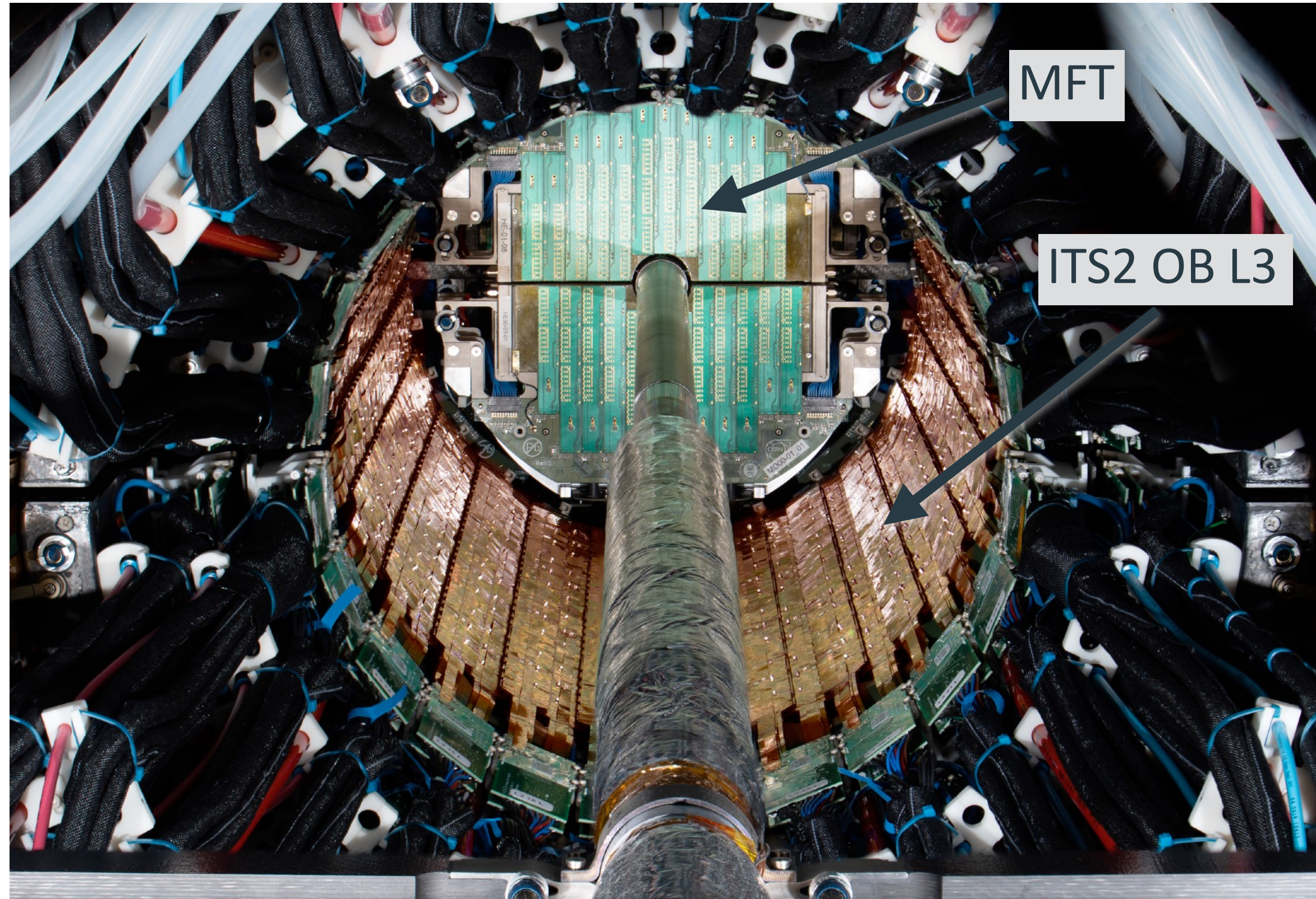
Inner Barrel final positioning



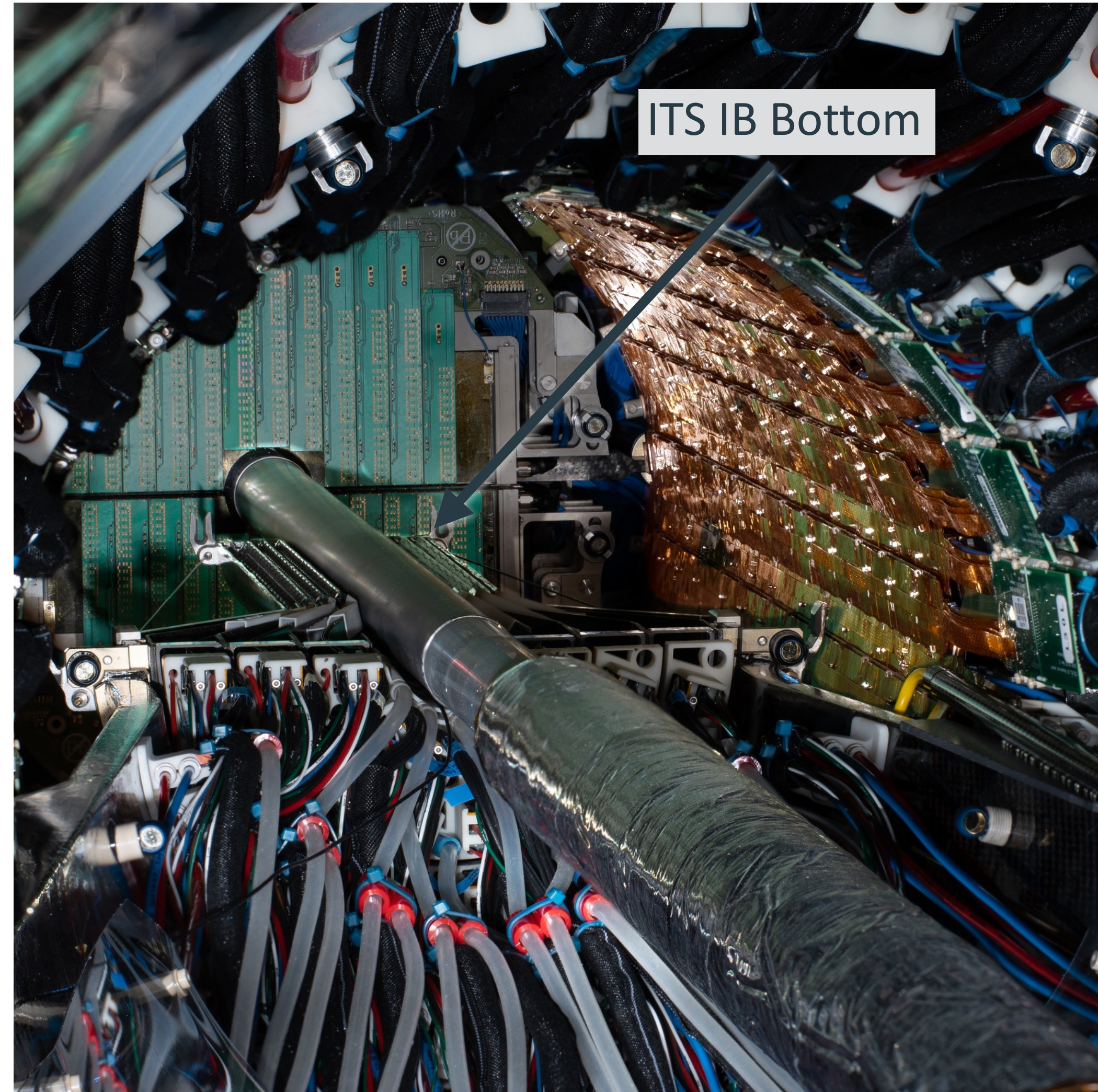
Real time verification using 6 cameras
+ comparison to 3D CAD scans



ITS2 in the ALICE experimental apparatus



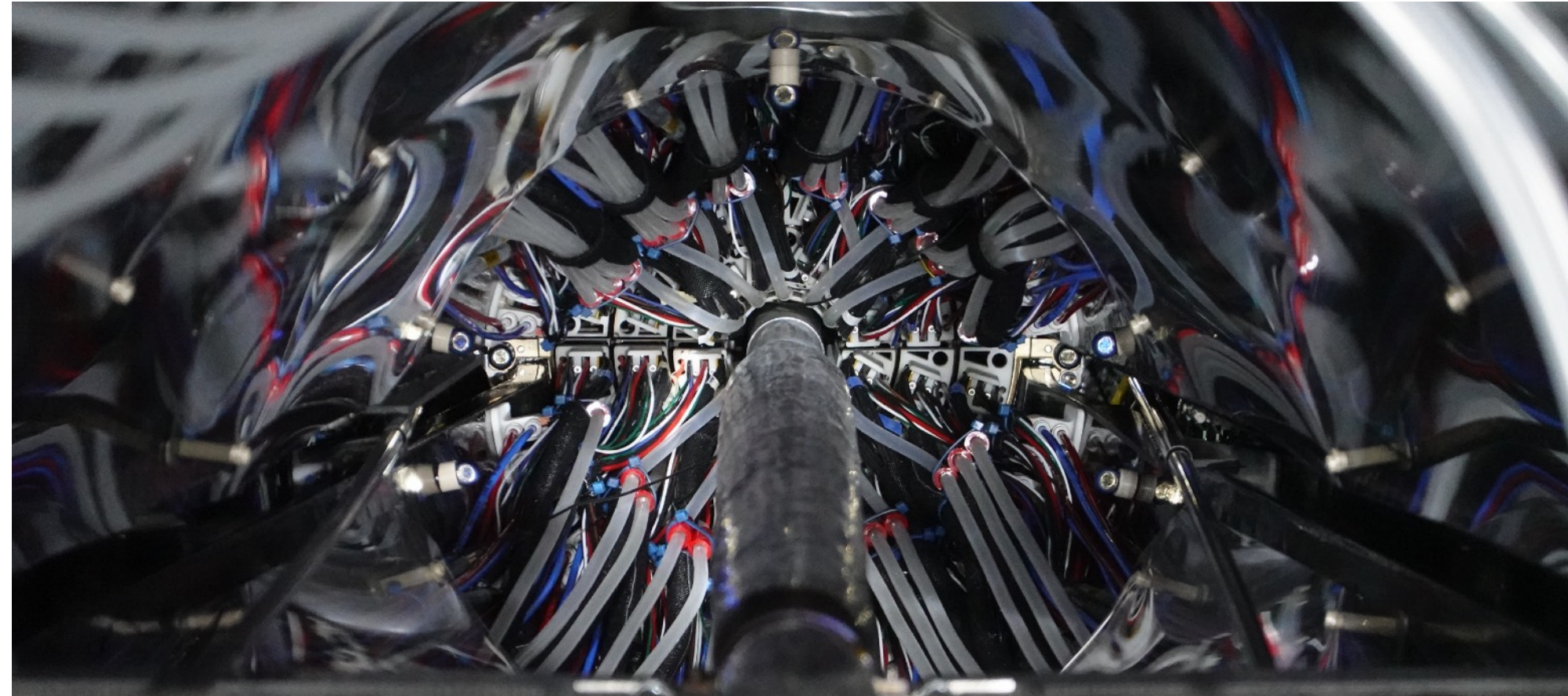
ITS Outer Barrel surrounding the beam pipe, MFT in the back



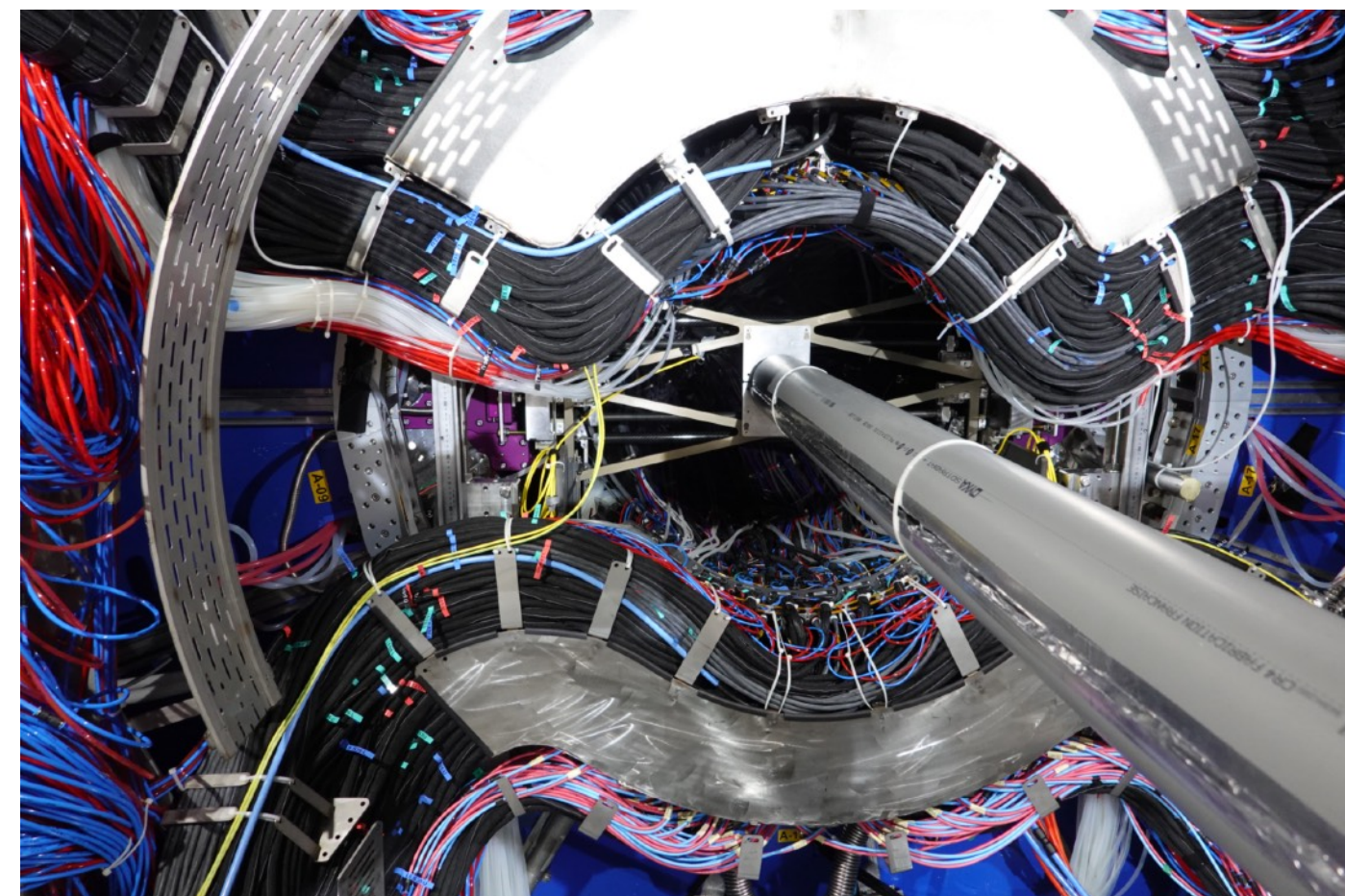
ITS Inner Barrel Bottom and Outer Barrel



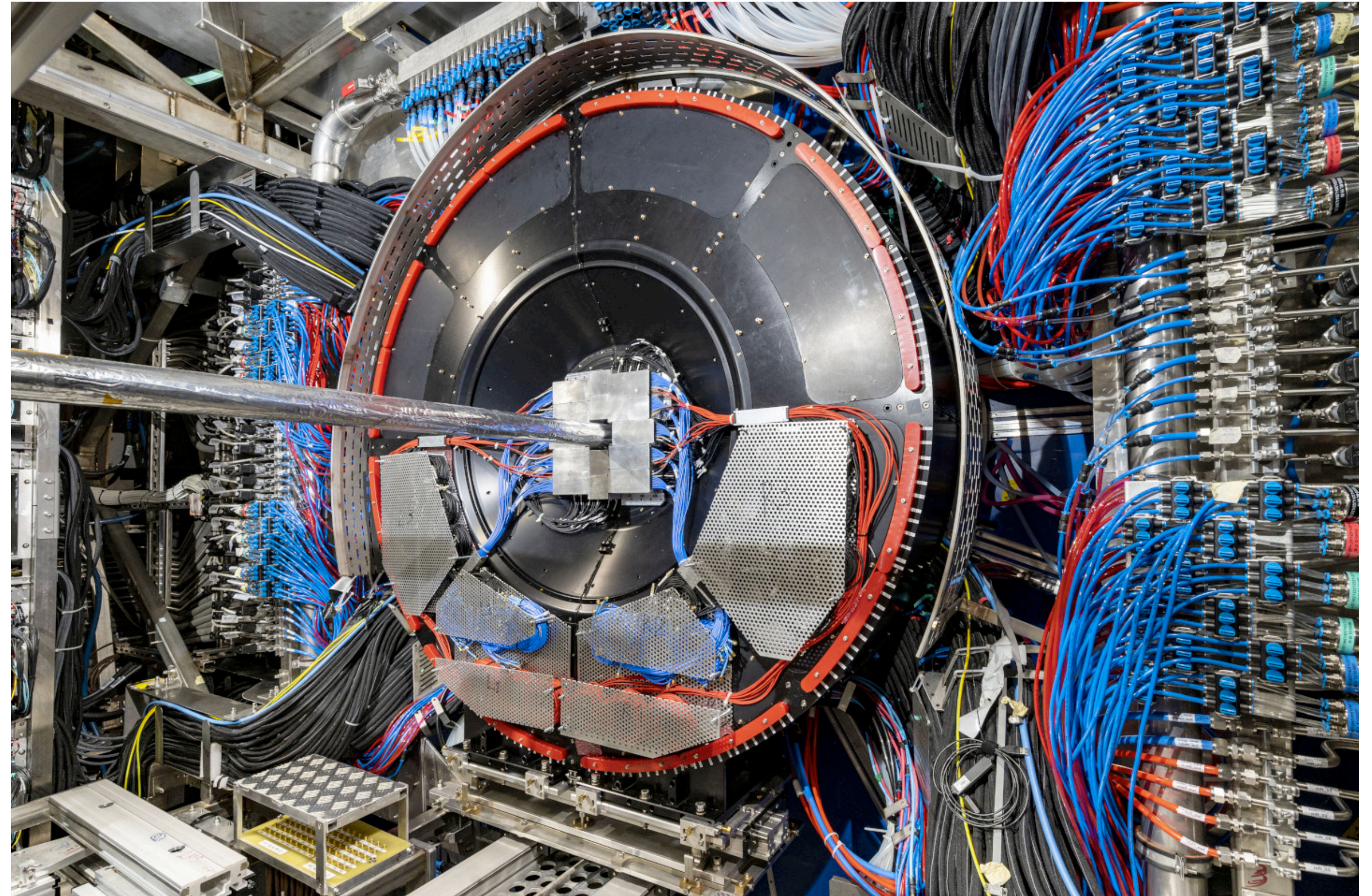
ITS2 in the ALICE experimental apparatus now



Inner Barrel fully mated - only services visible



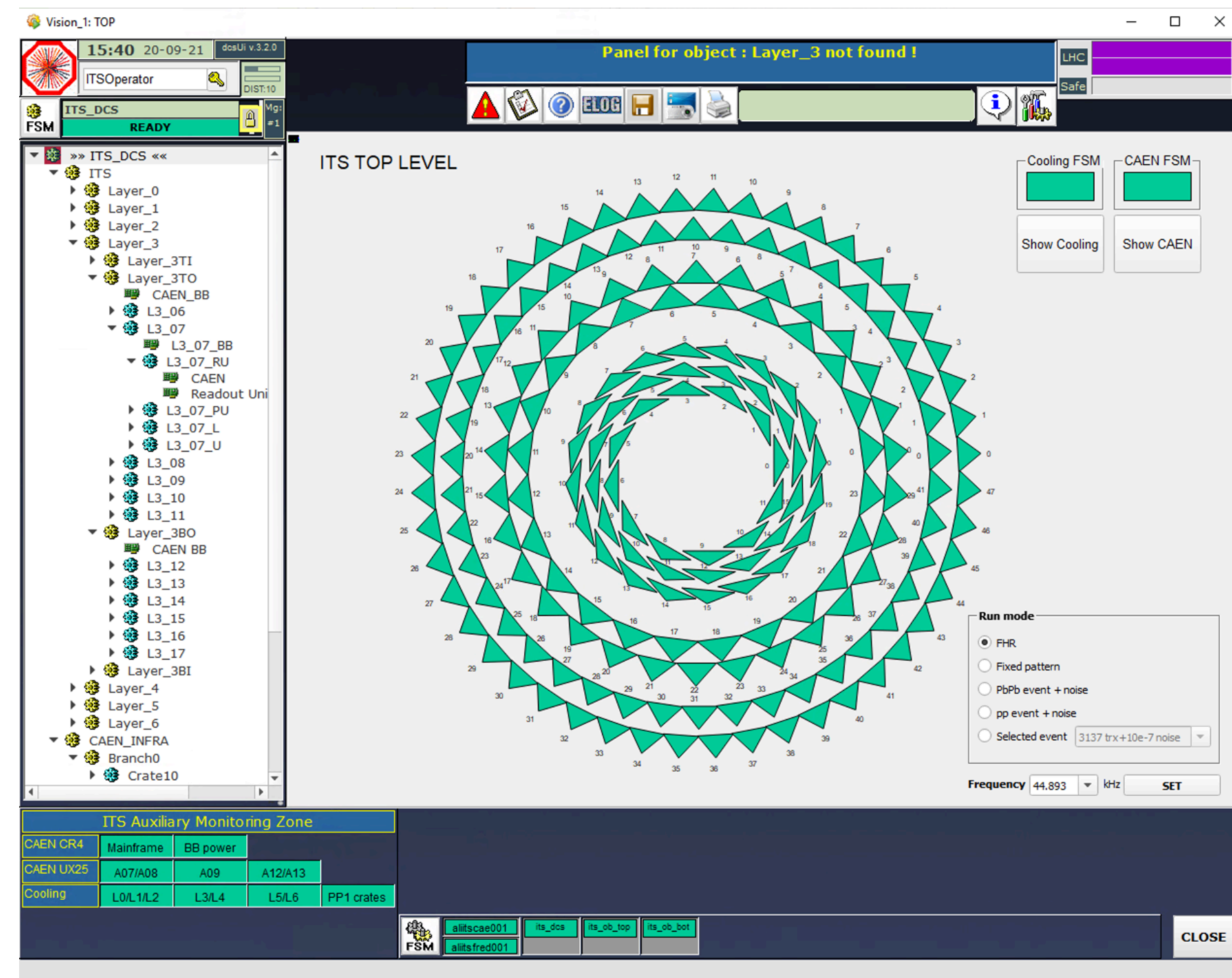
Full services connected (data, power, cooling, ventilation)



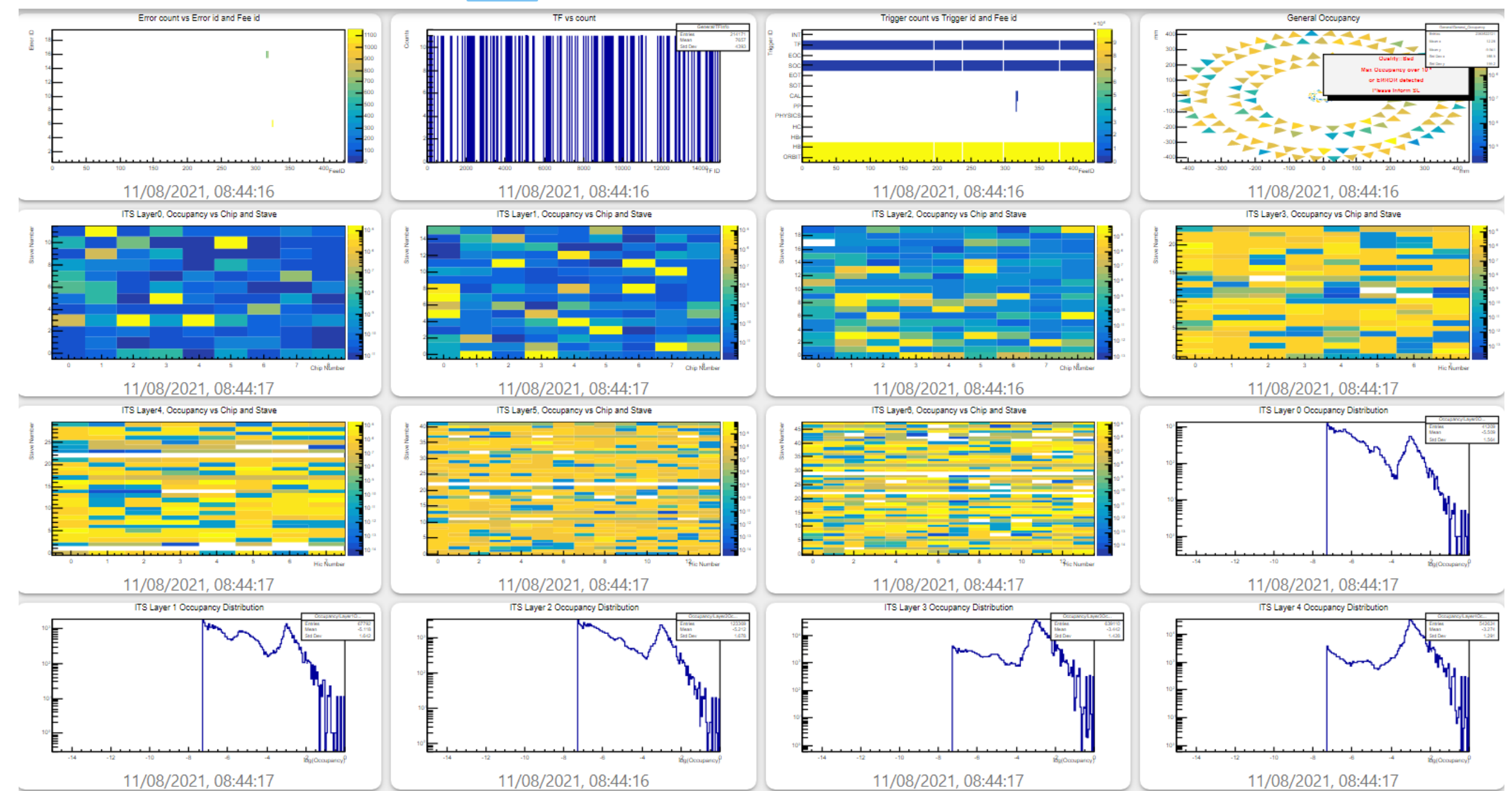
Fast Interaction Trigger (FIT) installed in front of the ITS and its services

Verification and commissioning in the cavern

- Tested connectivity and stability (running 24/7) after every integration step
- Focus on central system integration and operational experience with the final tools
- Detector Control System (DCS) and QC well advanced



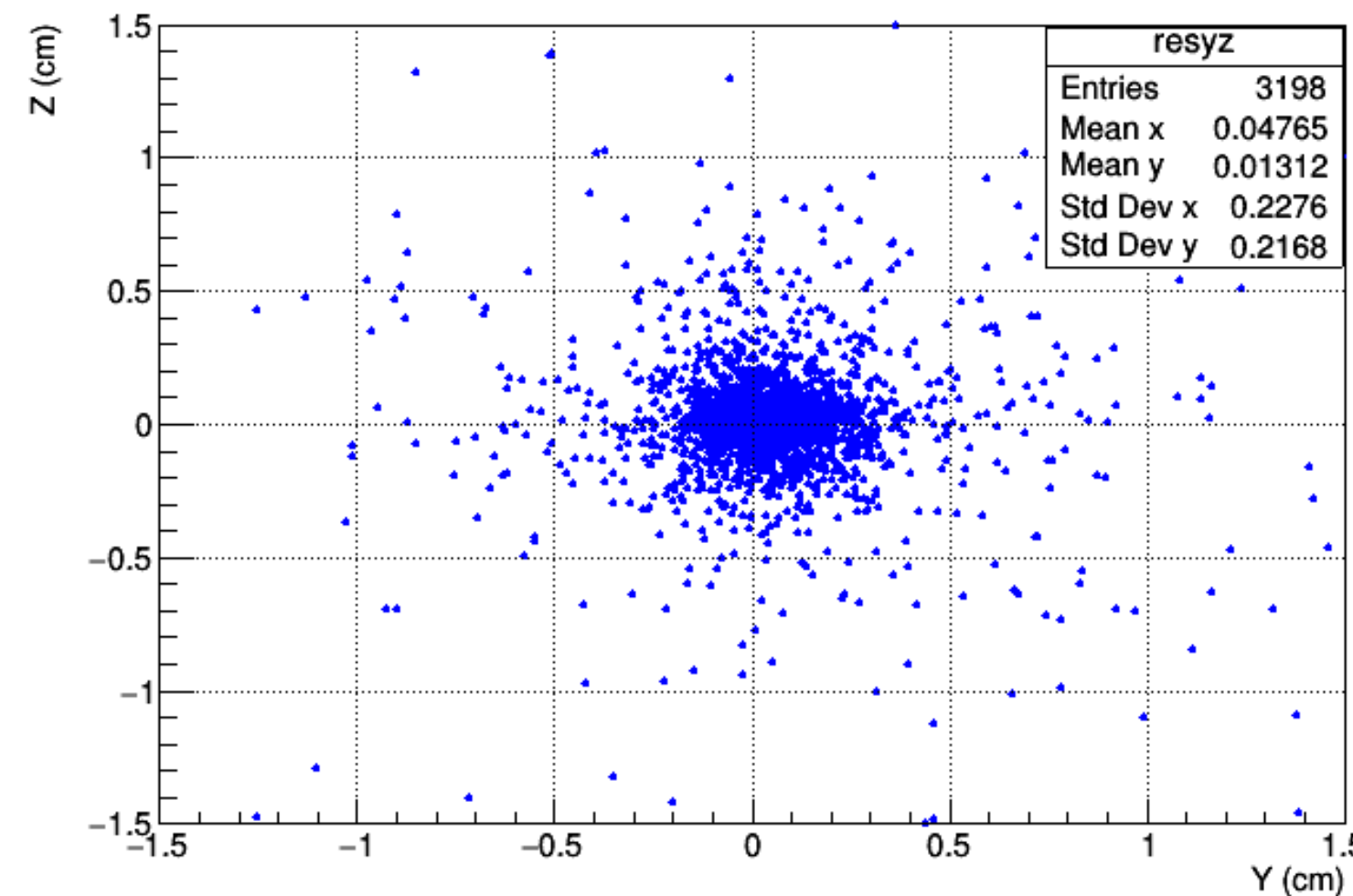
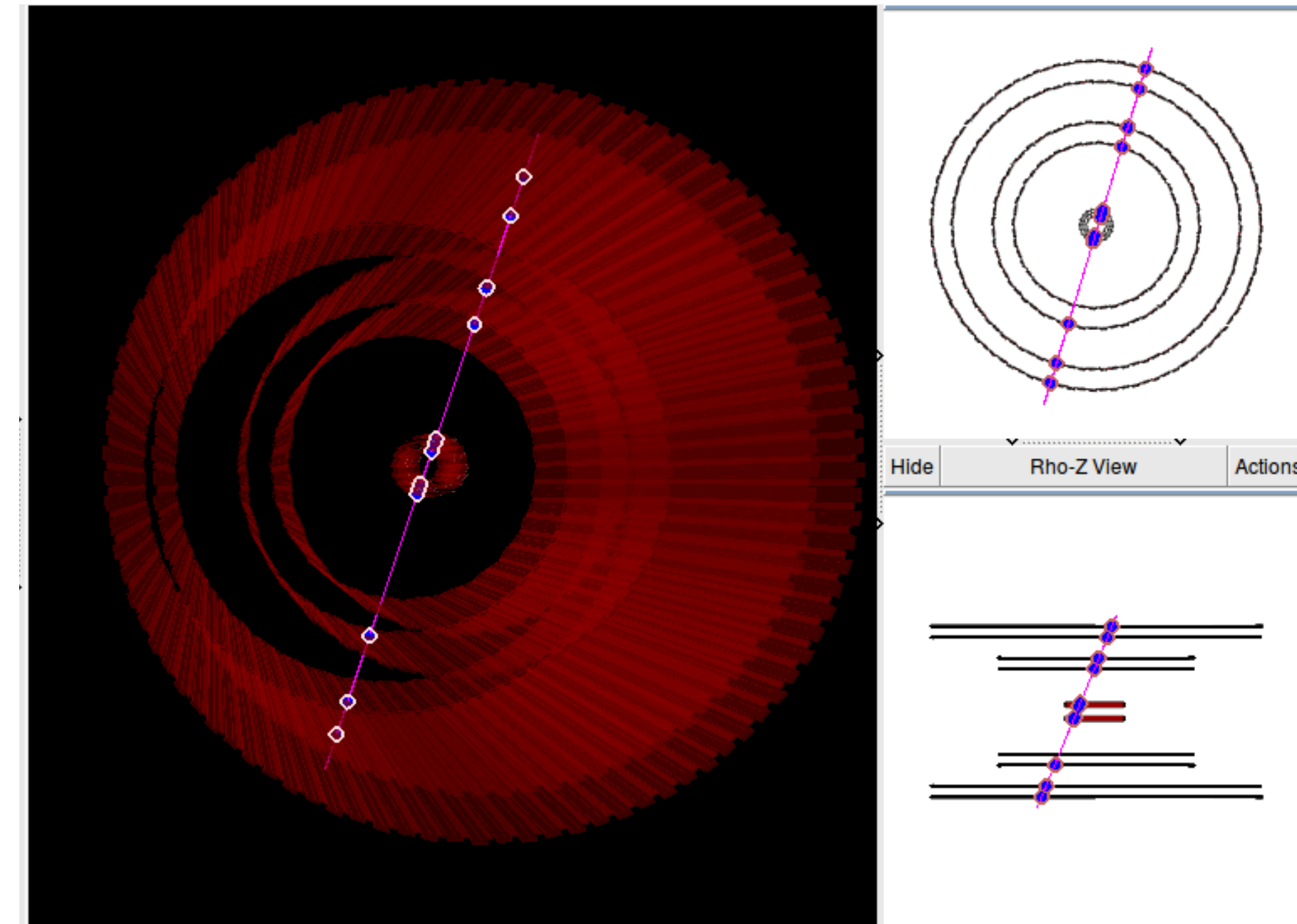
DCS main panel



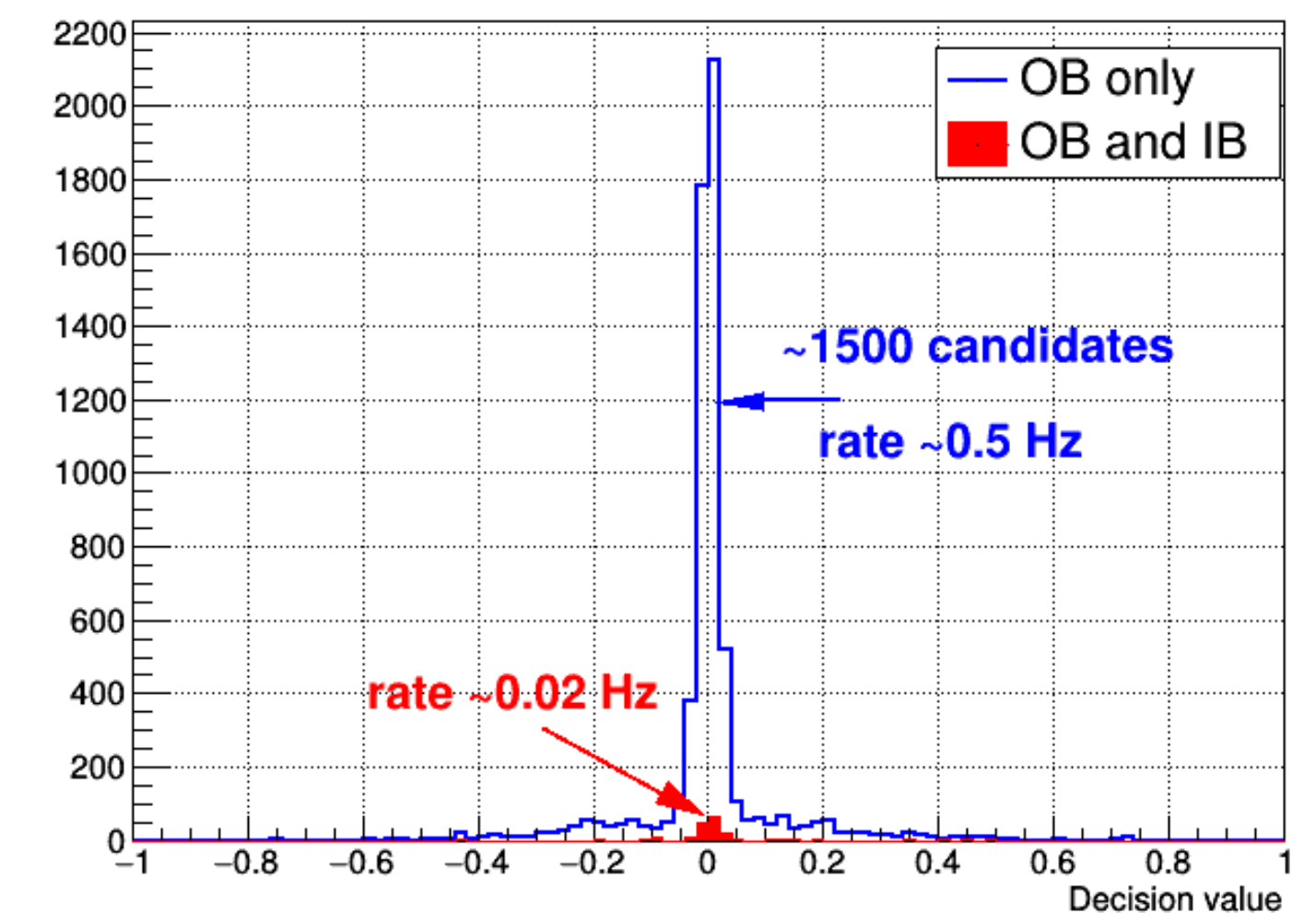
Occupancy view of the ITS2 QC

Cosmic muons in the cavern

- Data taking in the final system (services, trigger network, readout chain)
- Analysis of first tracks ongoing
- Cosmic track rate:
 - Outer Barrel: 0.5 Hz
 - Full detector (Outer Barrel + Inner Barrel): 0.02 Hz
- Residuals prior alignment of $O(1\text{ mm})$
 - Excellent hardware alignment
 - Mapping of chips correct
- Next steps:
 - Detector alignment
 - Track reconstruction



Residual of a straight line extrapolation to L6 before alignment



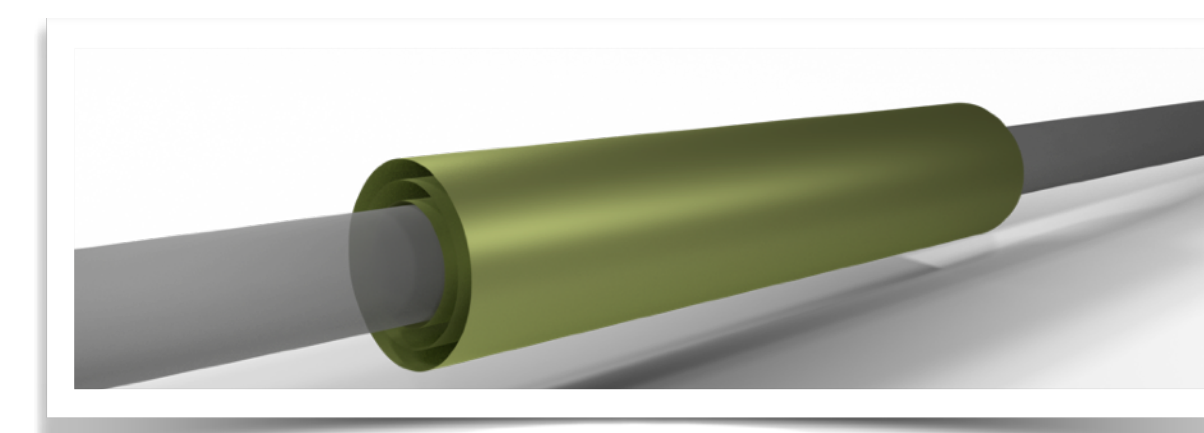
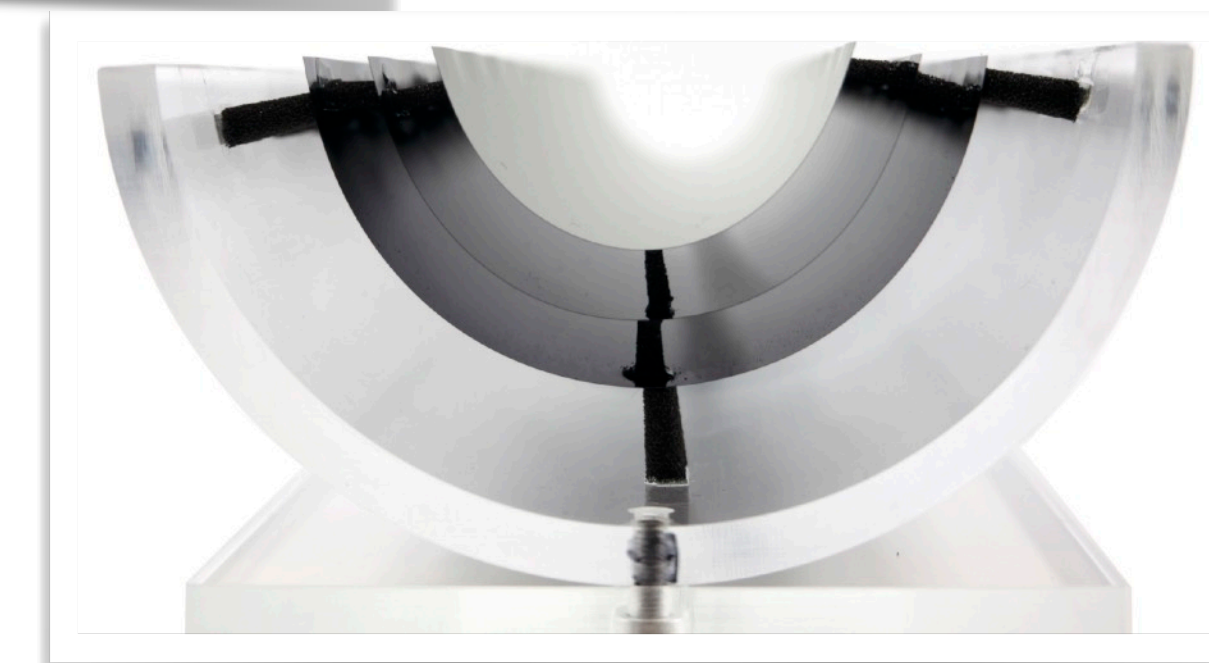
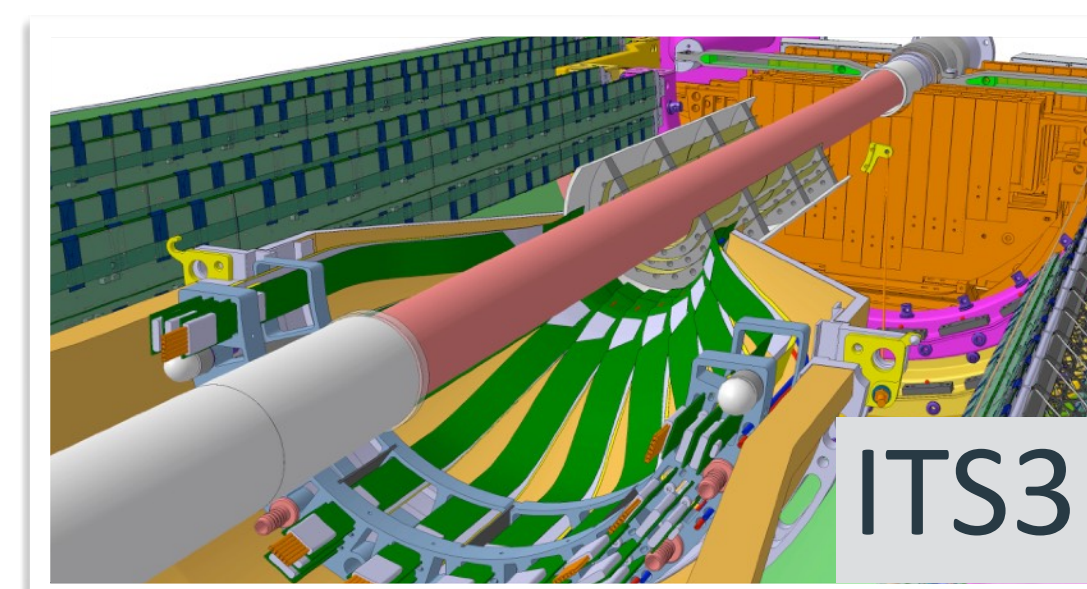
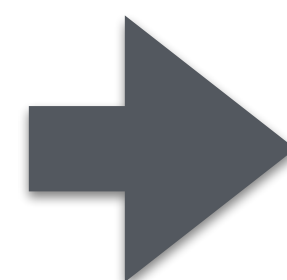
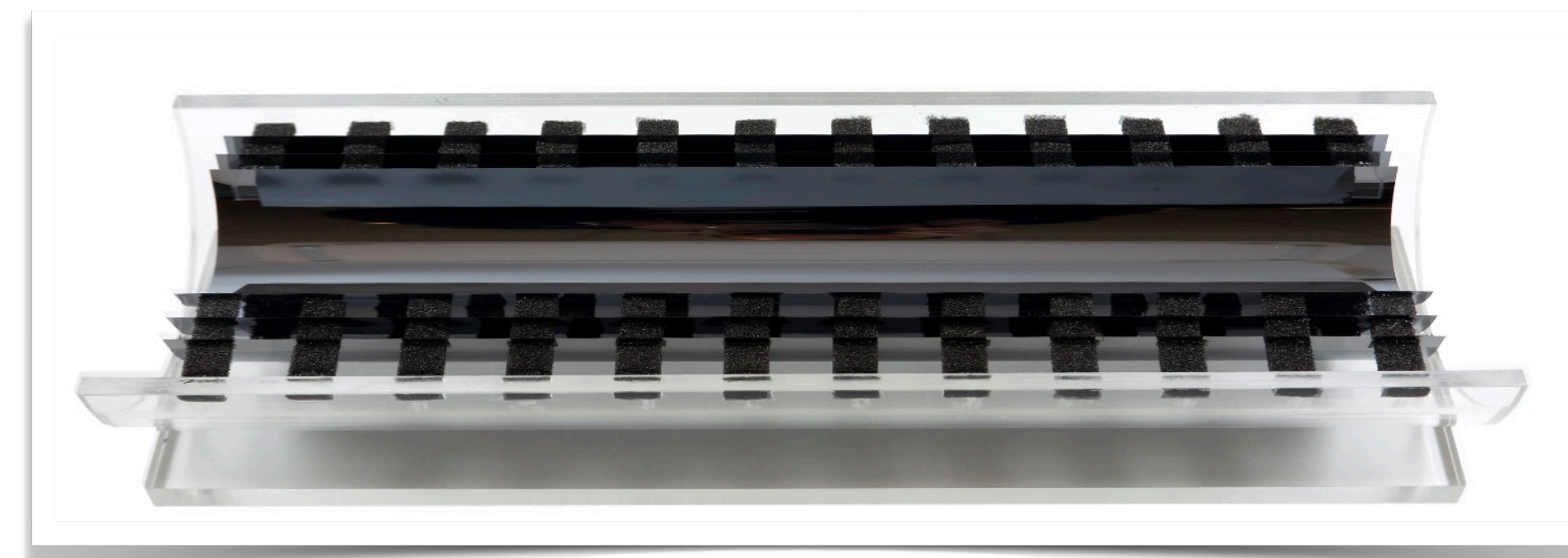
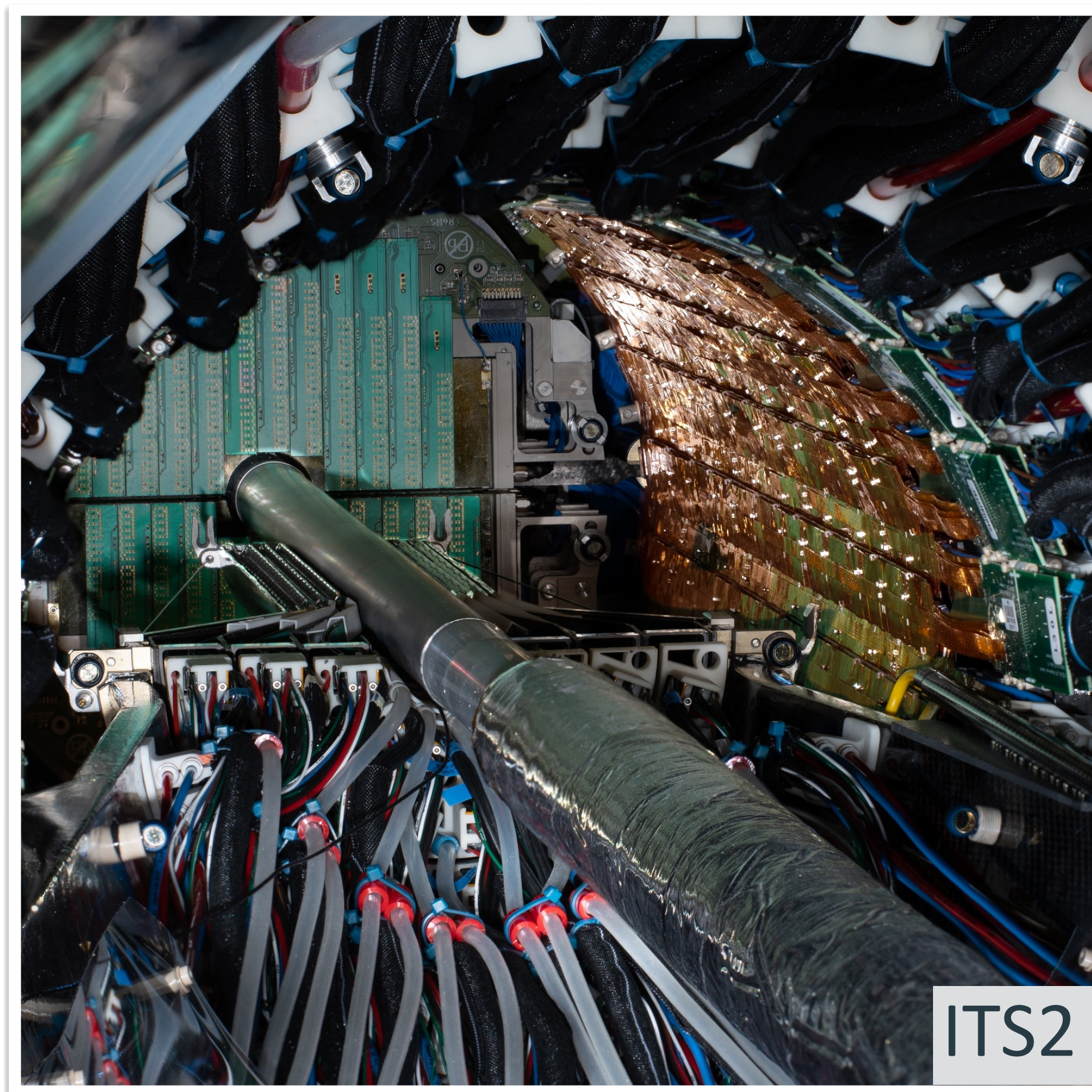
Track straightness based on hits in layers 4, 5 and 6

Summary

- Single chip performance confirmed during on-surface commissioning
- Detector fully installed
- Detector verified after installation
- First cosmic tracks observed
- Getting ready for the pilot beam test in October



ITS Outer Barrel during insertion tests



**Thank you for
your attention!**

ITS3 talks:

Development of bent silicon vertex detectors for ALICE in the LHC Run 4 - Matthew Buckland - on Tuesday 28th

Analysis of test beam data with bent MAPS sensors for the ALICE upgrade - Nicolo Jacazio - on Tuesday 28th