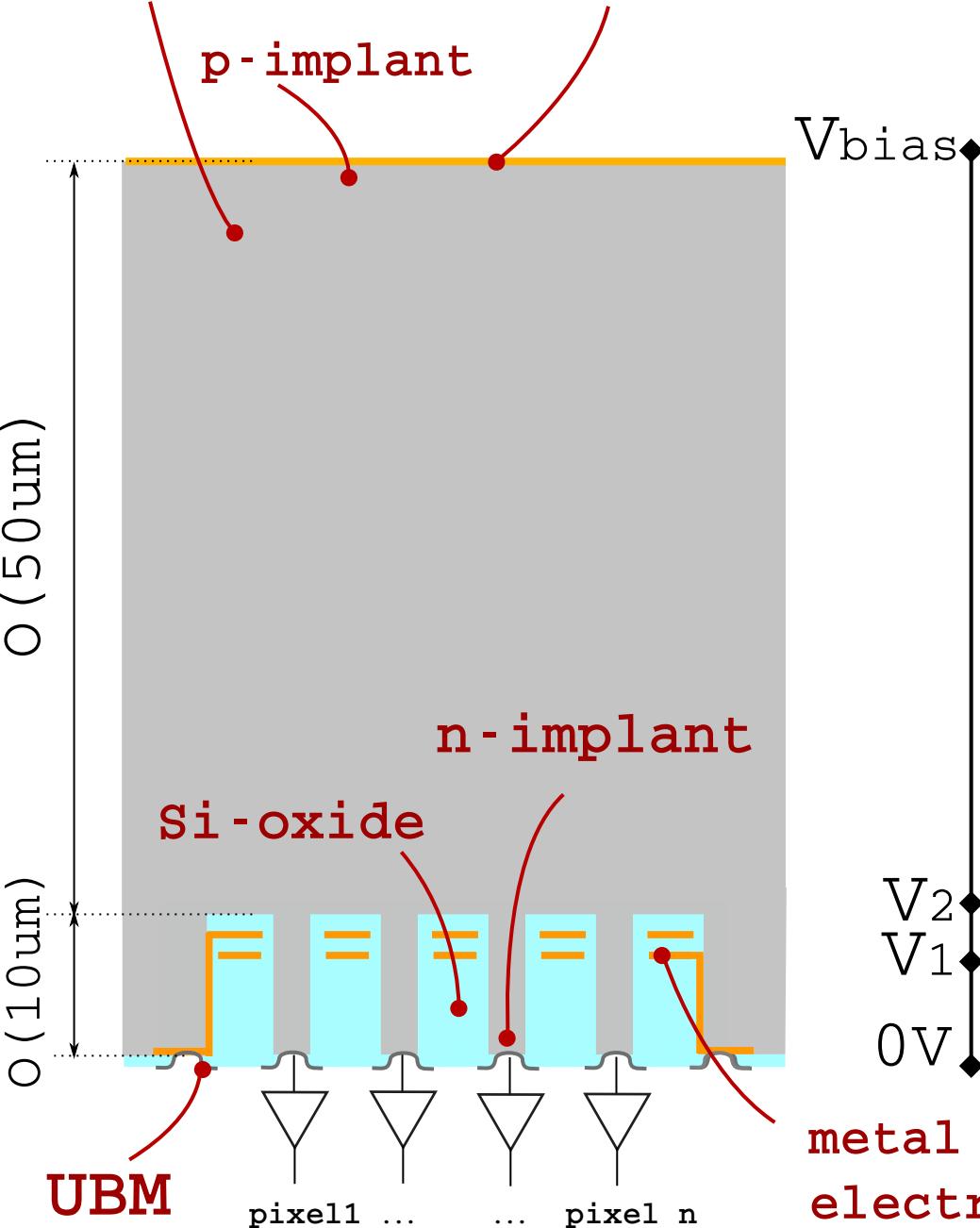


silicon bulk metallisation



V_{bias}

*Conversion
and Drift*

V_2
 V_1
0V

*Amplification
Transfer*

metal
electrode