



nexperia


BTTB 10

Career Paths Session

Jun 2022 • DHAM • Dr. Hendrik Jansen


# My CV in a nutshell

## Education




**The University of Bonn**  
Doctor of Philosophy - PhD, Experimental Physics  
Research performed at CERN within the ATLAS collaboration.

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**RWTH Aachen University**  
Diplom, Physics  
2003 - 2009  
Activities and societies: Search for Unparticles at CMS in the Di-Muon plus MET channel

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**Universidad Autónoma de Madrid**  
Physics  
Sep 2006 - Jul 2007  
ERASMUS year abroad

## Experience



**Program Manager Business Excellence**  
Nexperia · Full-time  
Nov 2021 - Present · 8 mos  
Hamburg, Germany  
Coordinator of various growth programs for the Hamburg site


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**DESY**  
7 yrs 6 mos


- Project Leader**  
Full-time  
Mar 2019 - Aug 2021 · 2 yrs 6 mos  
Hamburg Area, Germany  
Project Leader of R&D project in medical imaging and of R&D project in silicon sensor development
- Project Leader**  
Full-time  
Mar 2017 - Mar 2019 · 2 yrs 1 mo  
Project Leader of R&D project in silicon particle detectors
- Research Fellow**  
Mar 2014 - Feb 2017 · 3 yrs  
Hamburg Area, Germany

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**Lecturer (B. Sc. Media Technology)**  
Hamburg University of Applied Sciences · Contract  
Mar 2020 - Jul 2021 · 1 yr 5 mos  
Hamburg, Germany  
Lecturer of basic physics course within B.Sc. degree "Medientechnik".


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**Research Associate**  
CERN · Full-time  
May 2010 - Dec 2013 · 3 yrs 8 mos  
Geneva Area, Switzerland

# My CV in a nutshell – Technical Experience


## Education

 **The University of Bonn**  
Doctor of Philosophy - PhD, Experimental Physics  
Research performed at CERN within the ATLAS collaboration.


 **RWTH Aachen University**  
Diplom, Physics  
2003 - 2009  
Activities and societies: Search for Unparticles at CMS in the Di-Muon plus MET channel

 **Universidad Autónoma de Madrid**  
Physics  
Sep 2006 - Jul 2007  
ERASMUS year abroad

### CVD Diamonds, instrumentation

+ 

### Analysis, ROOT

 **DESY**  
7 yrs 6 mos

- Principal investigator**  
Full-time  
Mar 2019 - Aug 2021 · 2 yrs 6 mos  
Hamburg Area, Germany


### Silicon R&D, Imaging

Project Leader of R&D project in medical imaging and of R&D project in silicon sensor development
- Principal investigator**  
Full-time  
Mar 2017 - Mar 2019 · 2 yrs 1 mo

### Silicon R&D


Project Leader of R&D project in silicon particle detectors
- Research Fellow**  
Mar 2014 - Feb 2017 · 3 yrs  
Hamburg Area, Germany

### Test beam, integration, instrumentation

 **Lecturer (B. Sc. Media Technology)**  
Hamburg University of Applied Sciences · Contract  
Mar 2020 - Jul 2021 · 1 yr 5 mos  
Hamburg, Germany


### Teaching


Lecturer of basic physics course within B.Sc. degree "Medientechnik".


 **Research Associate**  
CERN · Full-time  
May 2010 - Dec 2013 · 3 yrs 8 mos  
Geneva Area, Switzerland

# My CV in a nutshell – Transferable skills

## Education

 **The University of Bonn**  
Doctor of Philosophy - PhD, Experiment  
Research performed at CERN within the ATLAS experiment

 **RWTH Aachen University**  
Diplom, Physics  
2003 - 2009  
Activities and societies: Search for Unparticles at CMS in the Di-Muon plus MET channel

 **Universidad Autónoma de Madrid**  
Physics  
Sep 2006 - Jul 2007  
ERASMUS year abroad

Find solutions to produce a result

→ Demonstrate that you can deliver a results in time and in quality within a given scope



**DESY**  
7 yrs 6 mos

• **Project Leader**  
Full-time  
Mar 2019 - Aug 2021 · 2 yrs 6 mos  
Hamburg Area, Germany

Project Leader of R&D project in medical imaging and detector development

• **Project Leader**  
Full-time  
Mar 2017 - Mar 2019 · 2 yrs 1 mo

Project Leader of R&D project in silicon particle detector development

• **Research Fellow**  
Mar 2014 - Feb 2017 · 3 yrs  
Hamburg Area, Germany

Projekt Mgmt.

→ Quality Assurance

→ Budget Mgmt.

→ Stakeholder Mgmt.

→ Change Mgmt.



**Lecturer (B. Sc. Media Technology)**  
Hamburg University of Applied Sciences · Contract  
Mar 2020 - Jul 2021 · 1 yr 5 mos  
Hamburg, Germany

Lecturer of basic physics course within B.Sc. degree "Media Technology"

End-to-end responsible for a given task

→ Resource Mgmt.

→ Stakeholder Mgmt.



**Research Associate**  
CERN · Full-time  
May 2010 - Dec 2013 · 3 yrs 8 mos  
Geneva Area, Switzerland

# Nexperia in a nutshell

Nexperia is a leading expert in the high-volume production of essential semiconductors.

The portfolio includes diodes, bipolar transistors, ESD protection devices, MOSFETs, GaN FETs and analog & logic ICs.

**6,000+** Automotive qualified products  
**200+** Packages  
**21,000+** Documents  
**400+** Jobs

**Bipolar transistors**  
DFN2020-6

**Diodes**  
DFN1006

**ESD protection, TVS, signal conditioning**  
DFN1006

**MOSFETs**  
DFN1006

**GaN FETs**  
DFN1006

**Analog & Logic ICs**  
DFN1006

- > Automotive diodes
- > Zener diodes
- > Switching diodes
- > Schottky diodes and rectifiers
- > Recovery rectifiers
- > Silicon Germanium (SiGe) rectifiers




Sell close to 100 billion products / y

# My job at Nexperia

## Experience



 Program Manager Business Excellence

Nexperia · Full-time

Nov 2021 - Present · 8 mos

Hamburg, Germany

Coordinator of various growth programs for the Hamburg site



# Your job at Nexperia?

Standort ▼ Voll-/Teilzeit ▼ Tätigkeitsfamilie ▼

Hamburg × Newport × Manchester × Munich × Nijmegen × Research & Development ×

[Auswahl aufheben \(6\)](#)

52 STELLEN GEFUNDEN

## Senior Principal TCAD Engineer [↗](#)

Bewerben

Manchester Full time  
Vor 21 Tagen ausgeschrieben  
R-20004829

### About the role

Nexperia is a world-class leader in semiconductor development and in-house production committed to ongoing investment in next generation technologies, state of the art high-volume manufacturing facilities and above all, people!

We have an exciting opportunity for a Senior Principal TCAD Engineer to join our growing R&D team. As Senior Principal TCAD Engineer, you will hold a key role within TeamNexperia overseeing the development and improvement of future generation Power MOSFET performance.

### What you will do

- Develop and improve the performance of future generation Power MOSFET product families and technology platforms through TCAD expertise
- Partner with the Process Integration team to resolve device level issues and suggest improvements
- Innovate and provide technical expertise for the team as a consultant across one or many project(s)
- Suggest design of experiment (DOE), process conditions based upon

## (Sr.) Development Engineer - Transistors (m/f/d) [↗](#)

Bewerben

Hamburg Full time  
Vor mehr als 30 Tagen ausgeschrieben  
R-20004446

### What you will do

- Define, develop and release bipolar transistors
- Contribute to the product performance as product designer / architect for development of bipolar transistors
- Lead small or medium sized product creation or optimization projects
- Competitive benchmarking concerning products and technologies of Nexperia
- Build up and maintain a competency network within the organization
- Provide technical mentorship to less experienced engineers

## Interested?

[hendrik.jansen@nexperia.com](mailto:hendrik.jansen@nexperia.com)

## Project Leader (m/f/d) – Small Signal MOSFETs R&D

Bewerben

Hamburg Full time  
Gestern ausgeschrieben  
R-20007039

### What you will do

- Lead complex R&D or engineering projects
- Define the overall project plan in consultation / agreement with all relevant management levels and stakeholders
- Implement and realize according to the project plan and within agreed boundaries - specification, quality, timing, costs, information / reporting and organization
- Manage all requested changes and execute change management process
- Analyze project risks (w. r. t. specifications, costs, timing, etc.) and develop & incorporate scenarios to overcome these risks
- Define competencies and resources needed and build up a performing project team. Take team leadership including operational guidance and conflict management
- Ensure that relevant quality standards for own projects are defined and met, monitor & control execution of own projects
- Initiate and contribute to quality improvement activities related to business processes & procedures
- Upgrade and adapt own competence, use cross organizational sharing and learning within the teams and the group of project leaders

## Principal Validation Engineer [↗](#)

Bewerben

Nijmegen Full time  
Gestern ausgeschrieben  
R-20006895

For the **Test & Product Engineering Group**, within BG Analog & Logic IC's in Nijmegen, The Netherlands, we are looking for a **Principal Validation Engineer**. In this exciting role, you will play a major part in the exploration and development of new, exciting and challenging Analog & Logic Semiconductor products that are unknown to the world as of now.

### What you will do

Your main responsibilities are:

- Responsible for the quality and execution of IC product tests according to validation plan
- Responsible for development of new lab test methods and hardware
- Responsible for documentation of performed tests and results
- Analysis of competitor devices and application related customer complaints and requests
- Accountable to execute tasks according planning, find solutions to keep the planning on track
- Participate in Risk assessment (FMEA) and root-cause analysis (RCA)

EFFICIENCY WINS.