

## **WP1.1 Hybrid Silicon Detectors**

W. Byczynski, V. Coco, P. Collins, R. Dumps, M. Gose [EP R&D TECH],
V. Gkougkousis [EP R&D Fellow], J. Haimberger, M. Halvorsen [EP R&D DOCT],
M. Moll, H. Schindler, Viros Sriskaran [EP R&D FELLOW], ...

### **Motivations** MIP detection in next generation of collider experiments

from the CERN Strategic R&D Programme on Technologies for Future Experiments [CERN-OPEN-2018-006]

| [fineprint in CERN-OPEN-2018-006]                | HL-LHC             | SPS              | FCC-ee           | FCC-hh           |
|--|--------------------|------------------|------------------|------------------|
| Fluence [n <sub>eq</sub> /cm <sup>2</sup> /y]    | 5x10 <sup>16</sup> | 10 <sup>17</sup> | 10 <sup>10</sup> | 10 <sup>17</sup> |
| Max Hit rate [cm <sup>-2</sup> s <sup>-1</sup> ] | 2-4G               | 8G               | 20M              | 20G              |
| Material budget per layer [X <sub>0</sub> ]      | 0.1-2%             | 2%               | 0.3%             | 1%               |
| Pixel size [µm <sup>2</sup> ]<br>inner trackers  | 50x50              | 50x50            | 25x25            | 25x25            |
| Temporal hit resolution [ps]                     | ~50                | ~40              | -                | ~10              |

- Timing resolution **10 to 50ps**
- Pixel pitches **25 to 50μm**
- Fluences up to 10<sup>17</sup>n<sub>eq</sub>/cm<sup>2</sup>/y
- Max hit rate up to **20G/cm<sup>2</sup>/s**

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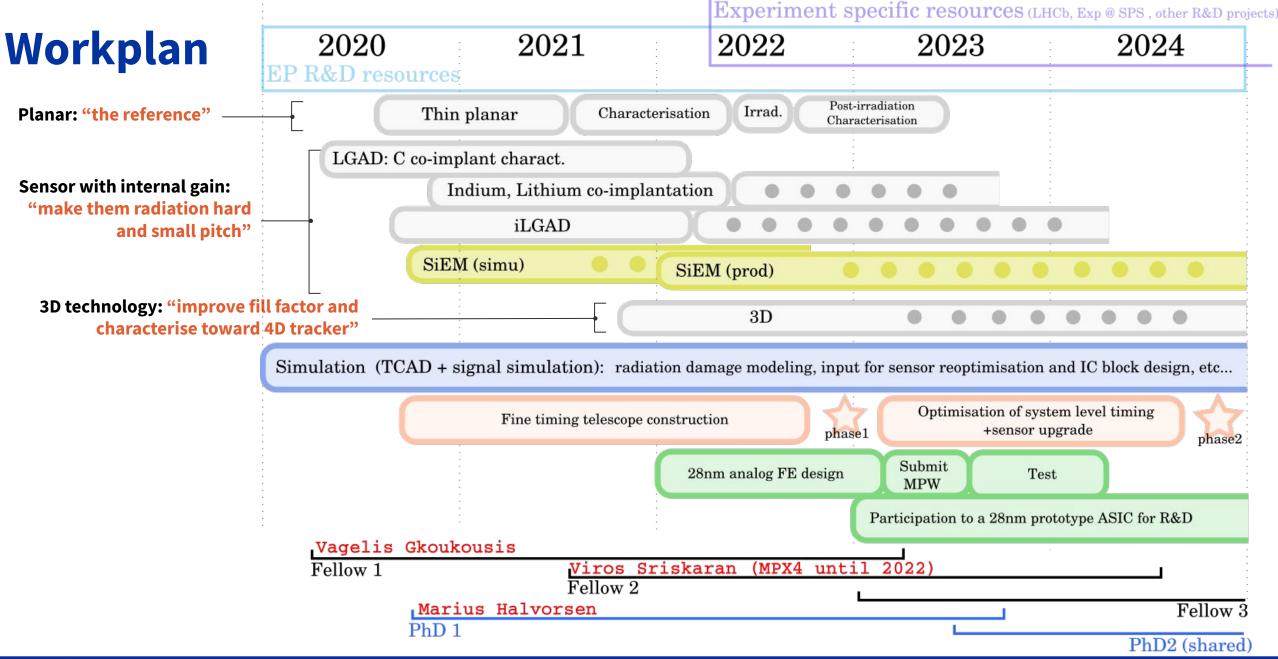
### **Challenges for sensor:**

- with **internal gain** (rad. hardness? segmentation?)
- with **small drift path** (**3D:** fill factor? capacitance? **Planar:** total charge?)
- with **integrated FE** (monolithic see WP 1.2)

### **Challenges for front-end electronics:**

- For hybrid sensor Timepix4 sets limits of what can be achieved in 65nm
- need to go beyond ⇒ 28nm design









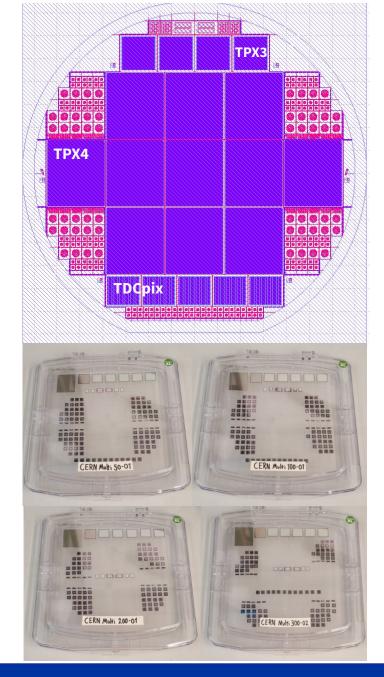
**Production and design** 



## **Planar Sensors production**

contact persons: V. Coco, P. Collins

- Tender awarded to **ADVACAM** January 2021
- **Production completed** August 2021
- n-in-p, four thicknesses: 50,100,200 and 300μm
- Sensors for various users:
  - Timepix4 (Telescope, Sensor R&D, Medipix collaboration)
  - Timepix3 (Sensor R&D)
  - TDCpix (Sensor R&D, NA62 upgrade studies)
- Several **test structures** (PIN-diodes, small matrices, structure for resistivity and doping profile measurements)
  - important part of the R&D program
  - see slide 20

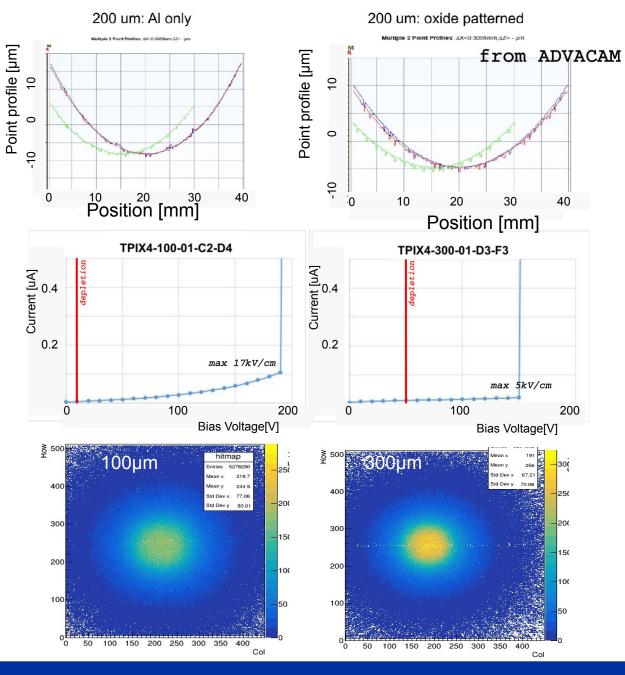




## **Planar Sensors production**

contact persons: V. Coco, P. Collins

- Large bow observed on the thin sensors
  - 40% better with oxide pattern on the backside
  - negligible after bonding (5μm for the 100μm sensor)
- Breakdown well above depletion
  - earlier to than hoped for. To be investigated
  - will limit unirradiated measurements as function of electric field
- 4 sensors bonded to TPX4 asics
  - tested with Sr90 source at Nikhef
  - used in TPX4 telescope prototype
- All test-structure **delivered and** pre-irradiation **characterisation started**

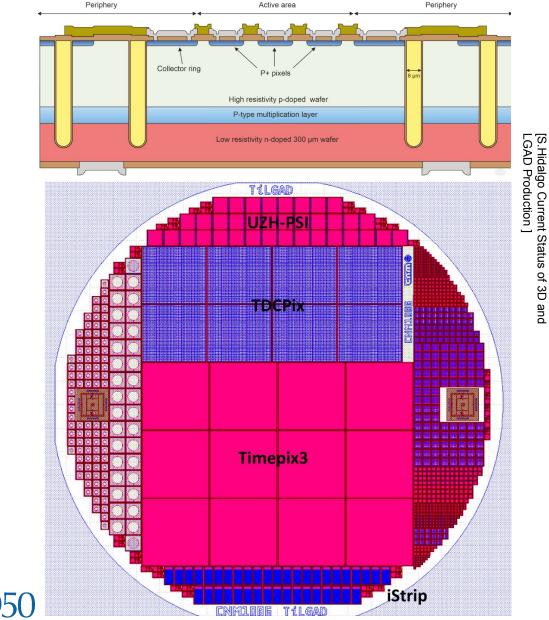




## **iLGAD production with CNM**

contact persons: M. Moll and E. Curras (SSD)

- LGAD technology provides **good time resolution** 
  - internal gain improves signal to noise and reduces jitter
- Segmentation is limited in conventional LGADs.
  - n-implant (gain layer) side segmentation limited by edge effects
  - low fill factor expected for small pitch geometries
- Backside (p-implant) readout allows for better segmentation without affecting the gain layer
- Third generation of iLGAD in production at CNM
  - Mask design ready since April 2021
  - Production to resume soon



(20um)

 $\cap$ 

(10um)

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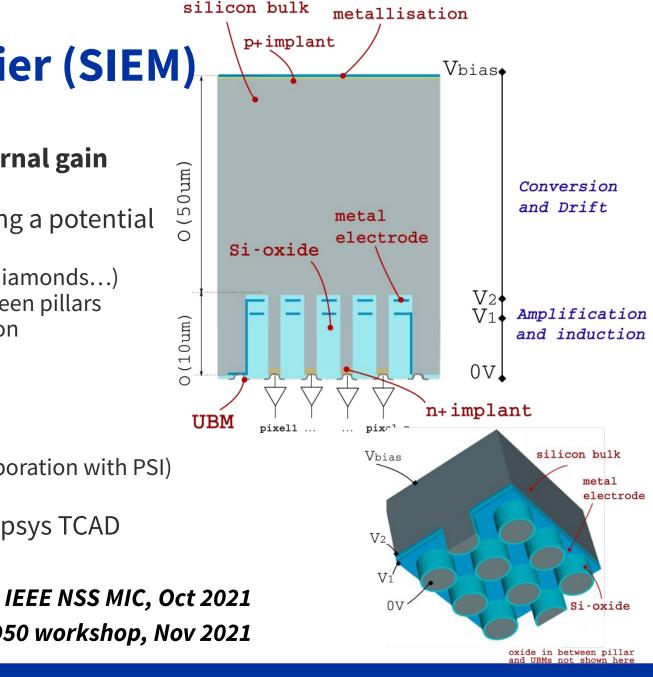
UBM

## **The Silicon Electron Multiplier (SIEM)**

contact persons: M. Halvorsen, V. Coco

- Goal: make a radiation hard sensor with internal gain
- Generate **high electric field** regions by applying a potential difference to a set of electrodes.
  - inversely etched or grown pillars of Silicon (SiC, diamonds...) Ο
  - single or multiple metallic electrode planes between pillars Ο
  - localised high fields promote charge multiplication Ο
- Production study will be started in 2022
  - DRIE based process (in discussion with CNM) Ο
  - Metal assisted chemical etching process (in collaboration with PSI) Ο
- Comprehensive simulation studies using Synopsys TCAD

11 November 2021

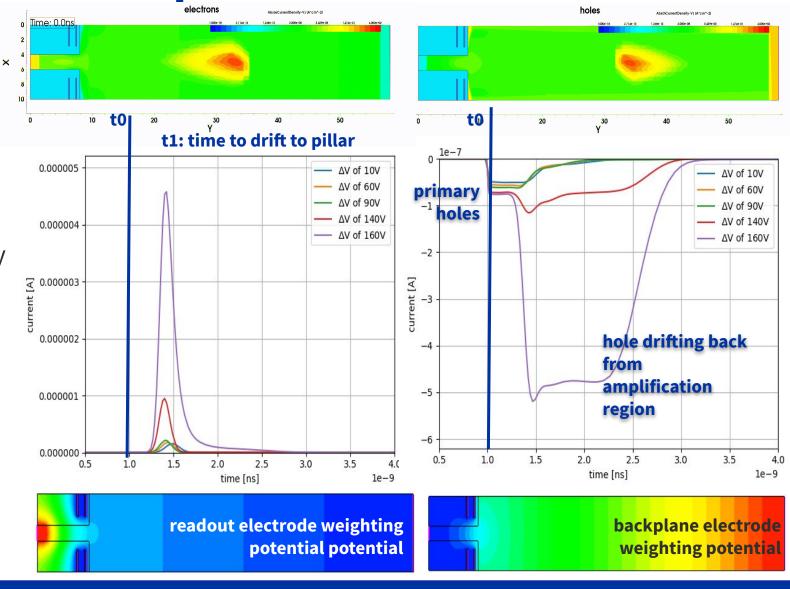


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## **SIEM - induced signal and amplification**

contact persons: M. Halvorsen, V. Coco

- Field above 20V/um in the pillar
- Gain up to 20-30 simulated
   Gain=Q<sub>readout</sub>/Q<sub>injected</sub>
- Signal at readout electrode is only induced by charges moving in the pillar
- Time resolution **expected to be similar to LGAD** 
  - Full MIP simulations ongoing





## Characterization

**Setups and results** 

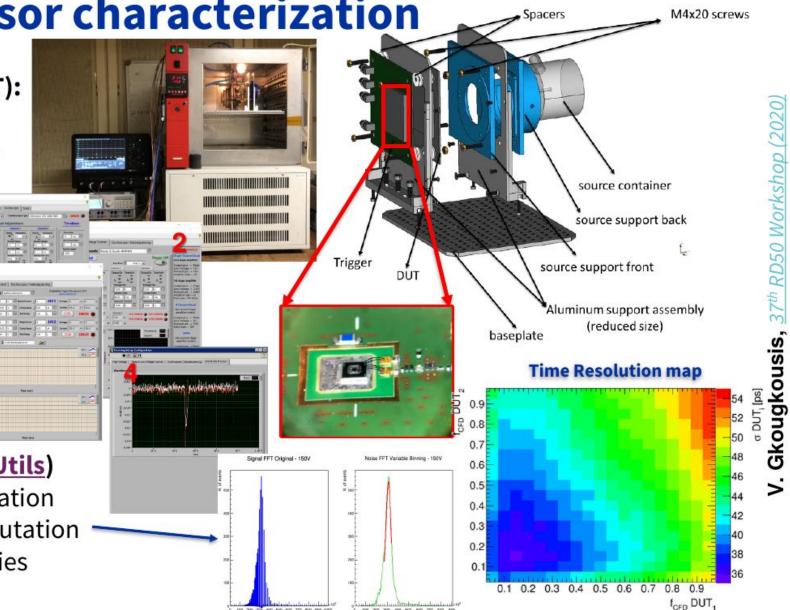


WP1.1 Hybrid Silicon Detectors

## Source setup for sensor characterization

contact persons: V. Gkougkousis

- <sup>90</sup>Sr Charge measurements (vs V<sub>bias</sub> & T):
  - ✓ Gain Charge
  - ✓ Time resolution, Jitter, Rise time
  - ✓ Relative Efficiency
  - Stability, Dark rate
- Fully automated DAQ (TiCAS)
  - ✓ Graphical interphase
  - Multi-instrument support
  - Remote management
  - Failsafe operation
- Waveform analysis framework (LGADUtils)
  - Iterative adaptive fitting optimization
  - Waveform shapes and FFT computation
  - **Bayesian asymmetric uncertainties**

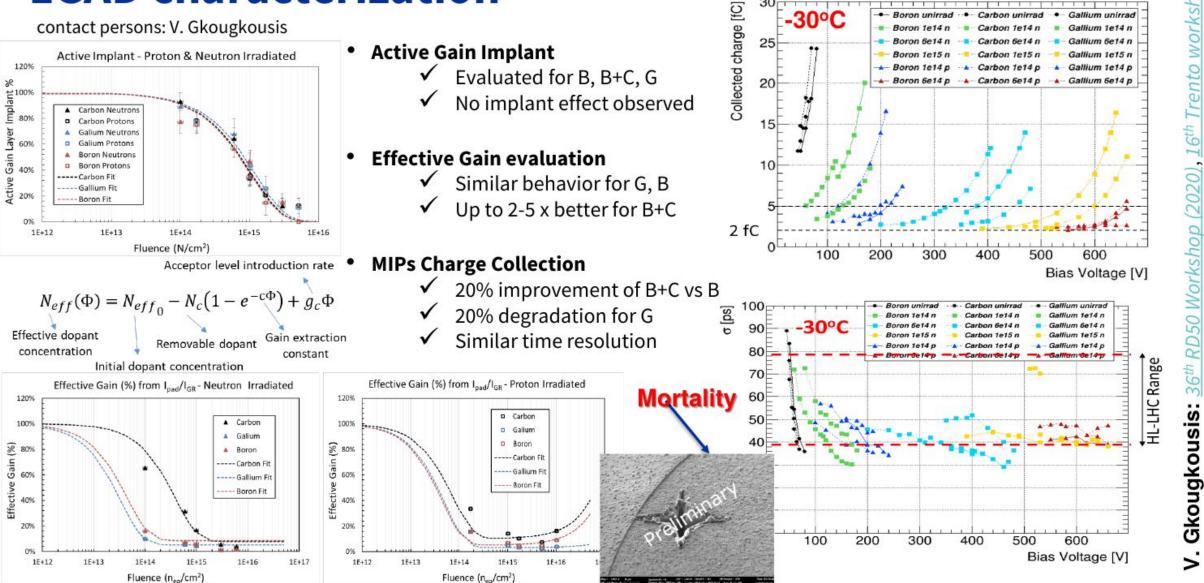




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## LGAD characterization





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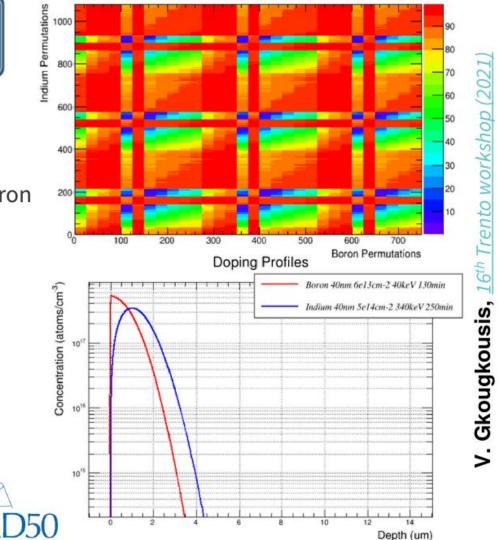
## **LGADs with Indium and Lithium Implantations**

contact persons: V. Gkougkousis

- LGADs susceptible to radiation damage
  - follow up on defect engineering studies (see slide 13)
- Indium implantation
  - Higher mass, less prone to lattice dislocations 0
- Lithium co-implant.
  - Boron with Lithium co-implantation demonstrates better neutron 0 radiation hardness
  - Lithium's light mass, and increased electronegativity favor formation of Li<sub>i</sub>O<sub>i</sub> rather then B<sub>i</sub>O<sub>i</sub>
- Implantation energy and doping profiles already optimized via TCAD simulations
  - Multiple wafer production variants and irradiation 0 campaigns are planned
- Common RD50 founded project with CERN, CNM & JSI (RD50-2021-03)



Boron - Indium Integral Variation (%)





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## **Test beam with 3D sensors**

contact persons: V. Gkougkousis

3D Sensors: Decoupling of charge generation and drift volume (Standard columns, TimeSpot, Hex geometries ect.)

### Pros

- High radiation tolerance up to several times 10<sup>16</sup> n<sub>ed</sub>/cm<sup>2</sup>
- Short drift distances with fast rise times
- Reduced Landau fluctuation, practically non-existent for perpendicular track

20 µm

8 um

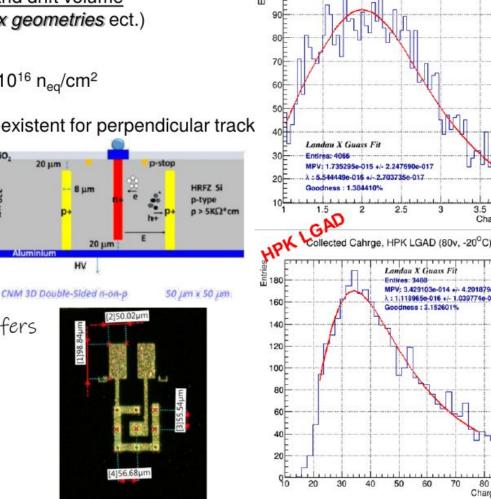
HV

#### Cons

- Non-uniform field geometry
- High cost
- Increased cell capacitance

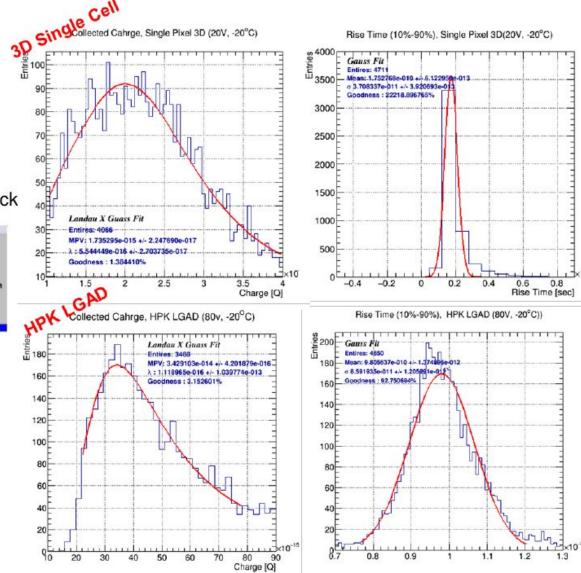
### **Tested Devices**

- Process: 2-sided
- Substrate: high Z, p-type FZ Silicon, 4" wafers
- Thickness: ~ 280 um
- Run: CNM 5936-11
- **Pixel Geometry:** 50 x 50  $\mu$ m, 1<sup>E</sup>, single cell ٠
- Capacitance: ~80 100 pF per cell



100

Cell





Workshop

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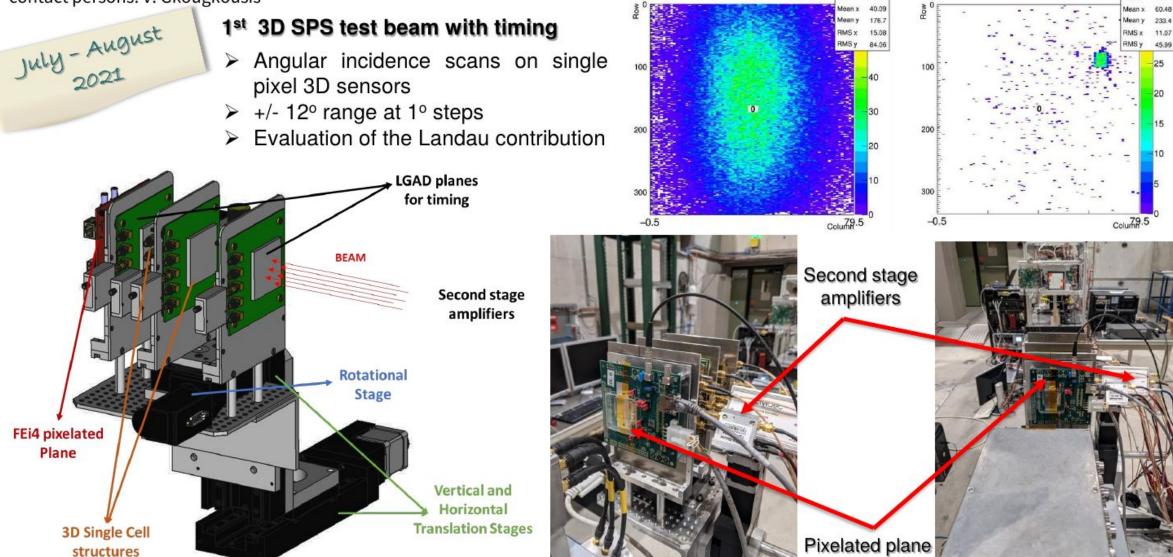
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Gkougkousis,

## Test beam with 3D sensors

contact persons: V. Gkougkousis



LGAD 1

Occupancy mod 0 bin 0

Occup\_0\_0\_MA

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BeamSpot

Occupancy mod 0 bin 0

Occup\_0\_0\_MA

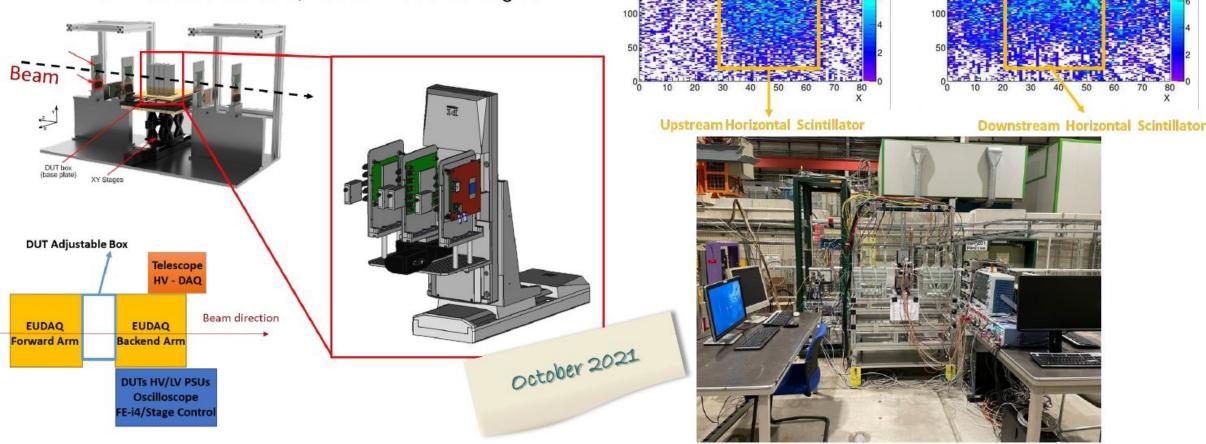
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## **Test beam with 3D sensors**

contact persons: V. Gkougkousis

### 2<sup>nd</sup> 3D SPS test beam with tracking and timing

- Tracking implemented thought the AIDA telescope
- Detailed timing and field maps
- Non-irradiated sensors, vertical incidence angles





USBPIX\_GEN2\_BOARD\_200 20 Raw Hitmap

48206 44.15 197.6

85.93

200

150

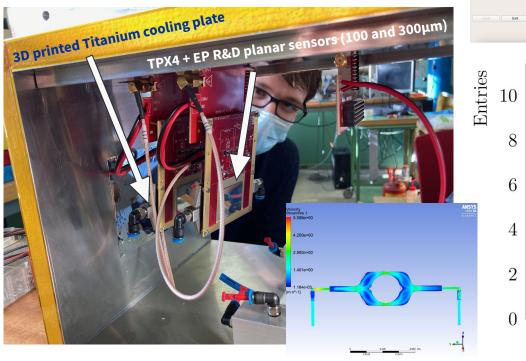
USBPIX GEN2 BOARD 200 20 Raw Hitmap

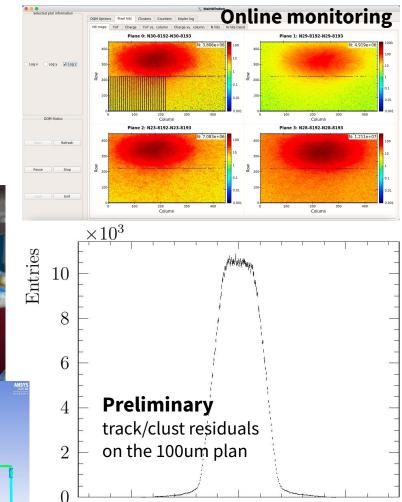
## **TPX4-based 4D telescope**

- Expect **30-40ps per track** in first phase (end 2022) and **25-30ps in second phase** (end 2024)
- Pointing resolution @DUT down to 2µm
- No rate limitation (TPX4 up to 358MHz/cm<sup>2</sup>, hit based)

### Single arm prototype in beam two weeks ago!







-0.1

 $y_{\text{clus.}} - y_{\text{track}} [\,\text{mm}]$ 

0.1

- **CERN contrib:** mechanics, cooling and DUT box design [R. Dumps, M. Gose], slow control (motion and env. monitoring) [M. Halvorsen, W. Byczynski], hybrid sensors (from thin planar prod + TPX4) [V. Coco], LGAD based timing plane [V. Gkougkousis], reconstruction software [H. Schindler, T. Evans]
- **Collaboration** with Nikhef, Uni. of Santiago, Uni. of Oxford, Uni. of Dortmund, Uni of Manchester
- AIDA-innova WP.3 task 3.3 will derive from it (1 to 2 TPX4 planes for EUDET telescope)



## Upgrade with the 16ch board

contact persons: V. Gkougkousis, E. Cid Lemos

### 16- Ch Base board

- ▶ 16-channel readout board with integrated I<sup>st</sup> and 2<sup>nd</sup> stage amplifier
- ➢ SiGe Based with 12 GHz cut-off
- ➢ 10 mA per channel max current at ≤ 1.3 mV noise
- 15 mm x 15 mm central opening with 140 mm x 140 mm outer dimensions
- Vertical miniaturized coaxial plug connectors for sensor board (16 channels + HV/RTD)
  Total Gain:

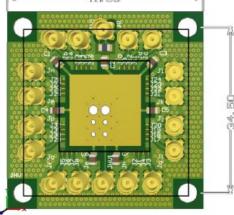


### Multi-Channel Fast Readout - SAMPIC

- Technology: AMS 0.18µm
- Sampling: between 3 and 8.4 GS/sec on 16 channels
- 16 channels per chip
- Signal Bandwidth of 12 GHz
- Discrimination noise 2 mV, chip noise < 1.3 mV RMS
- Max input Signal: IV unipolar (0.IV to I.IV)

### Single sensor carrier board

- Quick sensor test turnaround
- Simplify probing, reduce damage
- Batch testing with better control







## Simulation

### Making sense of the measurement and helping design



## **Motivation**

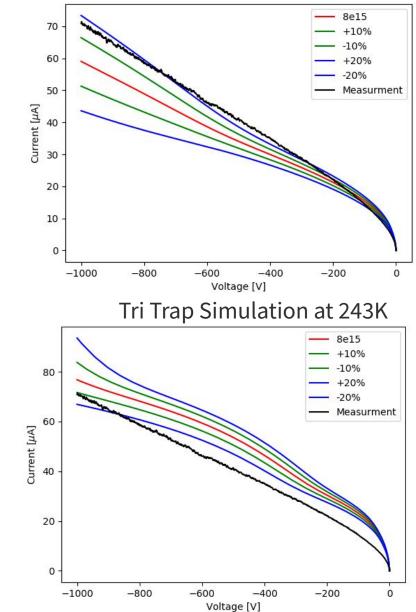
- MIP induced signal simulations is key component of sensor and IC design
- Need two components:
  - device simulation (Synopsis TCAD): E field, current, rad. damage models etc...
  - MIP simulation (Garfield++)

### • Focus on time resolution estimation

- as function of fluence
- for various geometries

### • Uses the test structures from planar sensor

- build some statistics
- have a controlled design: NDA (ref. KC5204) signed to perform full process simulation, and **publish doping profile**
- Provide simulation inputs and measurement to the community



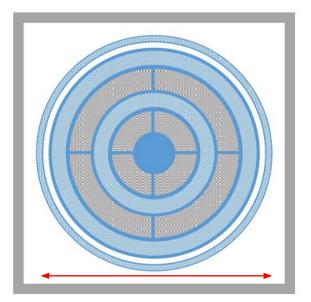
Penta Trap Simulation at 243K



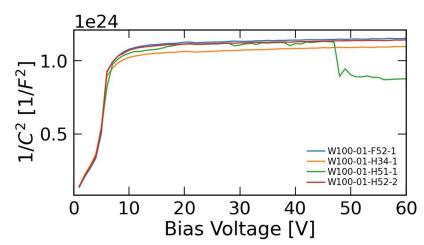
## **Measurement Campaign**

contact persons: J. Haimberger

- **Diode test structures** for development of model
- 50, 100, 200, 300 µm thickness
  - impact of weighting field on timing
  - $\circ$   $\,$  comparison with other types of detectors
- IV, CV and Charge collection measurements
  - Extract induced **signal shape**, **total charge** and **time resolution**
  - 60% of unirradiated IV/CV measured
- Neutron and proton irradiation for fluences from 10<sup>13</sup> to 10<sup>17</sup> n<sub>eq</sub>/cm<sup>2</sup>
  - irradiation campaign to start begin of next year
- 4 Diodes per point





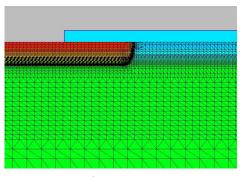


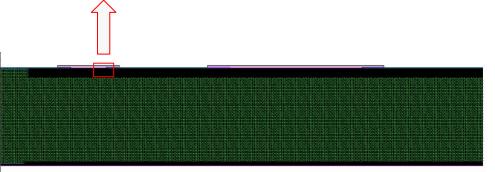


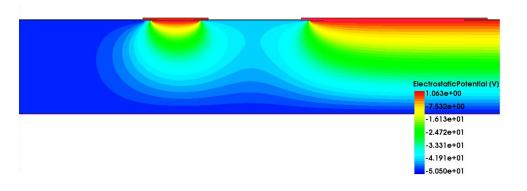
### **Device simulation** TCAD

contact persons: J. Haimberger

- Diode simulation
  - Large volume to simulate -> compromises have to be made
  - Currently optimizing mesh size for different thicknesses to reduce simulation time and still converge with the simulation
- Radiation damage
  - Modeled through **traps** which increase concentration with fluence
  - Exporting **trapping probability field** to Garfield++ by calculation it for each mesh point out of trap cross section and concentration









#### WP1.1 Hybrid Silicon Detectors

### MIP induced signal simulation Garfield++

contact persons: M. Halvorsen, H. Schindler

- Time resolution principles see "<u>W. Riegler et. al, Time resolution</u> of silicon pixel sensors"
- Time resolution from MIP with Garfield++ simulation:
  - Different thickness and sensor geometries
  - Different front-end electronics
  - See "<u>Time Resolution Study usingGarfield++</u>'

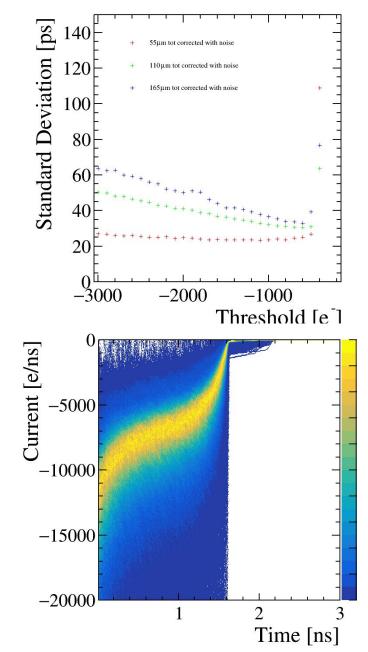


- Under development:
  - Timing after irradiation (introduction of traps)

### Input for IC design:

- capacitance and total charge for various sensors
- induced current time distribution

to be used by analog FE designer (R. Ballabriga Sune, V. Sriskaran)





## Outlook

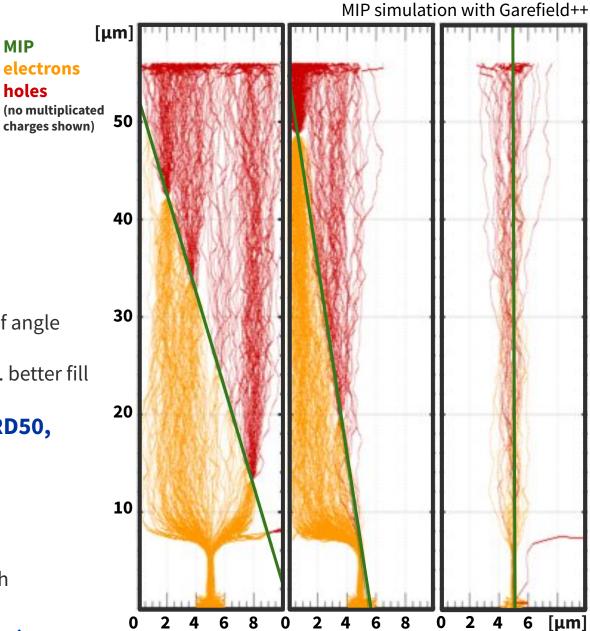
- **Planar sensor production completed** 
  - Characterisation and simulation on-going Ο

### Several studies of sensor with internal gain

- Defect engineering for LGAD radiation hardness Ο
- iLGAD production to reach small segmentation Ο
- New approach to internal gain with SiEM investigated Ο

### **3D technology investigation started**

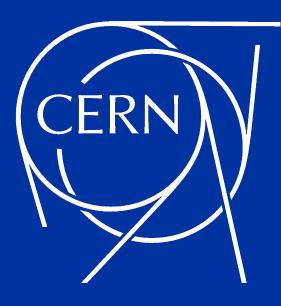
- Test beam campaign to measure time resolution as function of angle Ο and intra-pixel position.
- Next year focus on 3D production, towards smaller column (ie. better fill Ο factor, smaller pitch)
  - Will investigate synergies with other projects (RD50, AIDA-innova, Timespot, ....)
- Next year we start the IC side of the hybrid sensor
  - 28nm Analog FE design targetting 30ps resolution and small Ο power/footprint
  - Toward a small scale R&D ASIC for fast sensors with small pitch Ο
    - Will investigate synergies with other projects (AIDA-innova, EP-ESE, LHCb U2, NA62 upgrade,...)





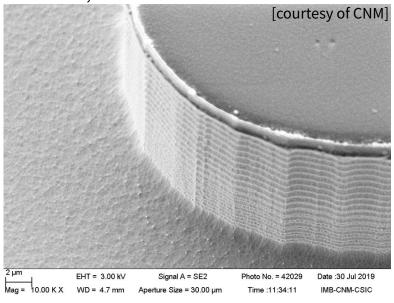
MIP

holes



## SiEM: Possible production process

contact persons: M. Halvorsen, V. Coco



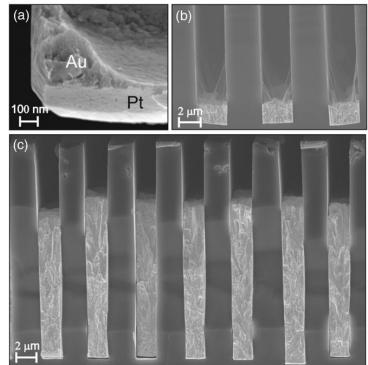
### DRIE based

- what topology can really be achieved?
  - electrode/wall guard, thickness of oxide, corner shapes...
- electrical properties?
  - SiO<sub>2</sub>/Si interface, scalloping, ...
- homogeneity of the production?
- performances

### **Production study to start in 2022**

### [submitted proposal to AIDA-innova blue-sky R&D, may need to find extra partners / funding otherwise]

### [L. Romano *et al*; AdEM 22 (2020) 2000258]



### Metal assisted etching

- less "production ready"
- more appropriate for single electrode structure
- no more constraints on the guard
- could be simpler

### To be investigated further PAUL SCHERRER INSTITUT

### [preparing collaboration agreement with PSI]



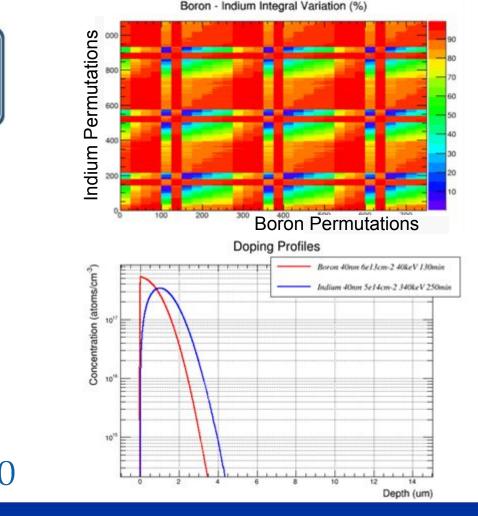
### Victor Coco | The Silicon Electron Multiplier

## LGADs with Indium and Lithium Implantations

contact persons: V. Gkougkousis

- LGADs susceptible to radiation damage
  - follow up on B, B+C and B+Ga studies (see slide xxx)
- Indium implantation
  - Higher mass, less prone to lattice dislocations
- Lithium co-implant.
  - Boron with Lithium co-implantation demonstrates better neutron radiation hardness
  - Lithium's light mass, and increased electronegativity favour formation of Li<sub>i</sub>O<sub>i</sub> rather then B<sub>i</sub>O<sub>i</sub>
- Implantation energy and doping profiles already optimized via TCAD simulations
  - **Multiple wafer production variants** and **irradiation** campaigns are planned
- Common RD50 founded project with CERN, CNM & JSI (RD50-2021-03)

### Gkougkousis V., 16th Trento Workshop (2021): link





114.818

Indium (Krl 4d<sup>10</sup>5s<sup>2</sup>5n

t-Transition Me