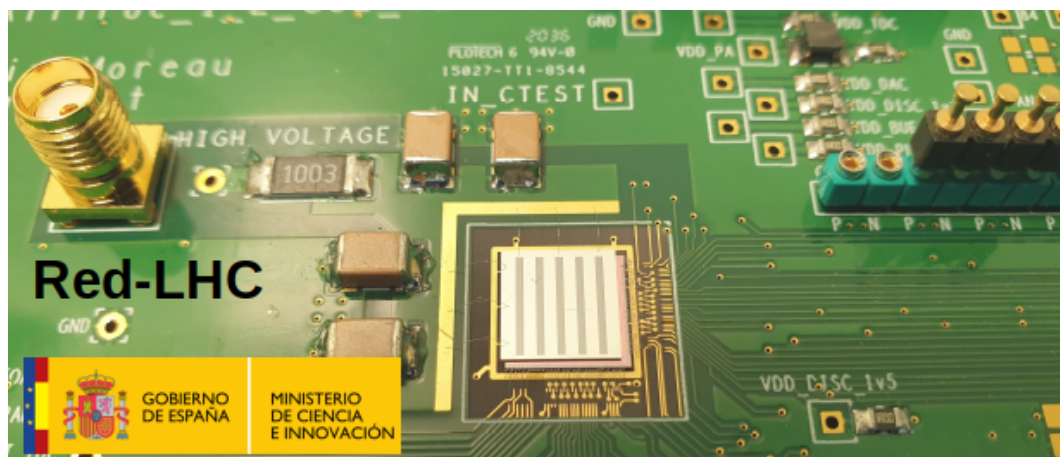


Session Program

27-30 Sept 2021



Workshop on LHC Technologies (Red-LHC)

Day 3: Electronics (Part 2)

Wednesday 29 September

09:00

Day 3: Electronics (Part 2)

Session | **Convener:** Ignacio Redondo Fernandez**09:00–09:25 The ALTIROC2 chip for the ATLAS HGTD Upgrade (IFAE)****Speakers**

Raimon Casanova Mohr, Sebastian Grinstein

09:25–09:50 CMS Pixel Electronics system design (ITANNOVA)**Speaker**

Fernando Jose Arteché Gonzalez

09:50–10:15 FastIC developments for LHCb and Belle RICH upgrades (UB)**Speakers**

Sergio Gomez Fernandez, Sergio Gomez Fernandez

10:15–10:40 Electronics for the CMS Muon Drift Tubes (CIEMAT)**Speaker**

Javier Sastre Alvaro

10:40–11:05 LHCb VeloPix front-end electronics (IGFAE)**Speaker**

Edgar Lemos Cid

11:05