TWEPP 2021 User Group Meetings

Report of Contributions

Contribution ID: **1**

Type: **not specified**

Digest of ATLAS(30') and CMS (30') applications and questions

Monday 27 September 2021 14:00 (1 hour)

Presenters: CHEN, Hucheng (Brookhaven National Laboratory (US)); FRANCONI, Laura (Universitaet Bern (CH)); HANSEN, Magnus (CERN)

Session Classification: Opto-electronics working group

Contribution ID: **2**   Type: **not specified**

LPGBT status, plans, qualification and Quality Assurance, followed by Q&A session. Audio Recording available from main opto-WG agenda page

Monday 27 September 2021 15:00 (45 minutes)

Presenter: KULIS, Szymon (CERN)

Session Classification: Opto-electronics working group

Contribution ID: **3**　　　　　　　　　　　　　　　　　Type: **not specified**

VTRx+ status, plans, qualification and Quality Assurance, followed by Q&A session. Audio Recording available from main opto-WG agenda page

Monday 27 September 2021 15:45 (45 minutes)

Presenters:　SOOS, Csaba (CERN);　TROSKA, Jan (CERN);　OLANTERA, Lauri (CERN)

Session Classification:　Opto-electronics working group

Contribution ID: 4   Type: **not specified**

VL+ cabling plant status, plans, followed by Q&A session. Audio Recording available from main opto-WG agenda page

Monday 27 September 2021 16:30 (45 minutes)

Presenters: VASEY, Francois (CERN); TOCCAFONDO, Iacopo (CERN)

Session Classification: Opto-electronics working group

Contribution ID: **5**   Type: **not specified**

Welcome

Tuesday 5 October 2021 14:00 (15 minutes)

Presenter: KLOUKINAS, Kostas (CERN)

Session Classification: Micro-electronics user group

Contribution ID: **6**   Type: **not specified**

CERN ASIC Support News - EP R&D WP5 developments

Tuesday 5 October 2021 14:45 (30 minutes)

Presenter: CARATELLI, Alessandro (CERN, EPFL)

Session Classification: Micro-electronics user group

Contribution ID: **7** Type: **not specified**

CERN EP R&D WP5: IC Technologies activities

Session Classification: Micro-electronics user group

Contribution ID: **8**   Type: **not specified**

EUROPRACTICE EDA Tools for the HEP Community

Tuesday 5 October 2021 16:00 (30 minutes)

Presenter: WILLOUGHBY, Mark (Manager Europractice Design Tools)

Session Classification: Micro-electronics user group

Contribution ID: **9**   Type: **not specified**

ChipFlow - Commercial Open Source EDA

Tuesday 5 October 2021 16:30 (30 minutes)

Presenter: TAYLOR, Robert (CEO at Chipflow; Director, LSD, Open Invention Networr)

Session Classification: Micro-electronics user group

Contribution ID: **10**   Type: **not specified**

Open Discussion

Tuesday 5 October 2021 17:00 (30 minutes)

Session Classification: Micro-electronics user group

Contribution ID: **11**   Type: **not specified**

EUROPRACTICE IC service offerings and foundry news

Tuesday 5 October 2021 14:15 (30 minutes)

Presenter: MALISSE, Paul (Senior Business Development Manager at IMEC)

Session Classification: Micro-electronics user group

Contribution ID: **12**   Type: **not specified**

Common SOC design platform and enablers

Tuesday 5 October 2021 15:15 (30 minutes)

Presenter: PEJASINOVIC, Risto (CERN)

Session Classification: Micro-electronics user group

Contribution ID: **13**　　　　　　　　　　　　　　　　　Type: **not specified**

CHIPS: CERN-HEP IC design Platform and Services

Tuesday 5 October 2021 15:45 (15 minutes)

Presenter:　FACCIO, Federico (CERN)

Session Classification:　Micro-electronics user group