## The 39th RD50 Workshop (Valencia)



Contribution ID: 1

Type: Talk

## **Topics in LGAD designs**

Thursday 18 November 2021 09:10 (20 minutes)

In this contribution, I will discuss two possible evolutions of the LGAD design.

- DC-RSD: up to now, resistive read-out in silicon detectors has been used only in AC-coupled detectors, the so-called AC-RSD or AC-LGAD. I will present here the first attempt to apply resistive read-out to a DC-coupled sensor.
- LGAD with bias ring: recent beam tests results showed that LGADs suffer destructive break-down if the field in the bulk is above 11V/um. SiPMs manage to survive similar conditions given the presence of quenching resistors. I will present a possible LGAD design that introduces AC-coupled read-out and quenching resistors in each pad. This design should also provide increased stability with respect to floating pads or pads with high currents.

**Authors:** SIVIERO, Federico (INFN - National Institute for Nuclear Physics); CARTIGLIA, Nicolo (INFN Torino (IT)); BORGHI, Giacomo (Fondazione Bruno Kessler); DALLA BETTA, Gian-Franco (INFN and University of Trento); PATERNOSTER, Giovanni (Fondazione Bruno Kessler); MENZIO, Luca (Universita e INFN Torino (IT)); PANCHERI, Lucio (University of Trento and TIFPA-INFN); FERRERO, Marco (Universita e INFN Torino (IT)); MANDURRINO, Marco (INFN); TORNAGO, Marta (Universita e INFN Torino (IT)); CENTIS VIGNALI, Matteo (FBK); BOSCARDIN, Maurizio (FBK Trento); ARCIDIACONO, Roberta (Universita e INFN Torino (IT)); SOLA, Valentina (Universita e INFN Torino (IT))

Presenter: CARTIGLIA, Nicolo (INFN Torino (IT))

Session Classification: LGAD - Low Gain Avalanche Diodes