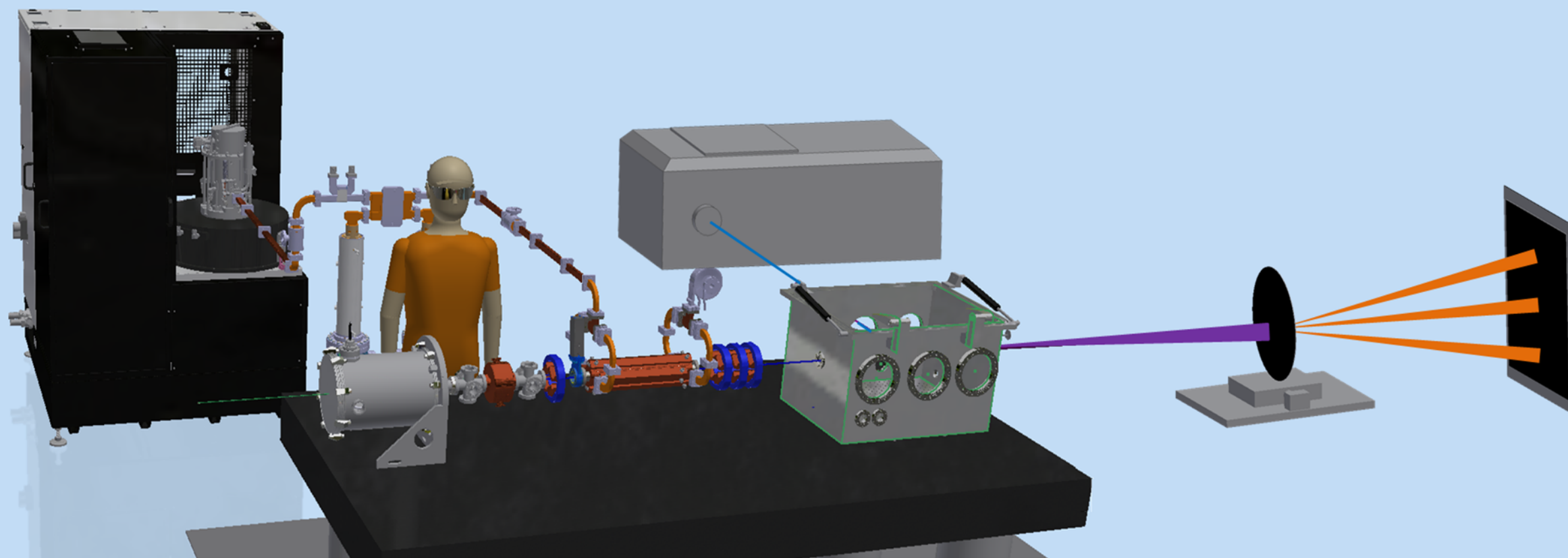


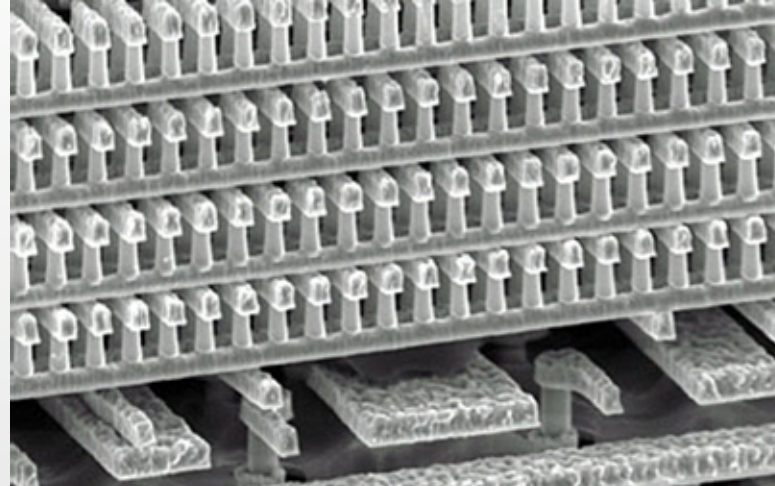
# ICS bright X-ray system



# The challenge

Measuring is knowing

How to measure and qualify the dimensions of these semiconductor devices?



The finest details will have dimensions of 1 to 2 nm in the future.

To optimize semicon equipment + material process parameters, sub-nm resolution metrology is required.

# Metrology solutions

- CD-SEM      Critical Dimension Scanning Electron Beam Microscope
- OCD      Optical Critical Dimension scatterometry
- CD-AFM      Critical Dimension Atomic Force Microscopy
- CD-SAXS      Critical Dimension Small Angle X-ray Scatterometry
- TEM      Transmission Electron Microscopy

Metrology method	Pros	Cons
CD-SEM (image-based) ★ (In-line use)	<ul style="list-style-type: none"> <li>➤ Measure <b>any complex arbitrary feature</b></li> <li>➤ <b>Direct</b> measurement from image (no modeling)</li> <li>➤ <b>Automated, stable, precise</b></li> </ul>	<ul style="list-style-type: none"> <li>➤ Mid throughput for large area coverage</li> <li>➤ Difficult to measure pattern height</li> </ul>
Optical Scatterometry (OCD) (model-based) (in-line use)	<ul style="list-style-type: none"> <li>➤ High <b>throughput</b> (for global monitoring)</li> <li>➤ High <b>sensitivity</b>, CD/ <b>3D profile</b> measurement</li> <li>➤ <b>Automated, stable, precise</b></li> </ul>	<ul style="list-style-type: none"> <li>➤ Average measurement only (unavailable for complex pattern)</li> <li>➤ Long time for modeling (recipe setup) (reference needs)</li> </ul>
CD-AFM (image-based)	<ul style="list-style-type: none"> <li>➤ Measure <b>3D profile</b> of arbitrary feature</li> </ul>	<ul style="list-style-type: none"> <li>➤ Measurable pattern is limited</li> <li>➤ Low throughput</li> </ul>
X-ray Scatterometry (CD-SAXS) (model-based) (off-line)	<ul style="list-style-type: none"> <li>➤ CD/ <b>2D X-section profile</b> measurement</li> </ul>	<ul style="list-style-type: none"> <li>➤ Need large test pad</li> <li>➤ Average measurement only (unavailable for complex pattern)</li> <li>➤ Low throughput</li> </ul>
Cross section TEM/ STEM (image-based) (off-line)	<ul style="list-style-type: none"> <li>➤ Atomic <b>resolution</b>, CD/ <b>3D profile</b> measurement</li> </ul>	<ul style="list-style-type: none"> <li>➤ <b>Destructive</b></li> <li>➤ Low throughput</li> </ul>

CDSAXS is the only non-destructive metrology for deep 2D x-section profile measurements

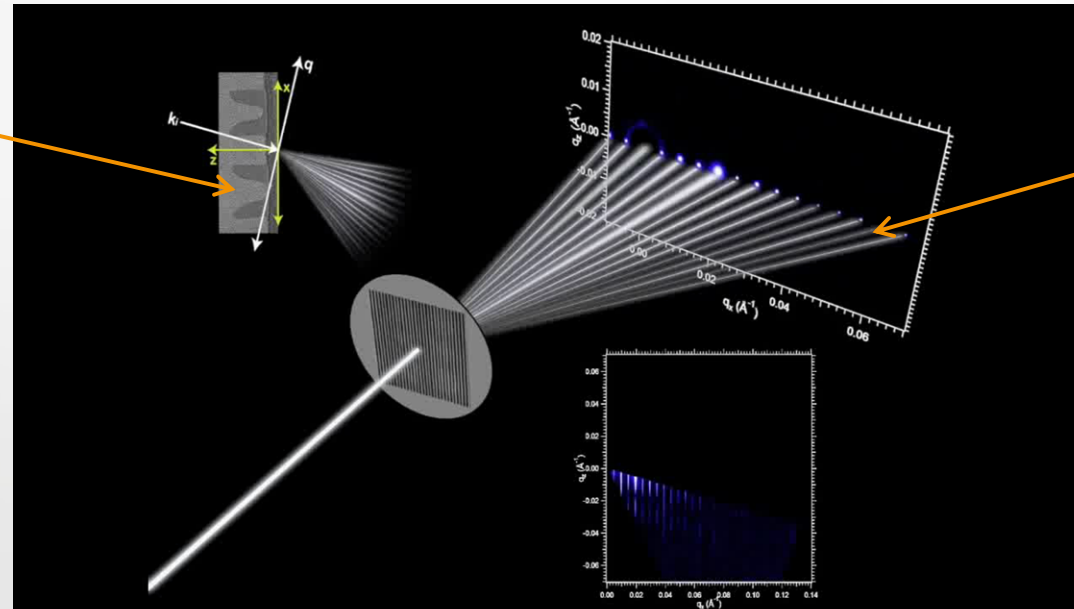
Unavoidable property of CDSAXS metrology

If throughput can be sufficiently increased, CDSAXS is a valuable metrology solution

# Metrology solution: CDSAXS

The measured intensities of the diffracted orders are used to reconstruct an average 2D profile

Reconstructed 2D profile

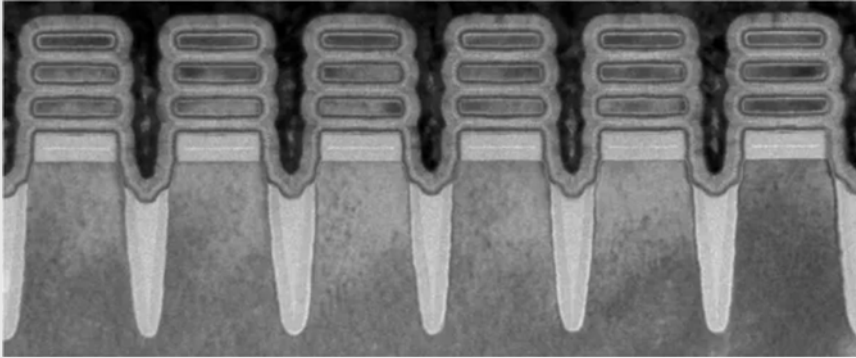


Diffracted orders

Ref.: X-ray scattering critical dimension metrology using a compact x-ray source for next generation semiconductor devices, Joseph Kline, J. Micro/Nanolith. MEMS MOEMS 16(1), 2017

# Destructive TEM versus non-destructive CDSAXS measurements

TEM picture of nanosheet transistors



Two-nanometer technology as seen using transmission electron microscopy. Two nanometers is smaller than the width of a single strand of human DNA. [Image: courtesy of IBM]

Destructive measurement (wafer can not be used anymore)

Reconstructed 2D profile using CDSAXS ??



Extracted information (?):

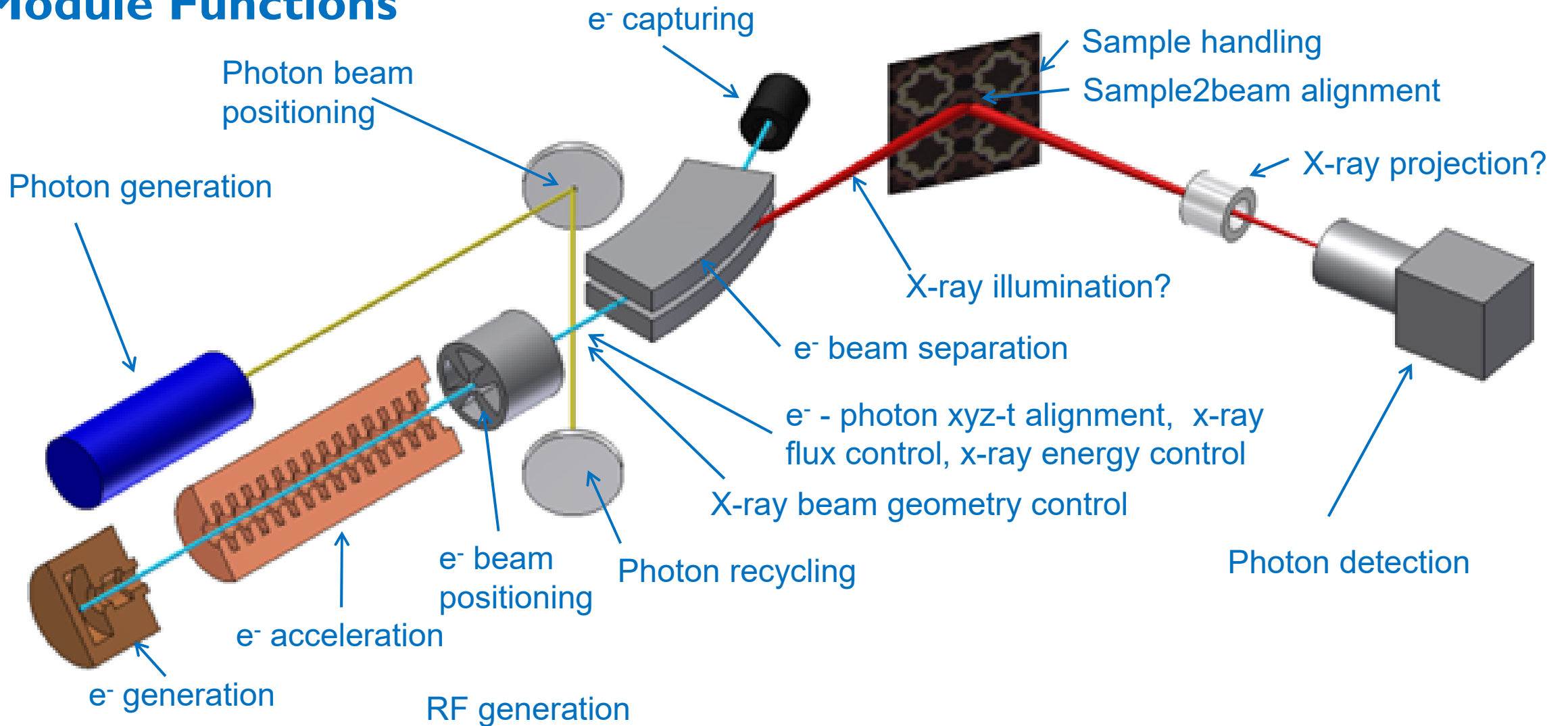
- Height
- Distances
- Slopes
- ?

It is not clear yet what useful information can be extracted with the CDSAXS measurements

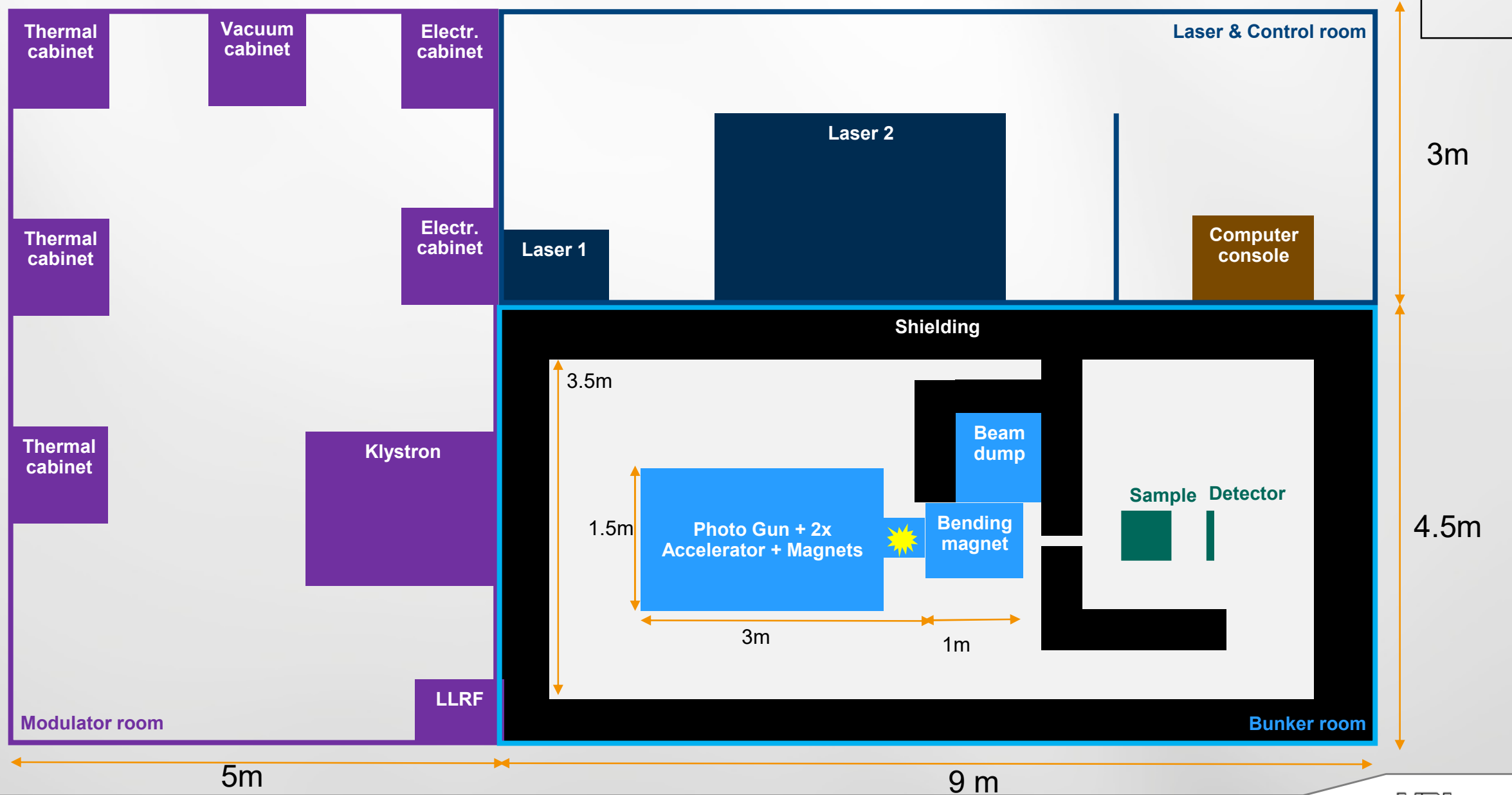
If sufficiently fast, measurements could also be done after successive process steps instead of measuring the complete profile at once



# Module Functions



## Possible layout

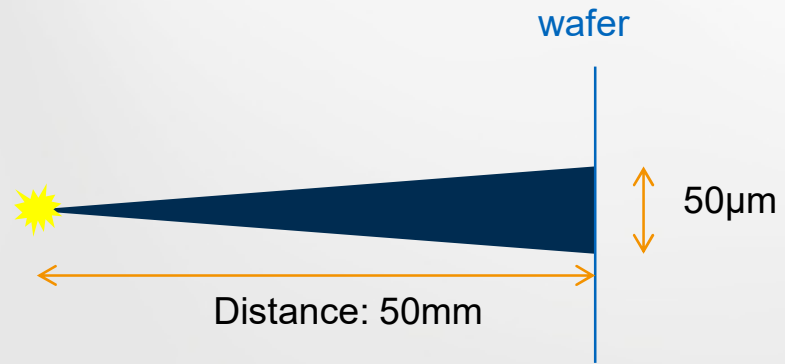




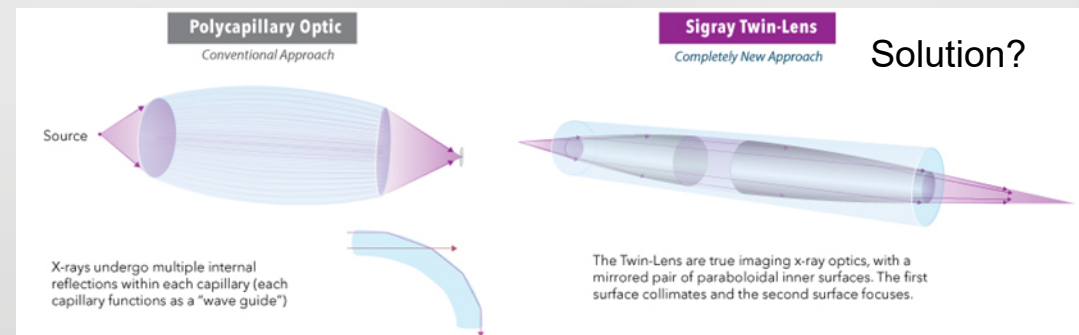
# X-ray optics

## Distances

With 1 mrad beam divergence and 50 $\mu$ m spotsize, source-wafer distance needs to be 50 mm, this is too small



Distance needs to be >100mm. What mirrors are needed? How much light will be lost?



# Performance

	Unit	Value	Remark
X-ray energy	keV	20	
X-ray energy spread, FWHM	%	1	
Beam divergence	mrاد	1	
Spot size at wafer	μm	100	50 μm may a better target
Photon flux	#/s	10 <sup>8</sup>	Request is 10 <sup>9-10</sup> , but 10 <sup>8</sup> seems feasible

It seems unlikely that changing the energy to probe the wafer at different depths avoids wafer rotation.

# Measurement time versus flux

- From the article of Joe Kline, NIST, relation flux – measurement time:

Exposure time (s)	High- <i>k</i> -coated fin (ph/s)	Si fin (ph/s)	Resist (ph/s)
1	$10^9$	$10^{10}$	$10^{12}$
10	$10^8$	$10^9$	$10^{11}$
100	$10^7$	$10^8$	$10^{10}$

# Concluding remarks

- Flux might be the bottleneck
  - If the flux can be made high enough, the ICS system is a feasible solution for wafer inspection
- IMEC, Leuven, is a candidate to be the first 'customer'
  - IMEC is a leading semicon research facility that serves many semicon industries
  - First impression: with the expected price between 6 and 10 M€, business case looks good