# Development of a hybrid single-photon imaging detector with encapsulated CMOS pixelated anode

Massimiliano Fiorini<sup>b,c</sup>, Jerome Alozy<sup>a</sup>, Nicolò Biesuz<sup>b</sup>, Riccardo Bolzonella<sup>b,c</sup>, Michael Campbell<sup>a</sup>, Viola Cavallini<sup>b,c</sup>, Angelo Cotta Ramusino<sup>b</sup>, Marco Guarise<sup>b,c</sup>, Xavier Llopart<sup>a</sup>

<sup>b</sup> INFN Ferrara <sup>c</sup> University of Ferrara <sup>a</sup> CERN







University of Ferrara



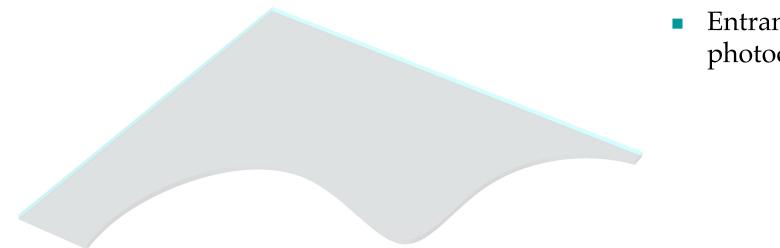
European Research Council

11<sup>th</sup> International Conference on Ring Imaging Cherenkov Detectors Edinburgh, September 12-16 2022

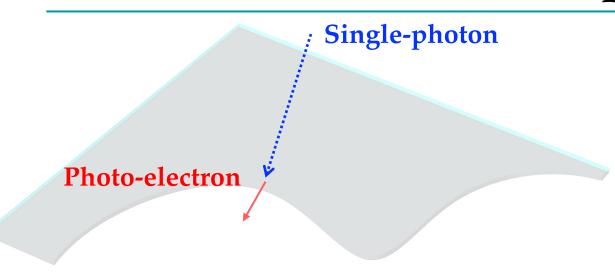
## Project goal and detector concept

- Development of a new photodetector with large active area able to measure single photons with simultaneous excellent timing and spatial resolution, with a low noise level at room temperature
- Detector based on a "hybrid" concept:
  - Vacuum detector; photocathode with high QE in the region of interest
  - Proximity-focusing geometry
  - Micro-channel plate (MCP) amplification
  - Silicon ASIC embedded inside vacuum tube
  - Reference: <u>JINST 13 C12005 2018</u>

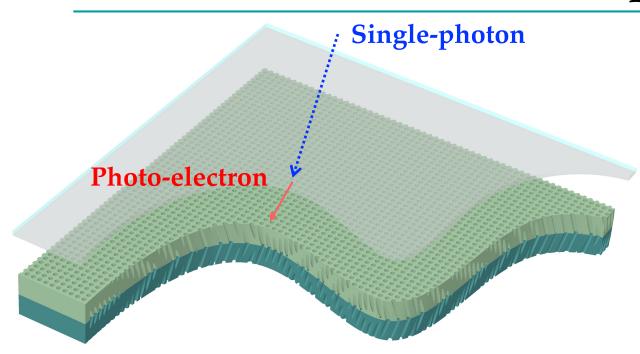
Target time resolution	<100 ps r.m.s.		
Position resolution	5-10 μm		
High-rate capability	10 <sup>9</sup> hits/s		
Low dark count rate at room T	$\sim 10^2 - 10^3 \text{ counts/s}$		
Large active area	7 cm <sup>2</sup>		
High channel density	0.23 millions		



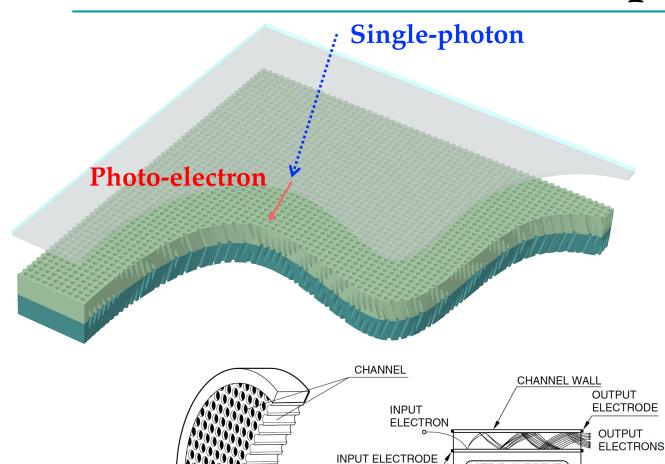
 Entrance window + photocathode



- Entrance window + photocathode
  - Photon conversion
  - High QE photocathode in the blue-green region
    - E.g. bialkali, multialkali
    - ~10<sup>2</sup> Hz/cm<sup>2</sup> dark count rate at room temperature
    - Best for timing
  - Flexible design allows to use different photocathodes



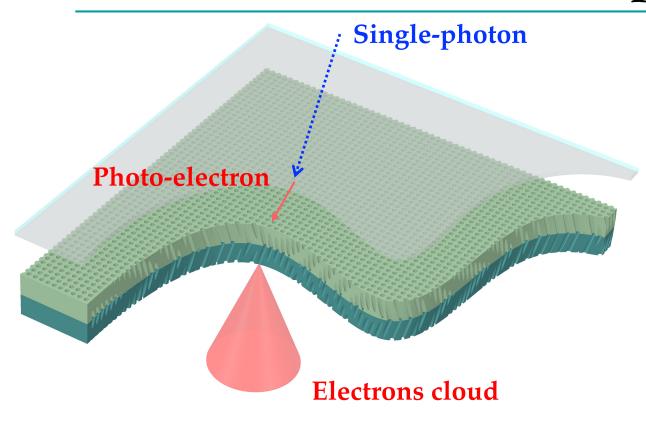
- Entrance window + photocathode
- Microchannel plate stack (chevron)
  - □ A few **10<sup>4</sup> gain**
  - **5-10 μm** pore size
  - Short distance from MCP to cathode and anode for best time and position resolution
  - Atomic layer deposition for increased lifetime >20 C/cm<sup>2</sup>



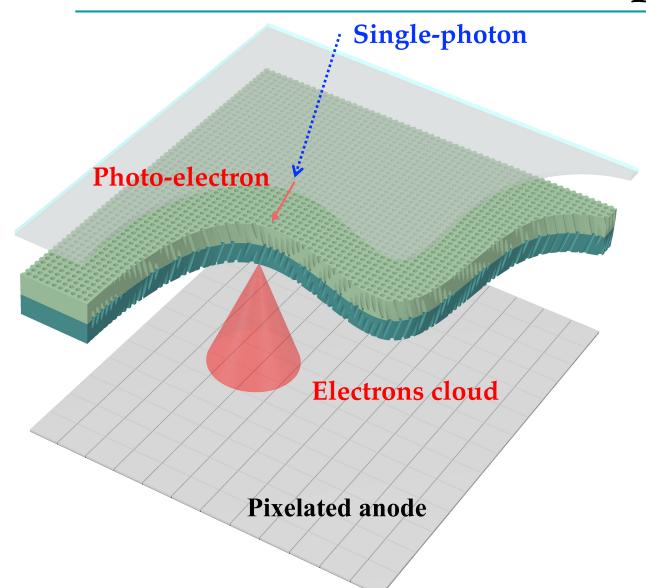
- Entrance window + photocathode
- Microchannel plate stack (chevron)
  - □ A few **10<sup>4</sup> gain**
  - **5-10 μm** pore size
  - Short distance from MCP to cathode and anode for best time and position resolution
  - Atomic layer deposition for increased lifetime >20 C/cm<sup>2</sup>

STRIP CURRENT

VD



- Entrance window + photocathode
- Microchannel plate stack (chevron)
  - □ A few **10<sup>4</sup> gain**
  - **5-10 μm** pore size
  - Short distance from MCP to cathode and anode for best time and position resolution
  - Atomic layer deposition for increased lifetime >20 C/cm<sup>2</sup>



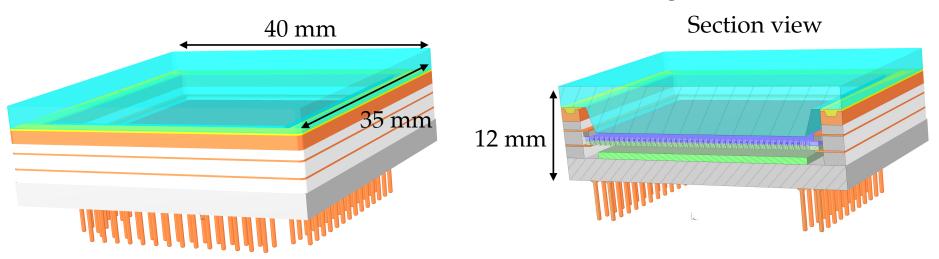
- Entrance window + photocathode
- Microchannel plate stack (chevron)

#### Pixelated anode

- Electron cloud spread over a number of pixels
- 55μm × 55μm
  pixel size
- 0.23 M pixels measuring arrival time and duration of input signals
- □ 7 cm<sup>2</sup> active area
- Up to 2.5 Ghits/s
- Local signal processing

#### Hybrid detector assembly

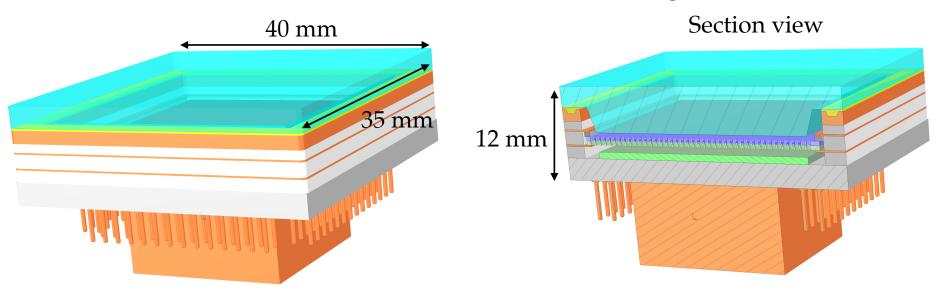
3D structure: detector rendering



- Vacuum-based detector
  - □ Assembly of many components under high vacuum (~10<sup>-10</sup> mbar)
  - High-speed connections through pins in ceramic carrier board

### Hybrid detector assembly

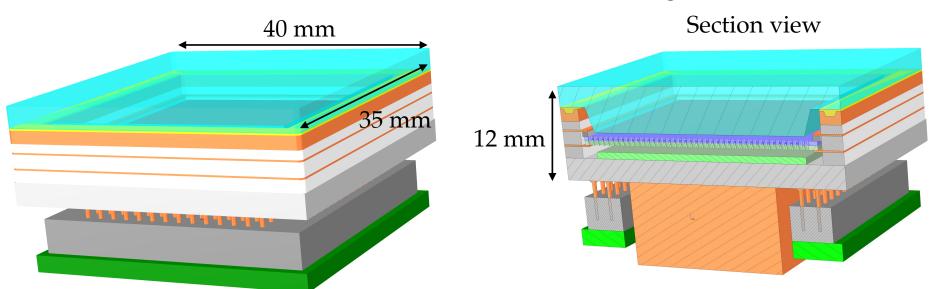
3D structure: detector rendering



- Vacuum-based detector
  - □ Assembly of many components under high vacuum (~10<sup>-10</sup> mbar)
  - High-speed connections through pins in ceramic carrier board
- Heat sink for stable detector operation (~5 W heat removal)

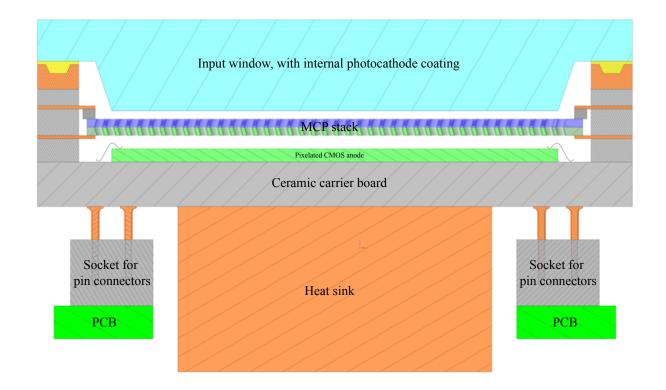
### Hybrid detector assembly

3D structure: detector rendering



- Vacuum-based detector
  - □ Assembly of many components under high vacuum (~10<sup>-10</sup> mbar)
  - High-speed connections through pins in ceramic carrier board
- Heat sink for stable detector operation (~5 W heat removal)
- Carrier printed circuit board (PCB)
  - Socket for detector pins, regulators and high voltage
  - Connected to FPGA-based read-out and DAQ via 16 × 10 Gbps links

## Detector geometry



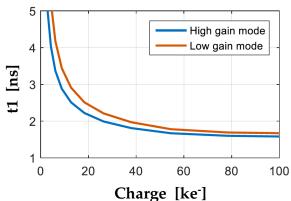
- Shortest photocathode-to-MCP distance preserves impact position information
- Optimized MCP-to-anode distance spreads the electron cloud over a number of pixels

#### **Pixelated** anode

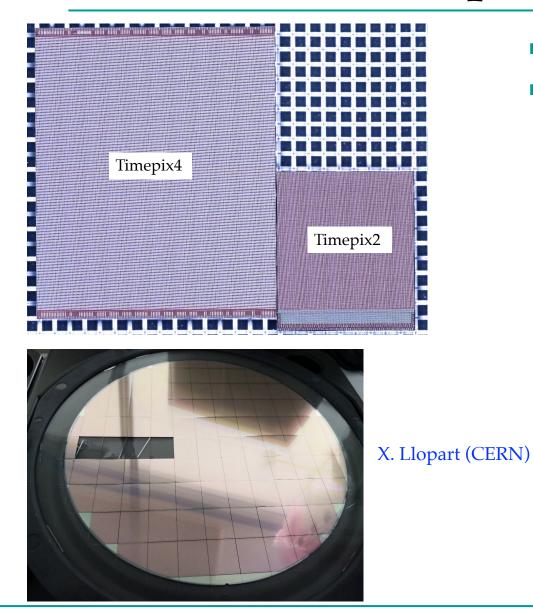
- Timepix4 ASIC in 65nm CMOS silicon pixel technology
  - Developed and produced by the Medipix4 Collaboration for hybrid pixel detectors
- Charge sensitive amplifier, single threshold discriminator and TDC based on Voltage Controlled Oscillator
  - 4-side buttable (TSV)
  - Data-driven and frame-based read-out



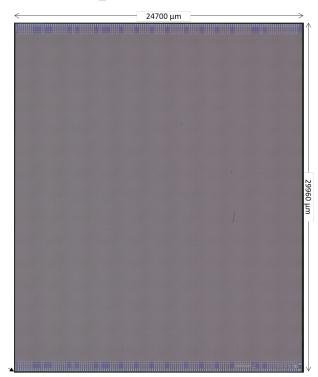
Technology			CMOS 65 nm		
Pixel Size			55 μm × 55 μm		
Pixel arrangement			4-side buttable 512×448 (0.23 Mpixels)	5	
Sensitive area			$6.94 \text{ cm}^2 (2.82 \text{ cm} \times 2.46 \text{ cm})$	4	
out es	Data driven	Mode	TOT and TOA	<b>[su</b> ] 3	
Read-out Modes		Event Packet	64-bit	<b>F</b> 2	
		Max rate	358 Mhits/cm <sup>2</sup> /s	1	
TDC bin size			195 ps	0	
Readout bandwidth		th	$\leq 163.84 \text{ Gbps} (16 \times @10.24 \text{ Gbps})$		
Equ	Equivalent noise charge		50-70 e⁻		
Target global minimum threshold		mum threshold	<500 e-		



#### The Timepix4 ASIC

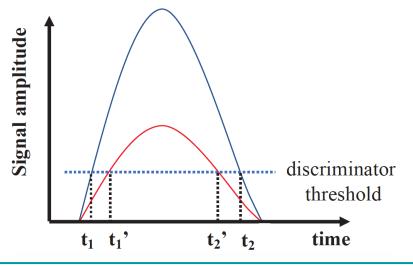


- 65 nm CMOS (TSMC)
- ASIC productions:
  - □ Timepix4\_v0 (Q1 2020)
  - □ Timepix4\_v1 (Q4 2020)
  - □ Timepix4\_v2 (Q4 2021)



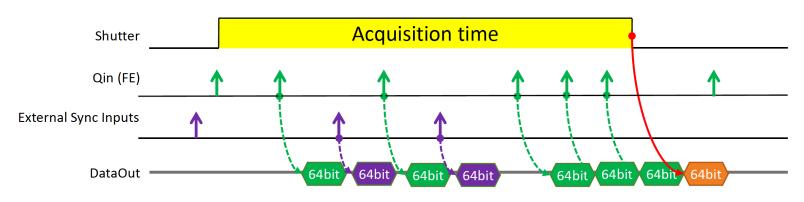
## Timepix4 hit data

- Measures arrival time (t<sub>1</sub>) and Time-over-Threshold (ToT=t<sub>2</sub>-t<sub>1</sub>)
  TDC bin size: 195 ps (56 ps r.m.s. resolution per pixel)
- Electron cloud spread over a number of pixels  $\rightarrow$  cluster
- Use ToT information (proportional to the charge in a pixel) to:
  - Correct for time-walk effect in every pixel
  - □ Improve **position resolution** by centroid algorithm
    - Go from  $55\mu$ m/ $\sqrt{12}$ ~16 $\mu$ m down to  $5\mu$ m
  - □ Improve **timing resolution** by multiple sampling
    - Many time measurements for the same photon → few 10s ps



## Timepix4 data-driven read-out

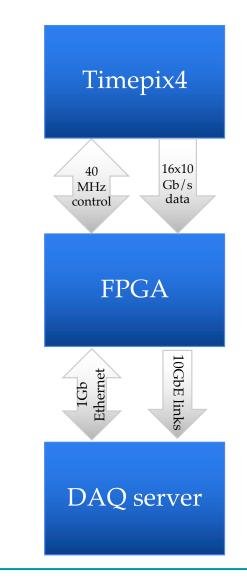
- Zero-suppressed continuous data-driven
  - Output bandwidth from 40 Mbps (2.6 Hz/pixel) to 160 Gbps (10.8KHz/pixel)
- 4 external inputs to synchronize/align external signals with data



		SPEC: Packet specifications ToA/ToT					
	Name	Width	MSB	LSB	Bits		
	Тор	1	63	63	[63:63]	$\left  \right\rangle$	
	EoC	8	62	55	[62:55]	ļ	Address: 18 bits
	SP	6	54	49	[54:49]		
	Pixel	3	48	46	[48:46]	2	
	ToA	16	45	30	[45:30]		
	ufToA_start	4	29	26	[29:26]	5	Time: 29 bits
	ufToA_stop	4	25	22	[25:22]		Time. 29 bits
04.1.1.	fToA_rise	5	21	17	[21:17]	)	
y: 21 bits	fToA_fall	5	16	12	[16:12]		
-	тот	11	11	1	[11:1]		
	Pileup	1	0	0	[0:0]		

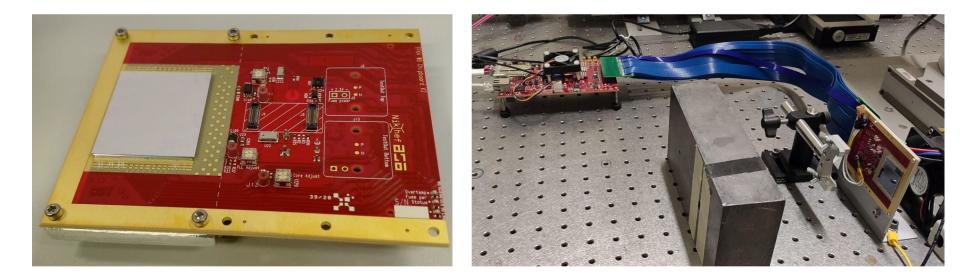
#### **Electronics and DAQ**

- On-detector electronics
  - Timepix4 ASIC
  - Electro-optical transceivers will link the ASIC to an FPGA-based board for the exchange of configuration (slow control) and the collection of event data
  - Regulators, etc.
- Off-detector electronics
  - □ FPGA far from detector
- The FPGA performs serial decoding and sends the data to a PC for data analysis and storage using fast serial data links



#### Current test system

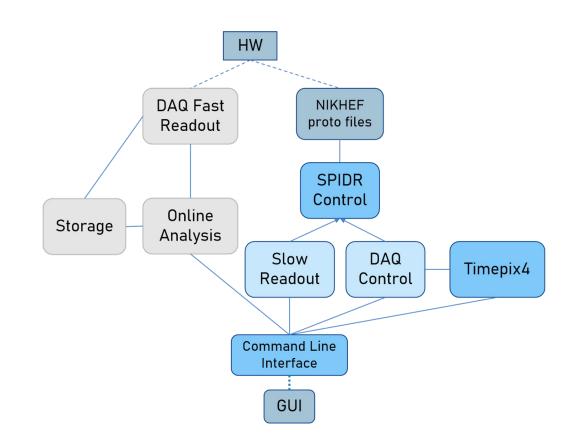
- Timepix4 bump-bonded to 300  $\mu$ m thick silicon sensor
- SPIDR4 FPGA read-out system and sensor carrier board
  - Developed by Nikhef Medipix4 group



Dedicated DAQ system under development

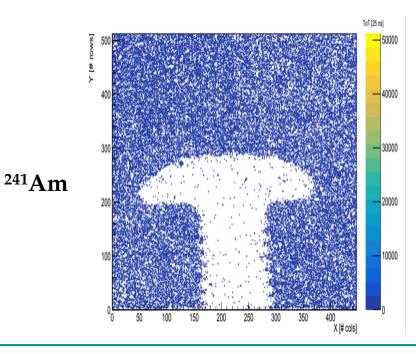
#### Software

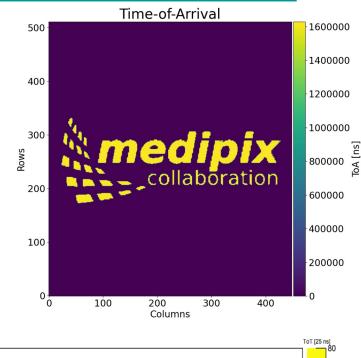
- Dedicated software under development
- C++ based
  - Low-level
  - Object-oriented
- Readout and Control in unique CLI
- Read and Write register functions
- Application Programming Interfaces for Timepix4
- Packets decoder

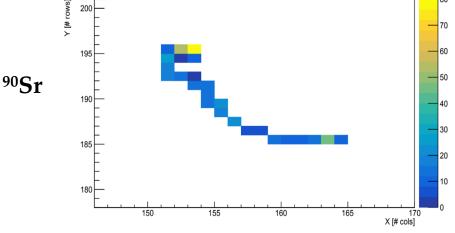


#### Measurements with Timepix4

- Digital test pulse:
  - Correct patterns, number of pulses and ToA-ToT
- Radioactive sources measurement:
  - Density based clustering (DBSCAN)
  - Preliminary ToT-charge calibration

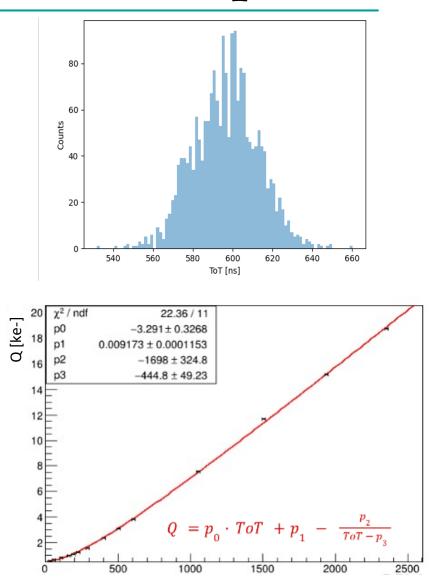






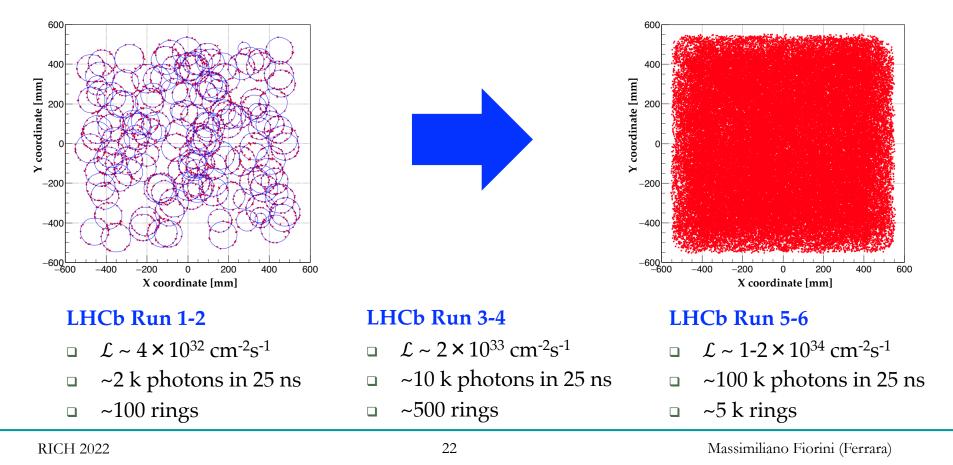
#### Measurements with Timepix4

- Analog test pulse:
  - Expected ToT gaussian distribution at fixed values of the test pulse voltage
  - Average ToT changes accordingly to the set voltage
  - Per-pixel ToT calibration through test pulse over the whole pixel matrix
  - Calibration validated using radioactive sources
- Next steps
  - Timing and spatial resolution measurement using digital pixels and laser setup



## A possible application

- LHCb RICH detector Upgrade II
  - Current detectors not adequate for RICH in the HL-LHC environment
  - Need for a new photodetector with fine granularity (<1 mm) and time resolution <100 ps (the smaller the better)</li>



#### Future RICH detectors

- Advantage of the proposed device for future RICH detectors:
  - □ 5-10  $\mu$ m position resolution  $\rightarrow$  the pixel size contribution to the Cherenkov angle resolution becomes negligible
  - High granularity (55  $\mu$ m × 55  $\mu$ m) and rate capabilities (2.5 Ghits/s) crucial in applications with large detector occupancies
  - <100 ps resolution per single photon excellent handle for pattern recognition and time-association of the individual photons
  - Negligible detector-related background at room T
  - Robust in magnetic fields
  - Longer lifetime compared to standard applications due to low gain
  - On-detector signal processing and digitization with large number of active channels (~230 k pixels), with limited number of external interconnections (~200)

#### **Radiation hardness**

- For the LHCb Upgrade II we expect, in the RICH region:
  ~2 Mrad TID, ~3 × 10<sup>13</sup> 1 MeV n<sub>eq</sub>/cm<sup>2</sup>, ~1 × 10<sup>13</sup> HEH/cm<sup>2</sup>
- Advantages of the proposed detector:
  - Optical window made of "silica" glass
    - Expect no degradation of window transmittance
  - □ 65 nm CMOS front-end technology
    - Resistant to >100 Mrad Total Ionising Dose
    - Triple Modular Redundancy not implemented in Timepix4
      - □ Single Event Upset mitigation: refresh configuration registers
  - □ FPGA-based back-end electronics far from detector region
    - Signals are digitized inside the vacuum tube
    - Use radiation hard components on-detector (transceivers, etc.)
- Future improvements
  - Use VeloPix2 (PicoPix) ASIC (30 ps TDC, rad. hard by design)

#### **Project** status

- Electronics
  - Timepix4: v2 bare ASIC extensively tested; first tests with Si sensor in summer 2022
    - Detailed study of ASIC performance and calibrations
    - Calibrations with radioactive sources and test pulses
    - Power measurements and cooling system development
  - Development of FPGA-based control board
    - Firmware almost complete
- C++ software development for ASIC configuration, DAQ and analysis
- Ceramic carrier studies
  - Engineering mock-ups
  - Thermal simulation and measurements
  - Simulation of effects of ceramics on 10 Gbps lines (signal integrity)
  - Components connection

#### Summary

- A detector for visible single photons, based on a bare Timepix4 CMOS ASIC embedded in a vacuum tube with a MCP is under development for the detection of up to 10<sup>9</sup> photons/s with simultaneous measurement of time and position with excellent resolutions
  - Fully exploit both timing and position resolutions of a MCP
  - High-performance data acquisition (up to 160 Gbps)
- The presented single-photon imaging technology could enable discovery in different fields of science
   High energy physics, life sciences, quantum entics, etc.

High-energy physics, life sciences, quantum optics, etc.

This project has received funding from the European Research Council (ERC) under the European Union's Horizon 2020 research and innovation programme (grant agreement No. 819627)











