XSEC: New pulse board

ADC

CTRV

Read out counts
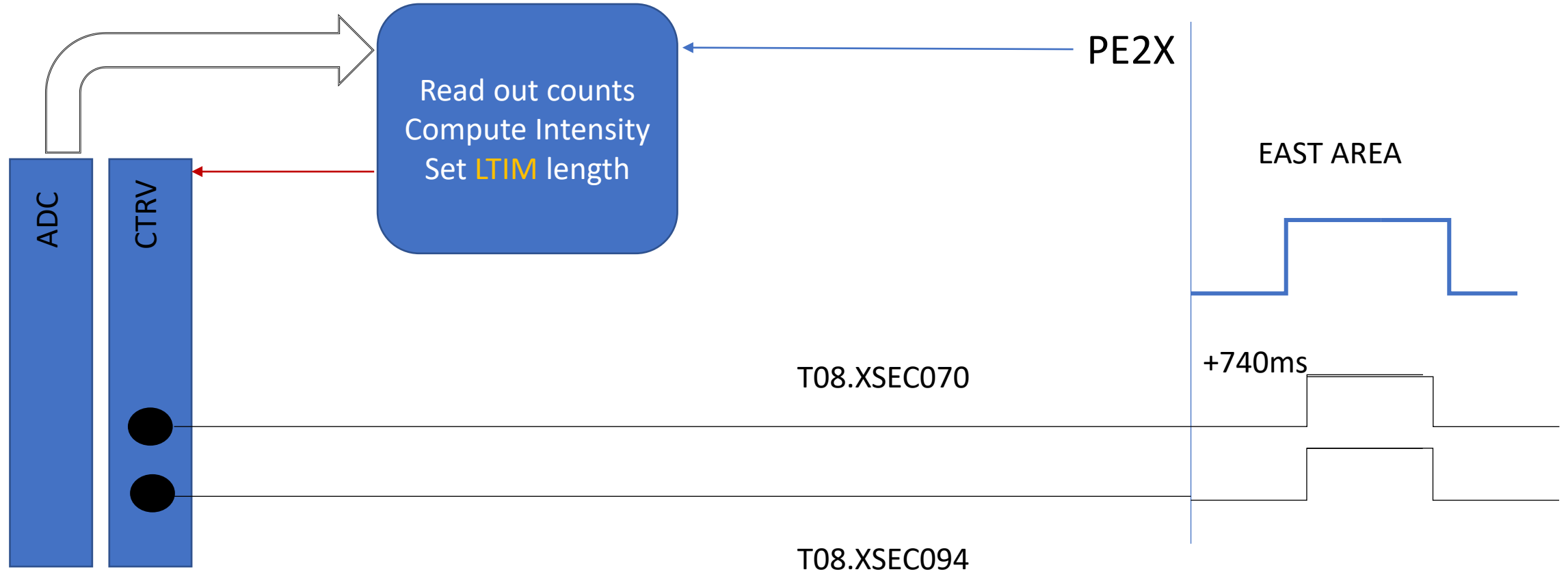Compute Intensity
Set LTIM length

PE2X

EAST AREA

T08.XSEC070

+740ms

T08.XSEC094

Problem: Load event (PE2X) triggers before LTIM length is written thus not taken into account in the current cycle

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