

Data Transfer To Remote GPUs Over High Performance Networks MPI CUDA METHOD DATA TRANSFER Andrea Bocci¹ - Ali Marafi² - Prof.Mohammad Almulla²



Experiment Environment: Software Part

Abbreviations

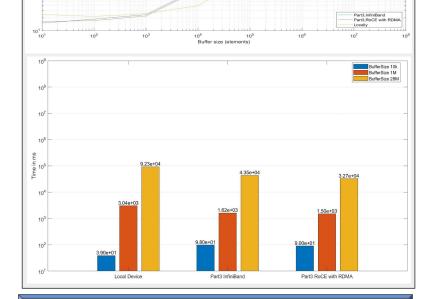
PCIe

Remote Experiment For Part 3

PCIe

GPU

Experiment Goal Experiment Environment: Hardware Part The measurements were performed on two pair of machines To perform the measurements using both local and remote GPUs we wrote two applications: a first one for programming a local GPU, cudaTimeMeasurment, The main purpose of this experiment is to find the most efficient method o and a second for programming a remote GPU, mpiCudaGeneric The first pair is equipped with dual Intel Xeon Gold 6130^[1] "Skylake" CPUs (with PCIe 3.0), Mellanox transferring data over a network connection from a client machine to a remote GPU in a server machine, ConnectX-5 100Gb/s network cards^[2] that can operate in InfiniBand and RDMA over Converged Both programs generate two arrays of single precision floating point numbers with a size variable between 10 and 400 million elements, then transfer them to Ethernet (RoCE) mode, and Intel 10Gb/s Ethernet network cards; one of the two machines is equipped the GPU to perform a computation, and copy the resulting array back to the host memory. The GPU simply computes the pairwise sum of the two arrays; to performing a computation on the remote with an NVIDIA Tesla T4 GPU^[3]; the machines are connected to each other via both Intel and Mellanox GPU, and transferring the results back to emulate the impact of a more complex computation, the sum can be repeated an arbitrary number of times. cards Finally, the result is compared with the result of the same operation performed on the CPU; if the results are correct, the program prints the amount of time To evaluate the impact of using a remote The second pair is equipped with single AMD EPYC 7502P^[4] "Rome" CPUs (with PCIe 4.0), Mellanox spent in the various parts of the task: data transfers and GPU computations. GPU, we need a reference that is not affected by any network overhead. ConnectX-5 Ex 100Gb/s network cards that can operate in InfiniBand and RoCE mode, and Broadcom Both programs use CUDA 11.5^[6] to program the GPUs Therefore, we use a reference the NetXtreme 1Gb/s Ethernet network cards; one of the two machines is equipped with an NVIDIAA10 performance of a machine that does the same computation on a local GPU. GPU^[5]; the machines are connected to each other via both Broadcom and Mellanox cards mpiCudaGeneric uses OpenMPI 4.1^[7] and optionally the Unified Communication X (UCX)^[8] library for the inter-process communications. The Broadcom and Intel cards support only the IP protocol. The Mellanox cards have been tested usin When using the Mellanox cards in InfiniBand or RoCE mode with the UCX library, the communication with the remote GPU can take advantage of the NVIDIA Naively, the local GPU should be the fast the InfiniBand, RoCE, and IP-over-InfiniBand protocols compared to any method that uses a GPUDirect Remote Direct Memory Access (RDMA) to transfer the data directly to or from the remote GPU memory, bypassing the machine's host memory. remote GPU. Experiment FlowChart Exp RDMA: Remote Direct Memory Access is a direct memory access from one machine to the system or GPU memory for another machine without involving either one's operating system, achieving high-throughput, low-latency data transfers. Of the cards tested in this work, only the Mellanox cards support RDMA. RoCE: RDMA Over Converged Ethernet allows remote direct memory access over an Ethernet network, by encapsulating the InfiniBand transport packets over Ethernet. In this work we used RoCE v2, an IP-based protocol Locally that supports routing. Of the cards tested in this work, only the Mellanox cards support RoCE. GPU A10 GPU A10 GPU A4 TCP Over Infiniband: TCP/IP protocol over an InfiniBand connection, encapsulating the IP packets inside nfiniBand packets TCP Over Ethernet: TCP/IP protocol over an Ethernet connection, encapsulating the IP packets inside Ethernet Method nar Part 2 Method nan Part 3 Part 1 Method nar Part 4 Method nan Part 4 Method na Part 1 Method nar Part 2 Method named Part 3 Part 3 No RDM
Part 3 ROMA.
Part 3 ROCE
RDMA.
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S- Part 3 BOCE N
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whernet. Part 1 No RDMA. Part 1 RoCE No Part 2 No RDMA. Part 2 RoCE No an 3 RDMA 110 ted package size. Part 3 RoCE MA. Part 1 No RDMA Part 1 RoCE No 1- Part 2 No RDMA 2- Part 2 RoCE No RDMA 1 btl tcp btl tep AA. art 3 RoCE Part 1 btl tcp iniband. nernet. - Part 2 btl tcp nfiniband. Part 2 btl tcp Part 3 btl tcj Local Experiment Remote Experiment For Part 1 & 2 PCIe PCIe Server GPU PCIe GPU Time Operation GPU Point of Vie Time Operation GPU Point of View For **Part 3**, the time measurements are taken in seven points: Time Input Preparations on Client: Send data from a Client **Pageable** Buffers to a remote GPU through MPI with different Methods: RDMA, For Part 2, the time measurements are taken in seven points For Part 1, the time measurements are taken in seven points The time measurements are taken in four points: Time Input Preparations on Client: Copy data from the Client Pageable Time Input Preparations on Client: Copy data from the Client Pageable Buffers to the Server **Pageable** Buffers through MPI with different Methods: RoCE with No RDMA, TCP over Ethernet, TCP over Infiniband Buffers to the Server **Pinned** Buffers through MPI with different Methods: RoCE with No RDMA, TCP over Ethernet, TCP over Infiniband Time Input Preparations: copy data from **Pageable** Buffer to **Pinned** Buffer, then copy from Pinned Buffer to GPU Buffer. RoCE with RDMA, TCP over Ethernet, TCP over Infiniband. Time Operations on CPU PointView: Computations done on the ime Input Preparations on Server: Copy data from the Server Pageable Time Input Preparations on Server: Copy data from the Server Pinned Time Input Preparations on Server: Receive data from Client Pageable GPU but time taken by CPU. Buffers to the GPU Buffers Buffers to the GPU Buffers Buffers to the GPU Buffers. Time Operations on Device PointView: Computations done on the GPU and taken by GPU. Time Operations on Client PointView: Computations done on the GPU Time Operations on Client PointView: Computations done on the GPU but Time Operations on Client PointView: Computations done on the GPU . time is taken by the Client CPU. but time is taken by the Client CPU. but time is taken by Client CPU. Time Output Preparations: Copy the results from GPU Buffer to **Pinned** Buffer, then Copy from **Pinned** to **Pageable** Buffer. Time Operations on Server PointView: Computations done on the GPU Time Operations on Server PointView: Computations done on the GPU but Time Operations on Server PointView: Computations done on the GPU ime is taken by the Server CPU. but time is taken by Server the CPU. but time is taken by the Server CPU. The calculation of Latency time taken for the Local transfer data is: [(Time input perpartions + Time Operations on Device + Time output preparations) - Time Operations on Server]. Time Operations on Device PointView: Computations done on the GPU Time Operations on Device PointView: Computations done on the GPU Time Operations on Device PointView: Computations done on the GPU and the time is taken by the GPU. and the time is taken by the GPU. and the time is taken by the GPU. Time Output Preparations on Server: Copy data from the GPU buffer to Time Output Preparations on Server: send data from the GPU buffer to lime Output Preparations on Server: Copy data from the GPU buffer to the Server Pageable Buffer the Server **Pinned** Buffer. the Client Pageable Buffers. Time Output Preparations on Client: Receive data from the GPU buffer to the Client **Pageable** Buffer through MPI with different Methods: RDMA, RoCE with RDMA, TCP over Ethernet, TCP over Infiniband. ime Output Preparations on Client: Copy data from the Server Pageable Time Output Preparations on Client: Copy data from the Server Pinned buffer to the Client Pageable Buffer through MPI with different Methods: buffer to the Client Pageable Buffer through MPI with different RoCE with No RDMA, TCP over Ethernet, TCP over Infiniband. Methods: RoCE with No RDMA, TCP over Ethernet, TCP over Infiniband Tesla A4 GPU Results: Local, Remote over InfiniBand (with RDMA), and RoCE (with RDMA) Tesla A10 GPU Results: Local, Remote over InfiniBand (with RDMA), and RoCE (with RDMA)



Tesla A4 GPU Results: Local, Part 1, and Part 2 (without RDMA) 10 107 10⁶ 10 103

Conclusion

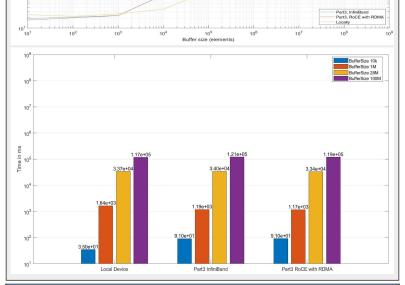
As expected, the InfiniBand cards are significantly faster than the Ethernet cards as we tested

On the Mellanox cards:

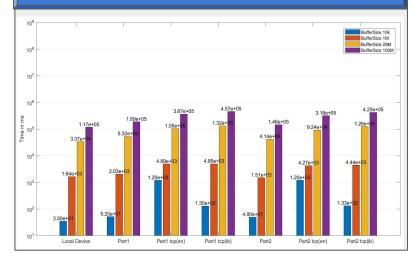
- We did not observe a significant difference in performance between the InfiniBand and RoCE protocols, Even without using the RDMA capability, the native InfiniBand protocols is 2-3 times faster than the
- IP-over-InfiniBand protocol; When paired with a communication library that can leverage the RDMA capabilities, the data transfer to
- remote GPUs is as fast (or even faster) than using a local GPU.

This is a very encouraging result for extending the use of GPUs beyond those available on a local machine.

Comparing the results from the "Part 1" and "Part 2" measurements we can see that - when RDMA is not available - the use of an intermediate buffer in pinned host memory plays a vital role in speeding up the data transfers with a remote GPU.



Tesla A10 GPU Results: Local, Part 1, and Part 2 (without RDMA)



Reference

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- html https://support.mellanox.com/s/product/details/a2v5000000052eDAAQ/connectx5-card https://www.nvidia.com/content/dam/en-zz/Solutions/Data-Center/tesla-t4/t4-tensor-core-product-brief.pdf https://www.nvidia.com/condent/dam/en-zz/Solutions/Data-Center/a10/pdf/A10-Product-Brief.pdf https://www.nvidia.com/condent/dam/en-zz/Solutions/Data-Center/a10/pdf/A10-Product-Brief.pdf https://docs.nvidia.com/conda/cuda-toolkit-release-notes/index.html https://www.nvidia.com/conda/cuda-toolkit-release-notes/index.html

- [2]. [3]. [4]. [5]. [6]. [7]. [8].

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https://www.open-mpi.org/ https://openucx.readthedocs.io/en/master/