

23rd Virtual IEEE Real Time Conference

Thursday, 4 August 2022

Architectures, Intelligent Signal Processing & Simulation (11:00 - 12:40)

time	[id] title	presenter
11:00	[27] PulseDL-II: a system-on-chip neural network accelerator for timing and energy extraction of nuclear detector signals	Dr AI, Pengcheng
11:20	[113] The LHCb HLT2 storage system: a 40 GB/s system made from commercial off-the-shelf components and open-source software	Mr CIFRA, Pierfrancesco
11:40	[106] The Real-Time System for Distribution of Clock, Control and Monitoring Commands with Fixed Latency of the LHCb experiment at CERN	FEO, Mauricio
12:00	[14] Using FPGA-based AMC Carrier Boards for FMC to Implement Intelligent Data Acquisition Applications in MTCA.4 Systems using OpenCL	Mr RIVILLA, Daniel RIVILLA, Daniel
12:20	[84] ERSAP: Towards better HEP/NP data-stream analytics with flow-based programming	Dr GYURJYAN, Vardan