

23rd Virtual IEEE Real Time Conference

Thursday, August 4, 2022

Architectures, Intelligent Signal Processing & Simulation (11:00 AM - 12:40 PM)

time	[id] title	presenter
11:00 AM	[27] PulseDL-II: a system-on-chip neural network accelerator for timing and energy extraction of nuclear detector signals	Dr AI, Pengcheng
11:20 AM	[113] The LHCb HLT2 storage system: a 40 GB/s system made from commercial off-the-shelf components and open-source software	Mr CIFRA, Pierfrancesco
11:40 AM	[106] The Real-Time System for Distribution of Clock, Control and Monitoring Commands with Fixed Latency of the LHCb experiment at CERN	FEO, Mauricio
12:00 PM	[14] Using FPGA-based AMC Carrier Boards for FMC to Implement Intelligent Data Acquisition Applications in MTCA.4 Systems using OpenCL	Mr RIVILLA, Daniel RIVILLA, Daniel
12:20 PM	[84] ERSAP: Towards better HEP/NP data-stream analytics with flow-based programming	Dr GYURJYAN, Vardan