

## Common building blocks: FPGA testing

*Wednesday 2 March 2022 09:30 (15 minutes)*

The presentation discusses the challenges one has to face when testing FPGAs, proposing a testing methodology for the LHC environment focusing on the NG-Medium and the PolarFire results. These will be compared with other, currently used, FPGAs, the SmartFusion2 and ProASIC3. A failure rate estimation analysis for the HL-LHC environment is presented showing the advantages and disadvantages of each FPGA.

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**Session Classification:** Research and Development