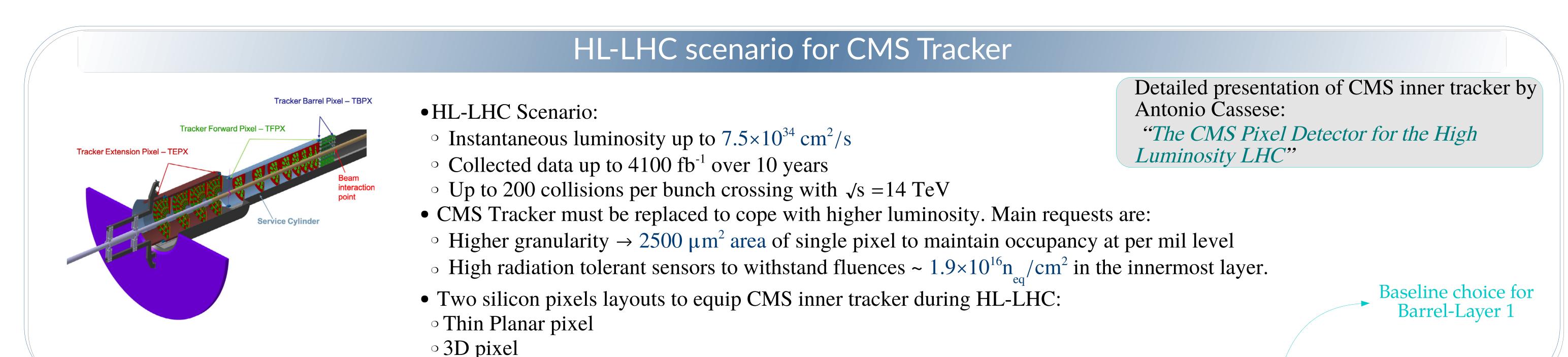


Results obtained with FBK Pixel Sensor prototypes for the HL-LHC Tracker Upgrade of the CMS experiment

iWoRiD 2022 – 23rd international workshop on Radiation Imaging Detectors

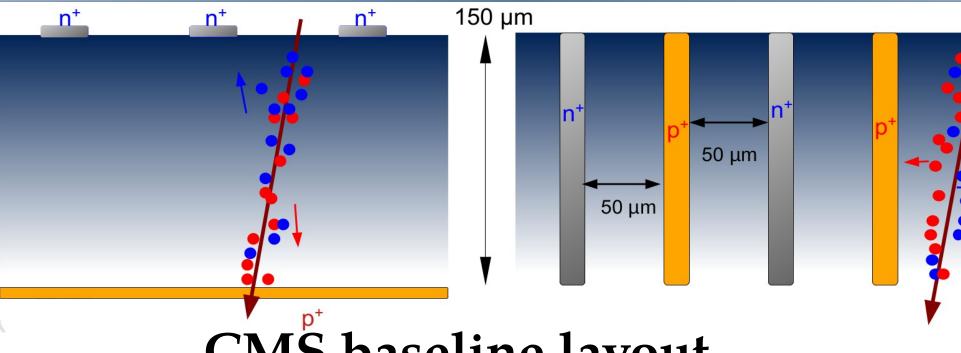




Pixel sensor protoypes

Planar Pixel

- Advantages of planar pixels:
- Well-known technology widely used in HEP experiments
- Easy to fabricate
- Large Power dissipation due to large bias voltage and leakage current (after irradiation)
- FBK planar "Mask Aligner" batches: **Bump Pad** • Bitten design • No Punch Through option BEAM. BRUNO KESSLER

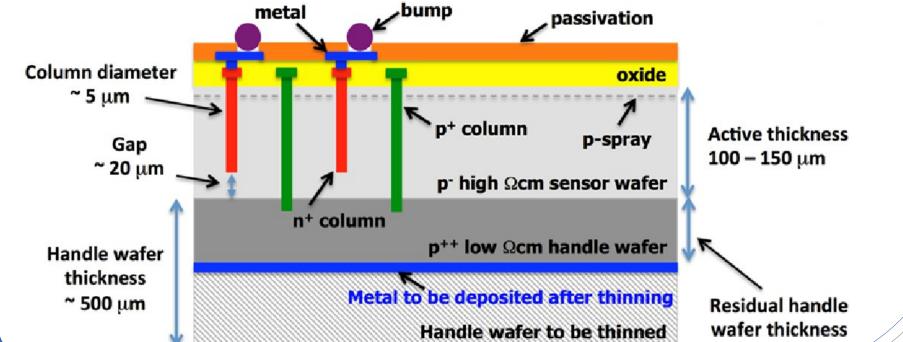


CMS baseline layout

- CMS baseline design for pixel sensors:
- Sensor type n-in-p
- Pitch $25 \times 100 \,\mu\text{m}^2$ and active thickness $150 \,\mu\text{m}$
- During R&D, sensor prototypes with prototype chip RD53A via bump-bonding:
- 65 nm CMOS technology
- Three front-ends for development purpose
- \square CMS \rightarrow linear FE
- Sensors developed by FBK Trento in collaboration with **INFN** Firenze
- Sensors irradiated at KIT with low energy protons (23) MeV) up to 2.4×10¹⁶ n_{eq}/cm^2
- Sensors measured with 6 GeV/c electrons beam at DESY

3D Pixel

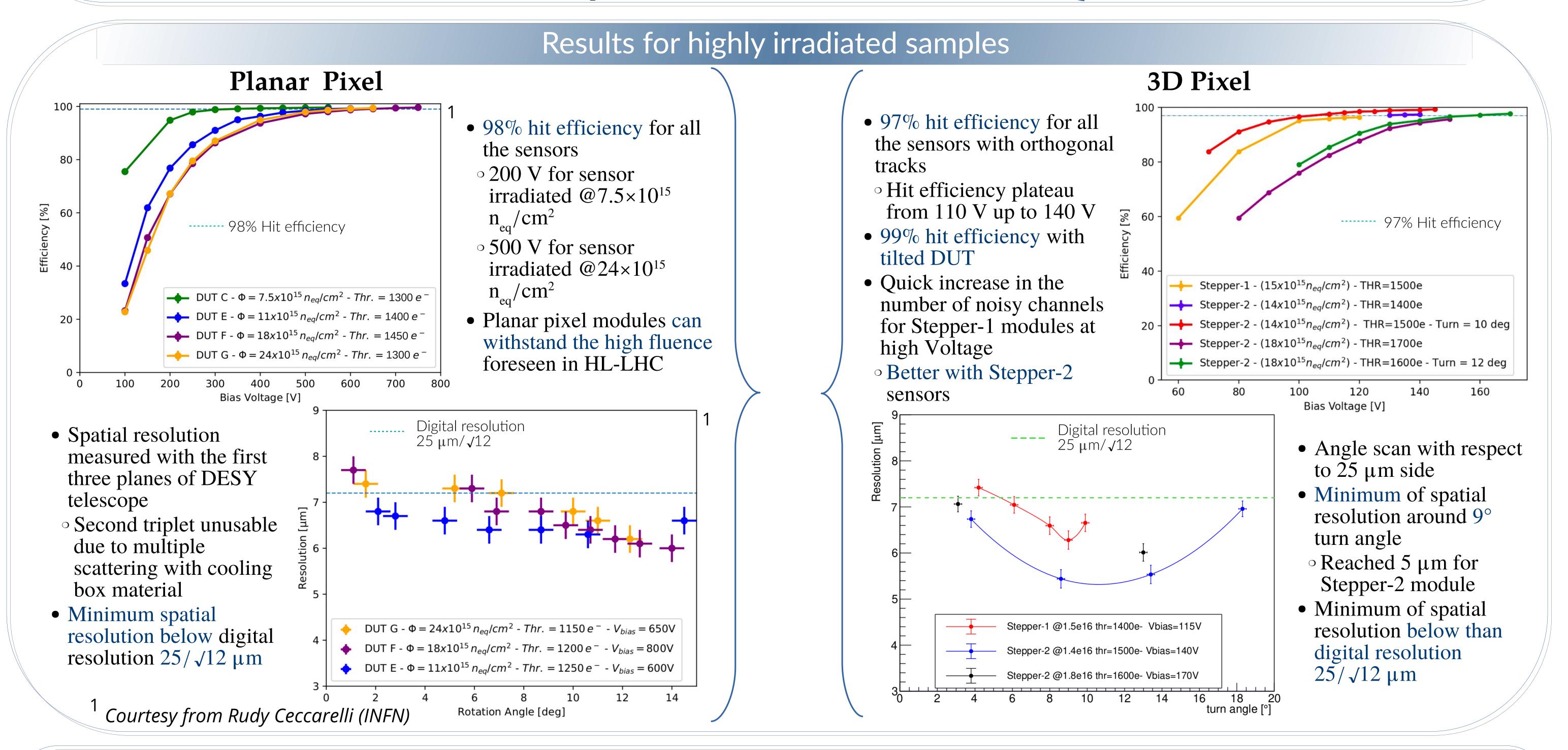
- Pros and Cons of 3D pixels:
- Reduced depletion voltage and power consumption
- Shorter signal collection path for ionization charges
- reduced trapping probability after irradiation
- Small inefficiency due to passive material (columns) with orthogonal tracks.
- Two 3D FBK "Step and Repeat" batches, Stepper-1 and Stepper-2:
- $^{\rm o}\,Reduced$ gap between n^+ columns and sensor backside in Stepper-2 batch
- Both n⁺ and p⁺ columns are etched only from the front-side (single face process)





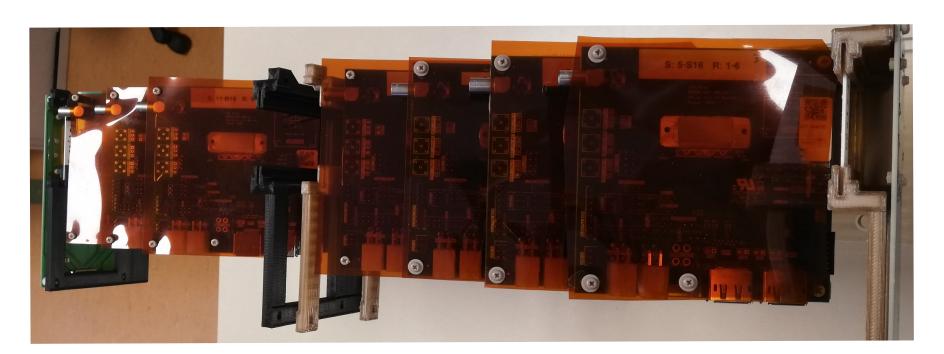
telescope

50 – 100 μm



Outlook and future perspectives

- Further investigation for irradiated 3D pixel
- Irradiation at CERN-IRRAD with 24 GeV/c protons
- Lower TID with respect to low energy protons
- \circ Expected fluence from $1.5 \times 10^{16} n_{eq}/cm^2$ up to $1.8 \times 10^{16} n_{eq}^{2}/cm^{2}$



- First 3D and Planar sensors assembled with CROC-v1 (CMS-ReadOut Chip prototype version 1)
- Final Chip prototype
- Only linear Front-End
- 145142 Channels

