





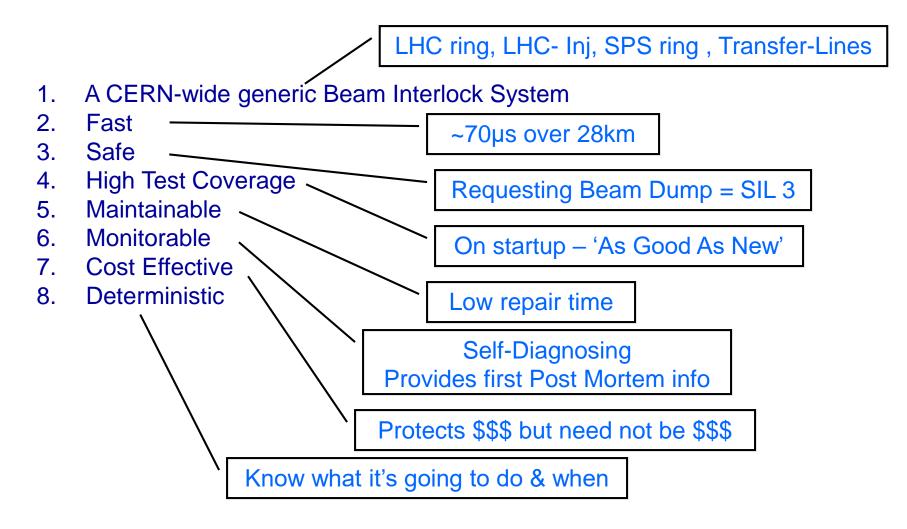
Realization of the interlocking between SPS, LHC and CNGS and open issues

B.Puccio & B.Todd (AB-CO-IN)



Specification

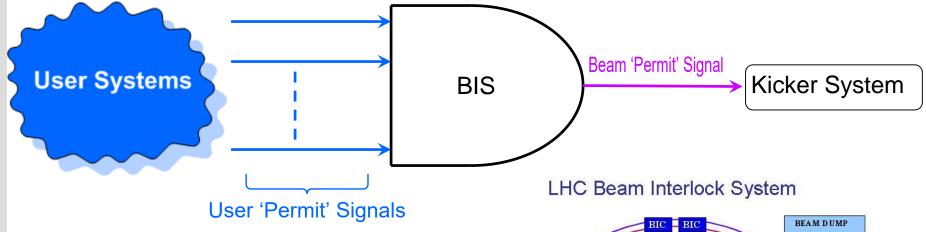






Function and the LHC Layout





- 16 Beam Interlock Controllers (BIC)
- 4 fibre-optic channels: 1 clockwise
 & 1 anticlockwise for each Beam
- 10MHz Permit Loops generated at IP6
 - Signal can be cut by any BIC
 - Signal can be monitored by any BIC



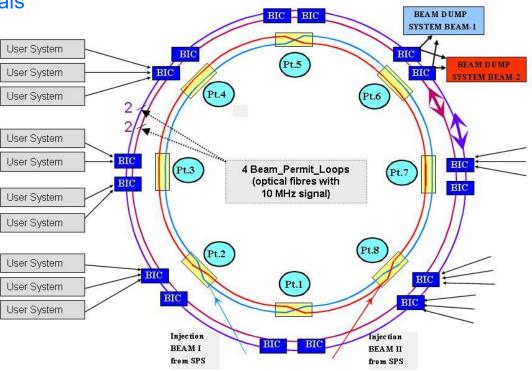
Missing 10MHz triggers BEAM DUMP!

■ Beam-1 / Beam-2 are independent!

Some *User Systems* give simultaneous permit.

Others give independent permit.

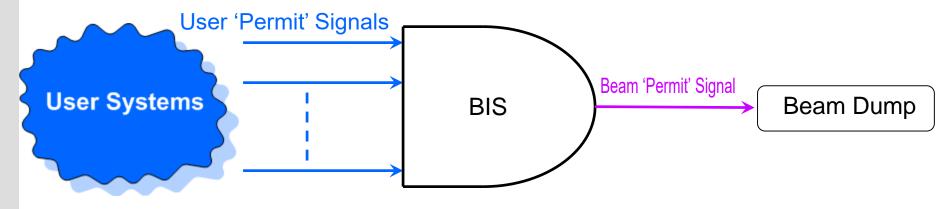
~120 User Systems distributed over 28kms





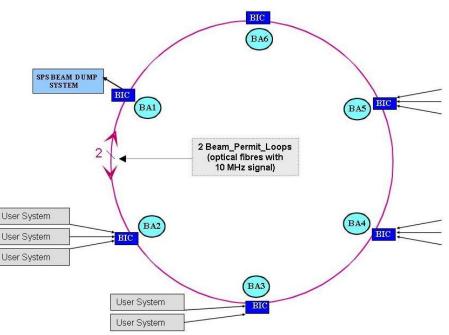
Same function with the SPS layout





- similar as LHC with only <u>one</u> beam
- 6 Beam Interlock Controllers (BIC)
- 2 fibre-optic channels: (1 cw & 1 anticw)
- ~30 User Systems

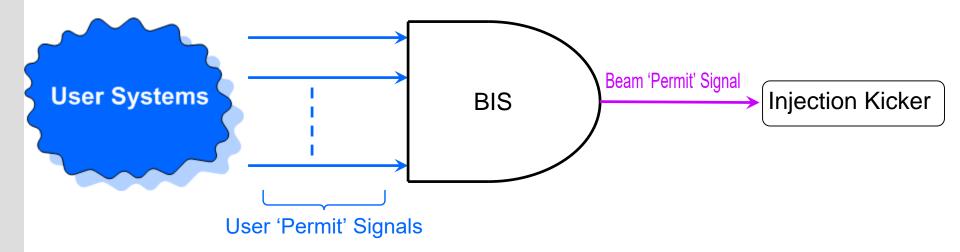
SPS Beam Interlock System





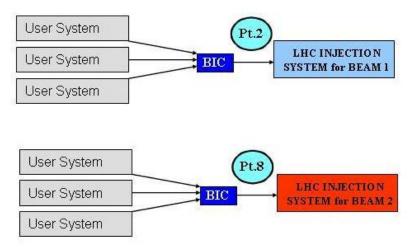
Same function with the LHC Injection layout





Injection Beam Interlock System

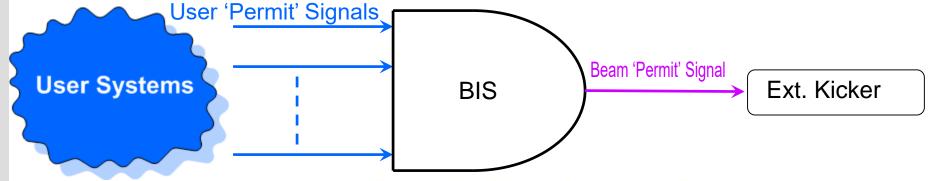
- 1 BIC per Injection region
- fiber-optic or copper outputs are available
- ~20 User Systems





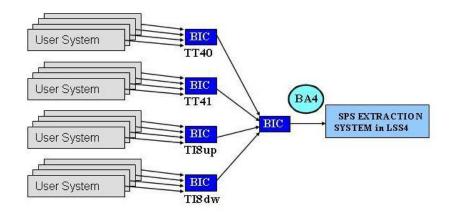
Same function with the SPS-LHC-CNGS Transfer lines layout

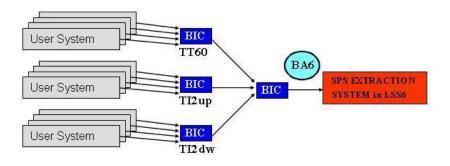




Extraction Beam Interlock System

- several "slaves" BICs(each one is managing a part of transfer line)
- One "Master" BIC per extraction kicker
- fiber-optic or copper outputs are available
- ~60 User Systems in total



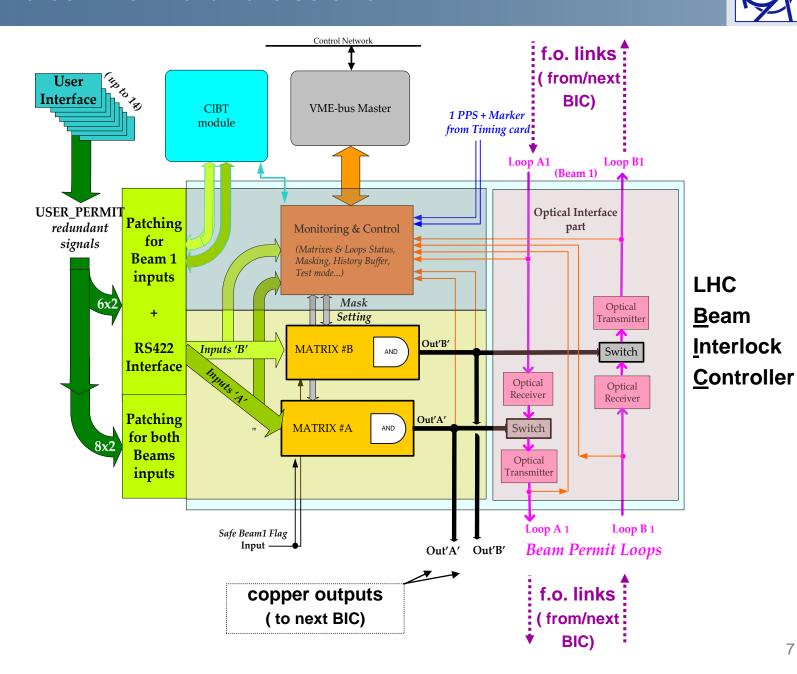






BIC: a common hardware solution



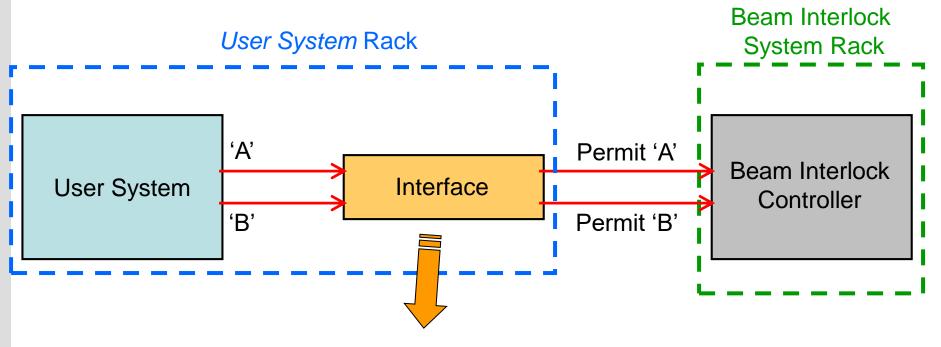






Beam Interlock User Interface: another common solution



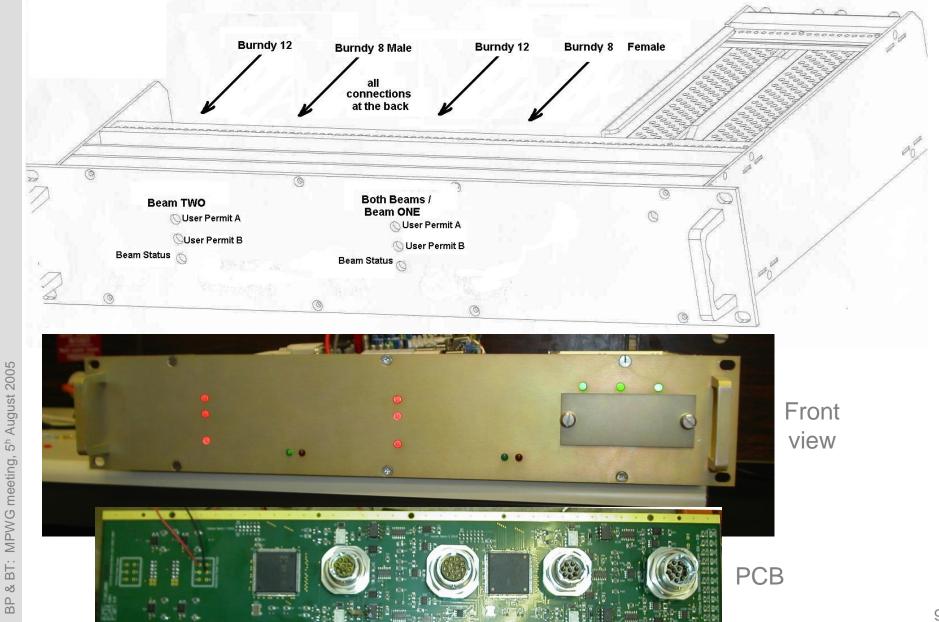


- allows direct connection with many different types of hardware platform
- takes the two USER_PERMIT signals
- transmit them to the nearest BIC in a <u>safe</u> and <u>reliable</u> manner
- using differential signals (RS422 standard) on copper cable: 1.2km distance max.
- 19" rack mounted board (height of 2U)
- fully redundant Power Supplies
- Hw within the panel can be <u>tested and monitored at distance</u>.



Beam Interlock User Interface View

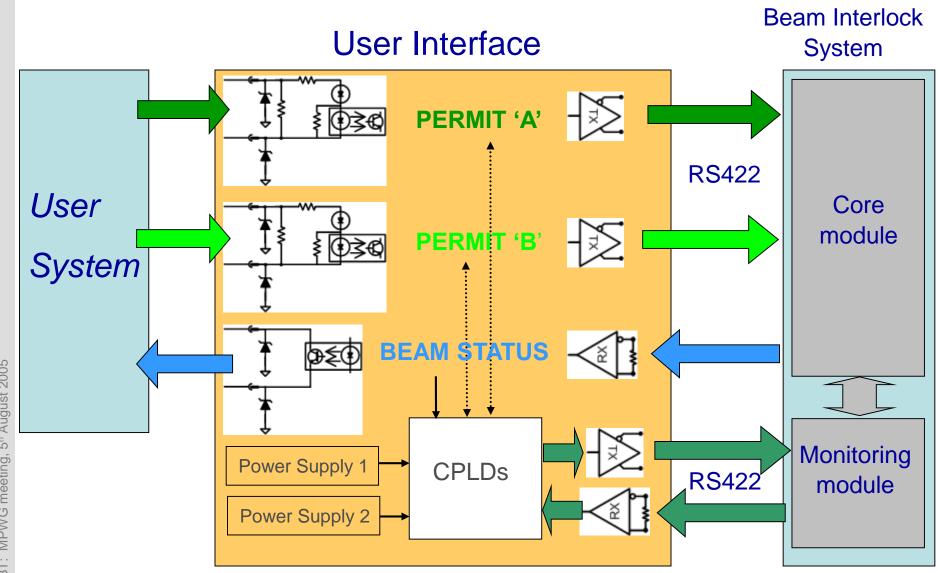






Beam Interlock User Interface: Detailed Block Diagram





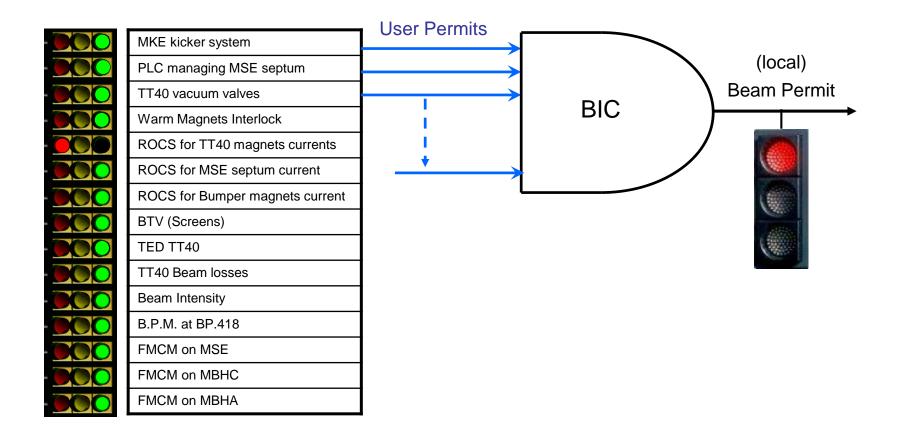




BIC: simple Hw solution ⇒ easy to commission



an example with BIC managing TT40:









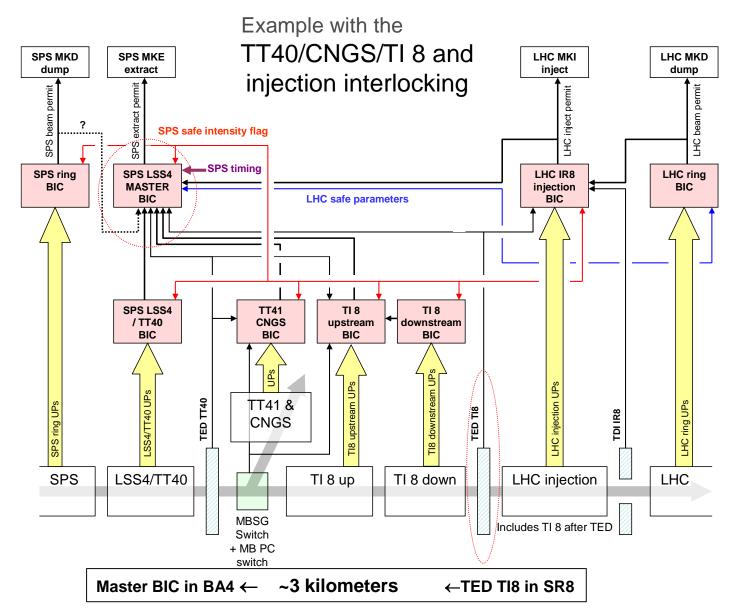
The open issues...



Technical issue (1/2):



How to transmit User Permits when distance > 1.2km?

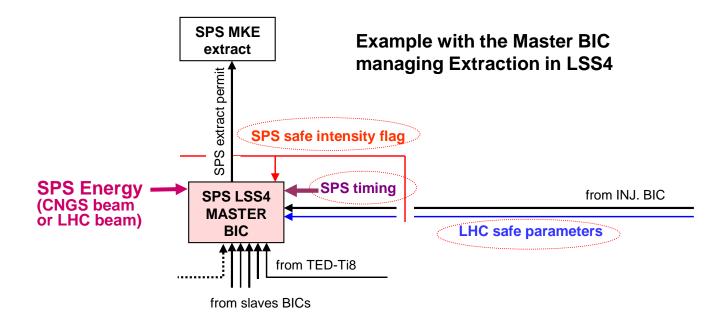




Technical issue (2/2):



How to "translate" SLP data and Timing frame as User Permits?



How to link the SPS Energy value to the Master-BIC (i.e. as User Permit)?



BP & BT: MPWG meeting, 5^h August 2005

Operational issue



Master BIC is diverging from all other BICs

How to perform its commissioning and perform efficient cold check out?

| | TT40 User Permits | 1 | 1 | 1 | 1 | - 1 | Truth table for Master-BIC in LSS4 | |
|--------------------------|-------------------------------------|---------------------------|----------------------------|---|---|--------------|------------------------------------|--|
| | TT41 User Permits | х | 0 | 0 | 0 | 1 \ | | |
| | TI 8 up and downstream User Permits | х | 1 | 1 | 1 | 0 | | |
| | LHC IR8 injection User Permits | х | x | 1 | 1 | х | | |
| | TED upstream 'IN beam' | 1 | 0 | 0 | 0 | 0 | `` | |
| | TED upstream 'OUT of beam' | 0 | 1 | 1 | 1 | 1 | \ Extraction | |
| | TED downstream 'IN beam' | х | 1 | 0 | 0 | х | Beam Permit | |
| | TED downstream 'OUT of beam' | х | 0 | 1 | 1 | х | <u> </u> | |
| | TDI / TCLI in 'PROTECT' position | х | х | Х | 1 | х | | |
| | LHC Beam Permit | х | х | Х | 1 | х | ! | |
| | LHC Beam Presence Flag | х | х | Х | 1 | х | | |
| | LHC Safe Beam Flag | х | Х | Х | 0 | х | / | |
| | SPS Safe Beam Flag | Х | Х | 1 | Х | х | | |
| | LHC beam type (timing signal) | х | 1 | 1 | 1 | 0 / | | |
| | CNGS beam type (timing signal). | х | 0 | 0 | 0 | _ M_ | | |
| | | | | | | | | |
| | | | | | | Beam to CNGS | | |
| Distributed information: | | | High intensity beam to LHC | | | | | |
| - Safe LH | | Low intensity beam to LHC | | | | | | |
| - Timing | | Beam to TI 8 TED | | | | | | |
| Beam to LSS4/TT40 TED | | | | | | | 15 | |



Conclusions



- → BIC designed for LHC could be "easily" used for interlocking LHC Injection & SPS Extraction
- → Generic solution with unique Hw + common SW

(BIC + User Interfaces)

(Monitoring purpose only)

- → Redundancy throughout: User Systems ⇒ BIS ⇒ Kicker system
- → Designed to be Safe, Fast, Monitorable, Deterministic,...
- → Simplicity for crucial process ⇔ Effort on monitoring and maintainability
- Cost effective:

 - User Interface

 1k CHF each (connection cable not included)
 - → Any problem for interlocking LHC injection
 - → Some issues for interlocking the SPS-LHC-CNGS Transfer Lines:
 - Technical ones (User System@3km? SLP data=User Permit? SPS Energy=User Permit?)
 - Operational one with Master BIC's commissioning







That's all Thank you!