

- On 11 September 2008 already operation with circulating beam
- At 22:45:08, the beam 2 was dumped by the beam dumping system
- The dump was caused by a water fault in the DC cables in the QD/QF circuit in arc 81
- This event allowed to address the performance of the interlock / machine protection systems at a very early state, as well as to understand the functionality of the post mortem (transient data) recording

Thanks for the help from B.Goddard, J.Uythoven, A.Rijllart, D.Nisbet and others

	lagnet current		FGC 51_self (FGC 51_self (080911-224508.580_F 080911-224508.580_F	RPHE.UA87.RQD.A81:I_A RPHE.UA87.RQD.A81:I_REF	
~~~		N	FGC 51 self (	080911-224508.580 F	RPHE.UA87.RQF.A81:I A	1
<mark>l = 0.03%</mark>		/	FGC 51_self (	 080911-224508.580_F	RPHE.UA87.RQF.A81:I_REF	:
1						
		Powering	Interlock System			
	ar fault 558 ms	Dump ree	quest 561 ms			
val			504 500			
Pow	er converter informed	Beam Du	imp 561.523 ms			
Pow	ering Interlock System					
	- <u> </u>					
		14-224E09 E90 DBL				
	E roc 51_seir 0809	11-224506.560_KPF	E. OA67. KQD. A61			
	0.c					
	Time	PROPERTY	SYMBOL	ACTION		
	2008/09/11 19:28:44.0!	3000 REF.RUN	1.000	FS(0)		
	2008/09/11 19:28:44.00	700C STATE.PC	RUNNING	SET		
	2008/09/11 19:29:27.70	700C STATE.PC	IDLE	SET		
		8037 DIM.VS	STA OFF_RECEIVED	on		
	2008/09/11 22:45:08.5	2008/09/11 22:45:08.558037 DIM.VS				
	2008/09/11 22:45:08.5!	8037 DIM.VS	STAVS_CONTROL	REMOTI		
	2008/09/11 22:45:08.5!	8037 DIM.VS	STAVS_VLOOP_SATURATED	no	<b>`</b>	
	2008/09/11 22:45:08.5!	8037 DIM.VS	STA SUB_1	ok		
	2008/09/11 22:45:08.5!	8037 DIM.VS	STA SUB_2	ok		
	2008/09/11 22:45:08.5!	58037 DIM.VS	STA SUB_3	ok		
	2008/09/11 22:45:08.5!	58037 DIM.VS	STA SUB_4	ok		
	2008/09/11 22:45:08.5!	58037 DIM.VS	STA SUB_5	ok		
	2008/09/11 22:45:08.50	2000 STATE.PC	FLT_STOPPING	SET		
		2000 VS STATE	VS_FAST_STOP	SET		
	2008/09/11 22:45:08.56			CLR		
	2008/09/11 22:45:08.50	2000 DIG.STATUS	VLOOPOK			
	2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50	2000 DIG.STATUS	VLOOPOK VSEXTINTLK	SET		
	2008/09/11 22:45:08.54 2008/09/11 22:45:08.56 2008/09/11 22:45:08.56 2008/09/11 22:45:08.56	2000 DIG.STATUS 2000 DIG.STATUS 2000 DIG.STATUS	VLOOPOK VSEXTINTLK PCDISCHRQ	SET SET		
	2008/09/11 22:45:08.54 2008/09/11 22:45:08.56 2008/09/11 22:45:08.56 2008/09/11 22:45:08.56 2008/09/11 22:45:08.56	22000 DIG.STATUS 22000 DIG.STATUS 22000 DIG.STATUS 22000 DIG.STATUS 22000 DIG.STATUS	VLOOPOK VSEXTINTLK PCDISCHRQ PCPERMIT	SET SET CLR		
	2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50	52000 DIG.STATUS 52000 DIG.STATUS 52000 DIG.STATUS 52000 DIG.STATUS	VLOOPOK VSEXTINTLK PCDISCHRQ PCPERMIT	SET SET CLR		
	2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50 2008/09/11 22:45:08.50	22000 DIG.STATUS 22000 DIG.STATUS 22000 DIG.STATUS 22000 DIG.STATUS 22000 DIG.STATUS	VLOOPOK VSEXTINTLK PCDISCHRQ PCPERMIT	SET SET CLR		
	2008/09/11 22:45:08.5/ 2008/09/11 22:45:08.5/ 2008/09/11 22:45:08.5/ 2008/09/11 22:45:08.5/ 2008/09/11 22:45:08.5/	52000 DIG.STATUS 52000 DIG.STATUS 52000 DIG.STATUS 52000 DIG.STATUS	VLOOPOK VSEXTINTLK PCDISCHRQ PCPERMIT	SET SET CLR		



## Data from Powering Interlock System

## BIS Supervision Application

## <u>S</u>YSTEM



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- The interlock / MP systems worked as expected
- The beam dump was performed before the magnet current changed
- The systems are well synchronised
  - beam dumping system for the time being with a resolution of better than one μs
  - powering interlock system (PLC based) with an error of 1-2 ms
  - beam interlock system at one μs (issues with logging / history buffer)
  - accuracy and stability of synchronisation still to be adressed
- The tools are available to understand what happened
- The objective is to understand the cause of each beam dump as well as the subsequent events
  - work is ongoing to improve the tools, with of automatic analysis for beam dumps