Experience with the LBDS XPOC-Checks

- XPOC checks that the last beam dump was executed correctly
 - Kicker waveforms (operational)
 - Vacuum (operational)
 - BLMs (operational as of today)
 - Other beam Instrumentation: BTVDD, BPMD, BCT (being put in operation)
 - Abort gap monitor (to be put in operation)



Jan Uythoven, MPP 11/12/09

Functionality of XPOC

- XPOC has to guarantee the 'as-good-as-new' status of the LBDS before each new fill
- No injection should take place if last XPOC has not been ok
 - If XPOC not OK need expert to give the green light
 - All anomalies should be understood
 - Might require some dumps with safe beams to re-qualify the beam dumping system for higher intensities
- If kickers really 'fail':
 - Backed-up by IPOC, which requires reset on the equipment level (IPOC explorer)

Signaling a bad XPOC

- Sequencer
 - Sequencer task which checks latest XPOC result and time stamp of latest relative to last time the kickers have been pulsed (IPOC)
 - Easily skipped if it has already failed some times, often due to other reasons
 - Will get bad XPOC after sending out dumped event without loops closed
 - Also needs to be skipped after an 'acknowledge' of the SIS
- Latch bit used by the SIS
 - After bad XPOC the latch turns bad
 - Stops the beam via SIS
 - Can be masked in SIS but this is disciplined
 - However, the XPOC result stays bad and needs to be skipped

Reset button under RBAC

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Experience so far / Conclusions

- Some initial problems which caused XPOC tasks in the sequencer to fail
 - Noisy signals -> enlarge limits
 - Check of energy between different modules too tight -> adjusted
 - Time stamps between different IPOCs, to be compared with XPOC -> fix in sequencer task this morning
 - General controls / communication problems which result in negative XPOC results
- Now this is (almost?) all sorted, but does the operator not skip the XPOC task out of convenience?
- How can we be sure that the task is executed for each fill ?

• With the present 'habits' and possibilities to skip sequencer tasks, the SIS latching is mandatory

• At some point the SIS interlock of the XPOC will have to become unmaskable

• We will need to converge to a more standard way of running sequences and clear instructions which tasks are mandatory to be run before each injection or each fill and under which conditions tasks can be skipped: **PROCEDURES**