Beam Interlock System for Linac4, Transfer Lines & PS Booster

LINAC4

Linac3

ER

B. Puccio & C. Martin (TE/MPE) MPP meeting of 31st Aug. 2012

TT2

Booster

Plan

- Final layout
- Connected systems
 - User Systems
 - Target Systems
- Current status:
 - Preparation phases, 3MeV Test Stand, Prototype for PSB injection...
- Outstanding issues
- Summing up

Specification

CERN CH-1211 Geneva 23 Switzerland



	LINAC4 Project Document No.
	L4-CIB-ES-0001 rev.1.0
CE	RN Div./Group or Supplier/Contractor Document No
	BE/OP
-	EDMS Document No.
	EDMS Document No. 1016233

Date: 2012-08-31

This presentation based on the new version of the Engineering Specification

Engineering Specification

BEAM INTERLOCK SPECIFICATIONS FOR LINAC4, TRANSFER LINES AND PS BOOSTER WITH LINAC4

Abstract

The beam interlock system for Linac4 and its transfer lines to the PSB will be based on a mixed system comprising hardware interlocks provided via the BIS (Beam Interlock System), software interlocks based on the SIS (Software Interlock System) and the concept of External Conditions used currently in the PS complex. This document summarises the beam interlock specifications to safely operate Linac4, the Linac4-to-PSB transfer lines and the PSB with Linac4 injection.

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Initial objectives

By reusing the BIS solution

- **Same Hw:**
 - Fast, Safe, Reliable
 - Standardised interface (CIBU)
 - Proven solution
 - Cost-effective
 - Same Monitoring Sw:
 - Unique application in CCC
 - 100% Online test coverage
- Operational flexibility
 - Software Interlock Inputs
 - External Condition signals used as User_Permits
 - Masking available on half of input channels

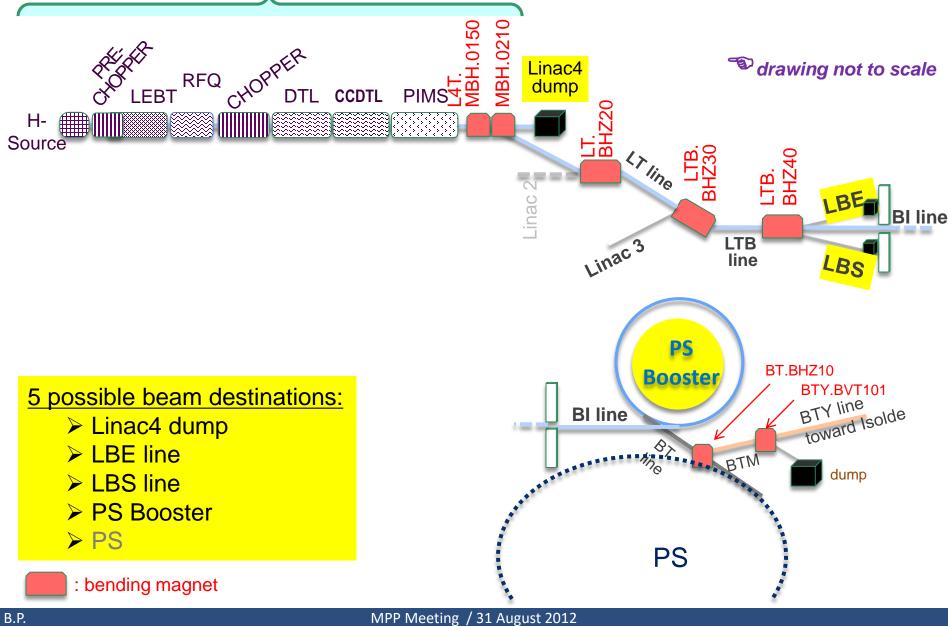


Plan

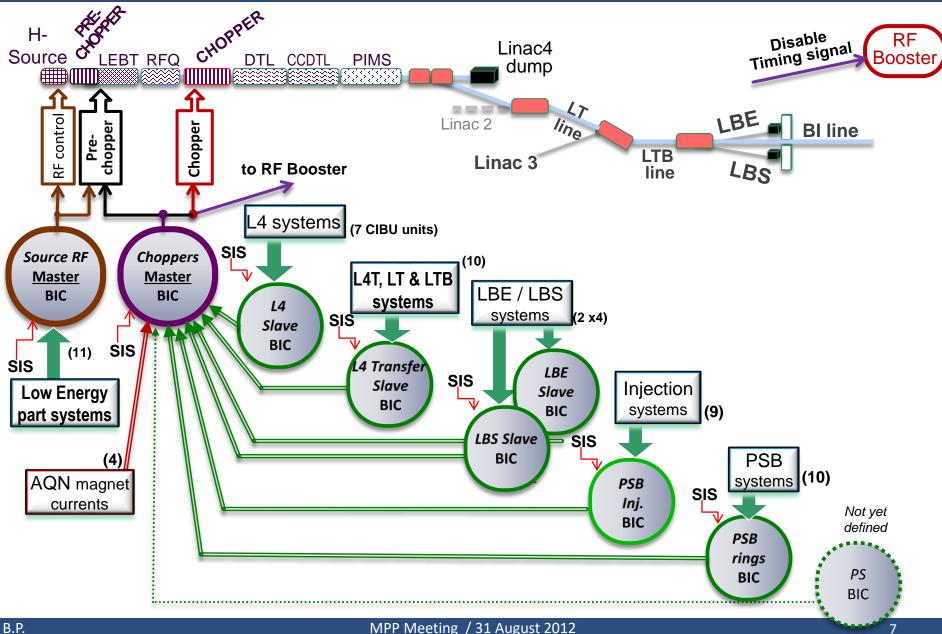
• Final layout

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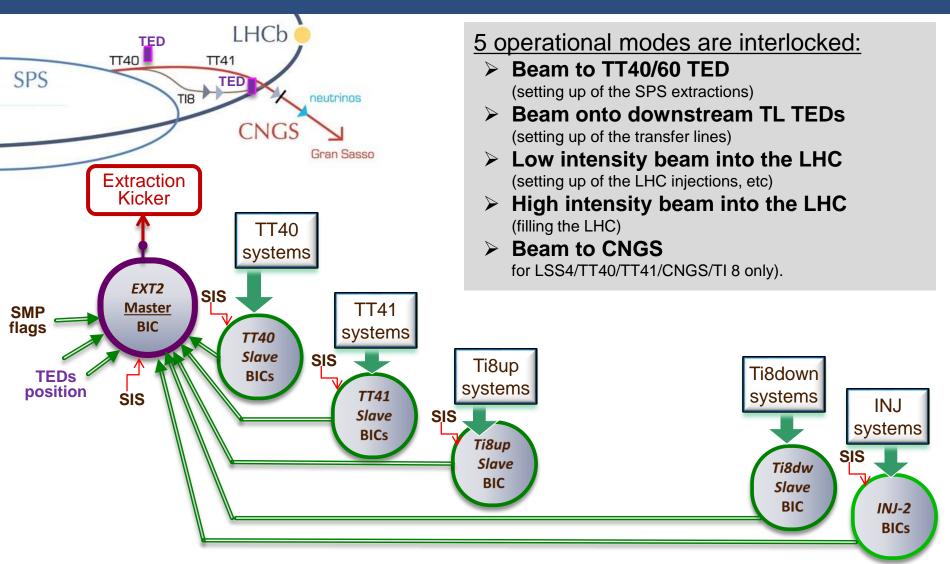
Layout of Linac4, Transfer Lines & PS Booster



Interlocking Architecture for Linac4 and Transfer lines

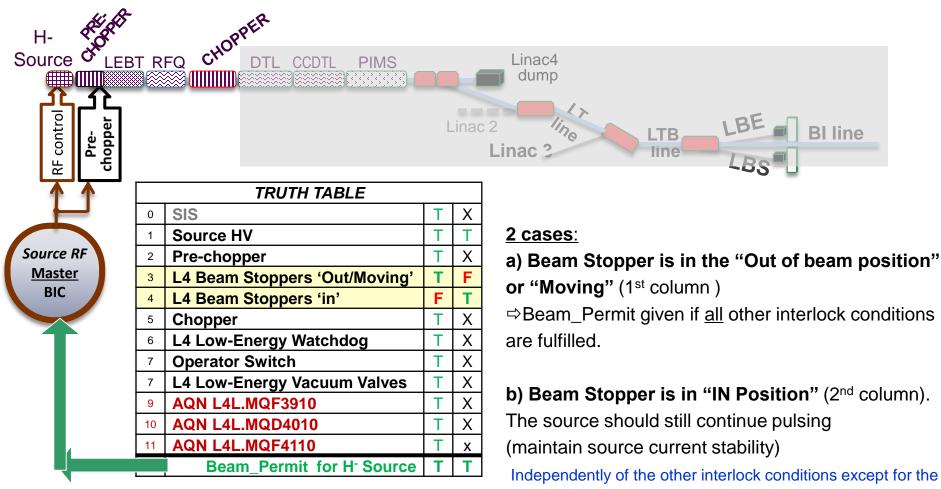


Reminder: Interlocking Architecture for SPS Extraction line (beam-2)



- Sectorization to minimize "Xtalk" between LHC and CNGS, and allow flexible operation & setup.
- Each sector / zone controlled by one (or more) dedicated BIC.

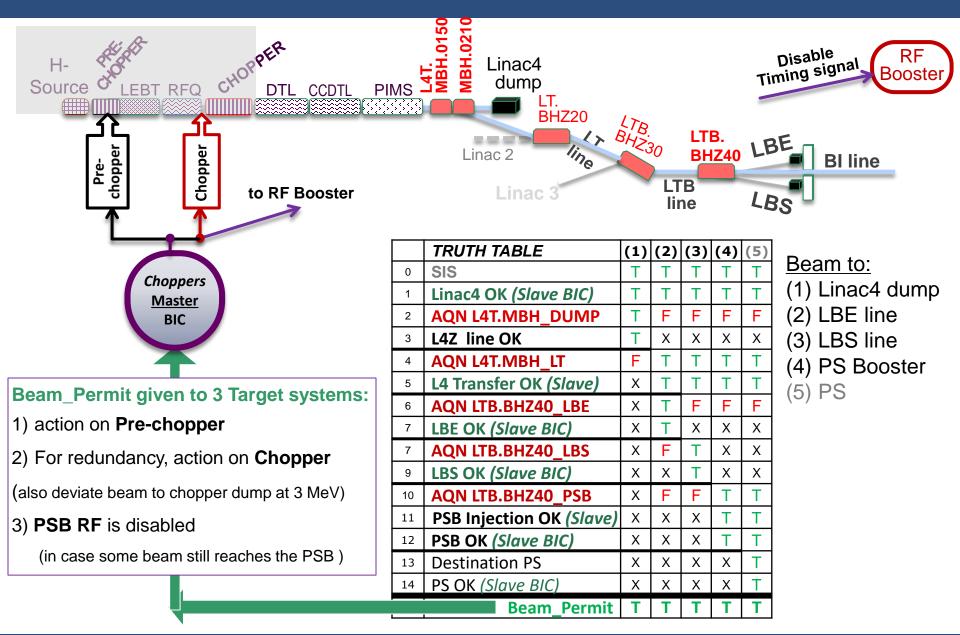
"Source RF" Master BIC



Beam Permit signal provided to 2 Target systems: RF Control & Pre-Chopper

source high voltage itself.

"Choppers" Master BIC



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11

User Systems connected to BIS (L4 & Transfer lines)

4 systems

L4 Slave BIC 4T, LT & LTB

systems

Transf

Slave BIC LBE/LBS

LBS Slave BIC

LBE

Slave

SIS

Source RF <u>Master</u> BIC

Low Energy

part system

AQN magnet currents Choppers Master

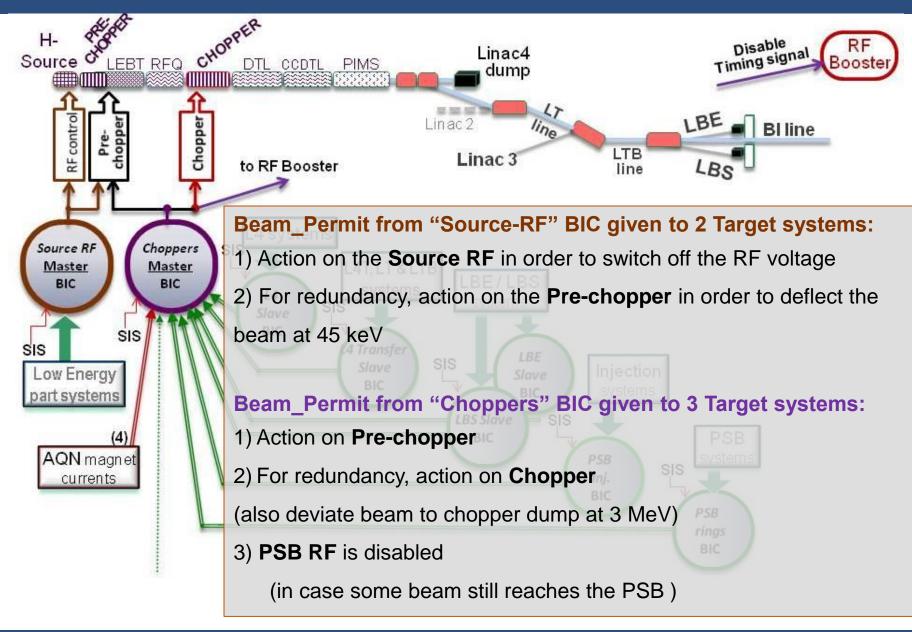
BIC

User Systems	Source-RF Master BIC	Choppers Master BIC	Linac4 Slave BIC	Transfer Line Slave BIC	LBE Slave BIC	LBS Slave BIC
Software Interlock	•	•	٠	•	•	•
Beam Loss Mon.			••	••	••	••
"Watch Dog" (i.e. BCT)	•		•	••		
Vacuum	•		•	•	•	•
Beam Stopper	••			•		
AQN (magnet's current)	•••	••••		•••		
Dump		•			•	•
RF			•			
Source HV	•					
Pre-Choppers	•					
Choppers	•					
Operator Switch	•					
Ext Conditions			•			

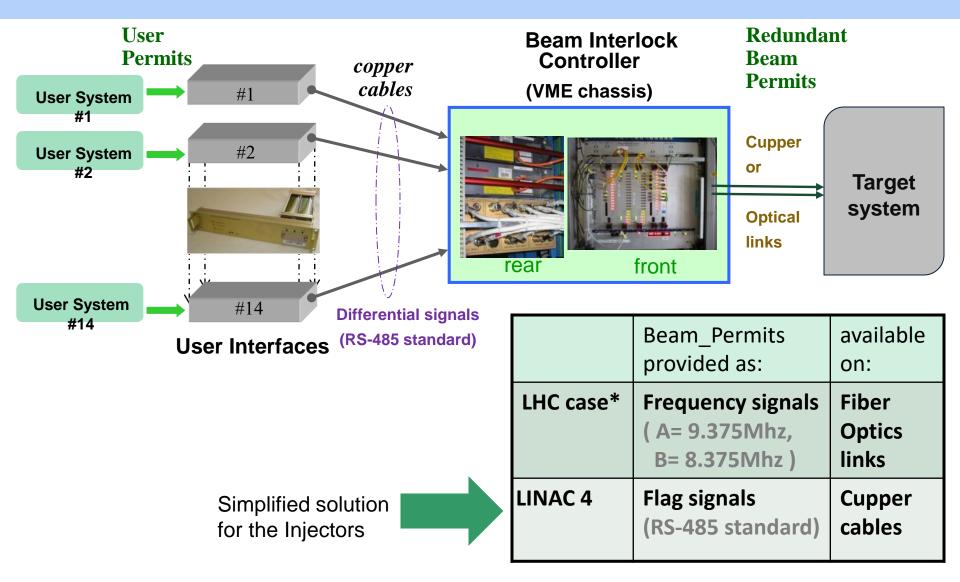
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Two different Beam_Permits given to 5 Target systems



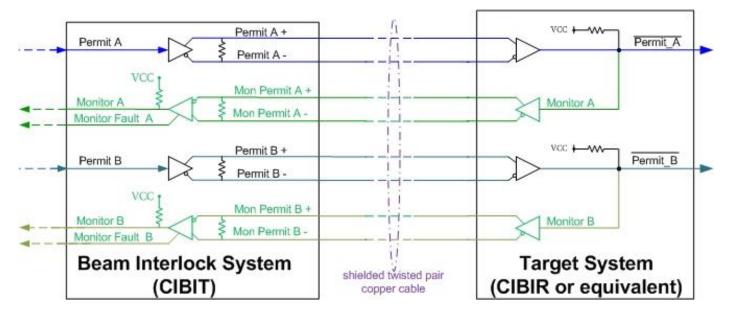
Reminder on BIS Interfaces



* and also for SPS, Extractions lines and Injection BIS

Beam_Permit signals: Differential Transmission details (1/2)

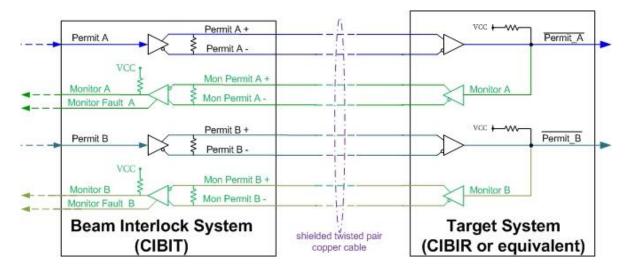
- ➤ Conversion Single-ended ⇒ Differential ⇒ Single-ended
- Redundant signal permit A & B
- Separation Critical/Monitoring signals



Thanks to Monitoring part, one can check that :

- Target system board is powered
- Cable is connected to the target system
- Beam_Permit signals are correctly transmitted (& read back)
- (if needed) Logic for the monitoring signal can be more elaborated

Beam_Permit signals: Differential Transmission details (2/2)



<u>VME card:</u>Single endedto differential conversion

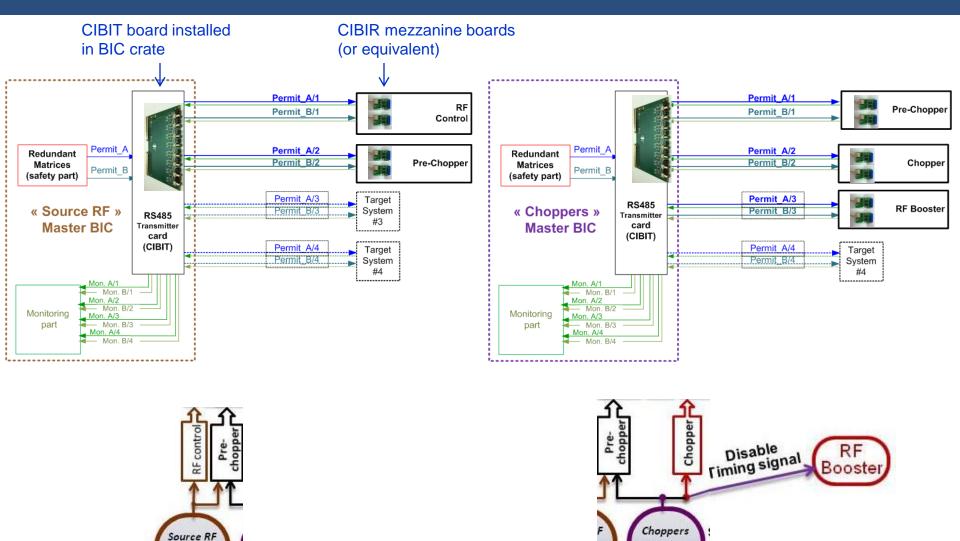
- 4 independent output channels
- Fail safe operation [on-board MAX3440 chip]



CIBIR <u>= Mezzanine cards (5 x 3 cm)</u>:

- Differential to single ended conversion
- Fail safe operation [MAX3440]
- 2 cards required for redundancy
- Provided by MPE/EP section.

Beam_Permit signals to L4 Target Systems



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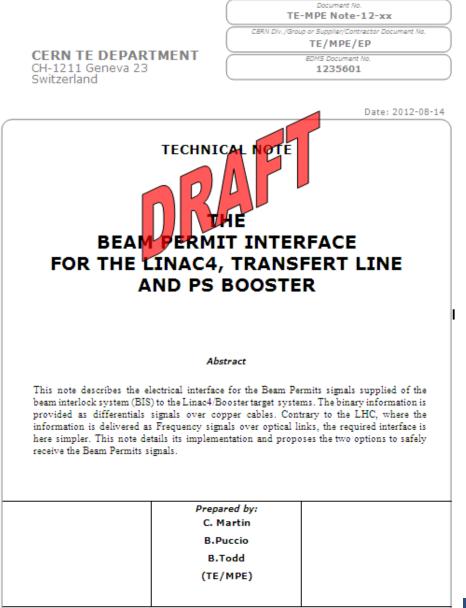
Master

BIC

Master

BIC

Beam_Permit signals: Technical Note



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Linac4 Master Plan

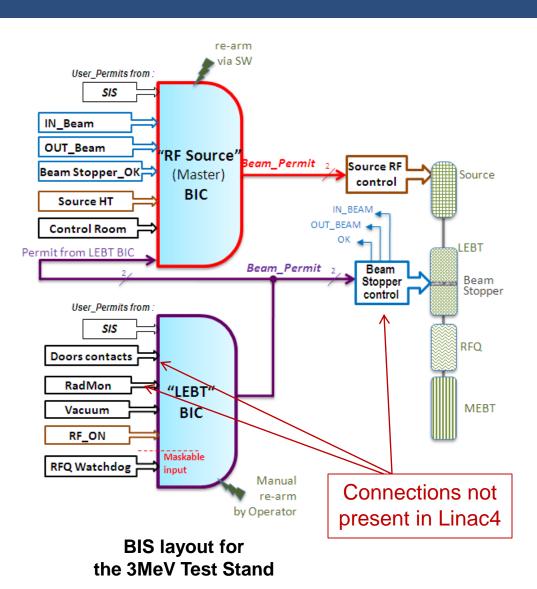
ID	Task Name		2013		2014		2015	2016	20
		JJASOND	J F M A M	JJASOI	NDJFMA	MJJASO	NDJFMAMJ	JASONDJFM	IAMJJASONDJ
1	Commissioning on Test Stand	P							
2	RFQ commissioning (test stand)								
3	H- source commissioning (test stand)								
4	3 MeV beam measurements (test stand)								
5	Commissioning Linac4							•	
6	Preparation and testing DTL/CCDTL								
7	H- source commissioning (Linac4)								
8	HW commissioning 3 MeV								
9	RFQ and chopper line installation in Linac4								
10	3 MeV beam measurements (Linac4)								
11	HW commissioning 50 MeV								
12	DTL1 installation and commissioning								
13	DTL2/3 installation and commissioning					1			
14	New ion source installation								
15	HW commissioning 160 MeV								
16	CCDTL installation and commissioning								
17	Installation and alignment transfer line								
18	PIMS installation and commissioning								
19	Linac4 ready for proton operation					o1/0	99		
20 21	Beam tests, reliability run								
	160 MeV beam tests								
22	Reliability run								
23	Linac4 ready for H- operation			PIMS		CCDTL	DTL chop	per line RFO	▲ 14/11
Version12/07/2012									
B.P.			160 MeV		104 MeV	86 m	50 MeV	3 MeV	21

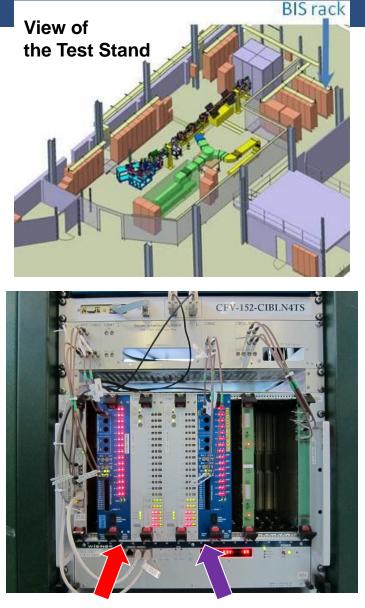
Status of the deployment

- BIS for 3MeV Test Stand fully installed and almost ready for commissioning
- For additional and internal tests: a dedicated BIC will be deployed in the Booster during next TS
- Material specific to Linac4 deployment (VME crates, BIC boards, CIBU units) ordered
- Monitoring Software and JAVA Application already available
 - Adaptation to fast cycling machine and specific screens are on going (will be tested with "Test BIC" and with the BICs in 3MeV Test Stand)
- Most* of the cables have been ordered
 - (*) in some cases,, the rack numbers have been unfortunately not provided by the User_System responsible.

B.P.

BIS in the 3MeV Test Stand

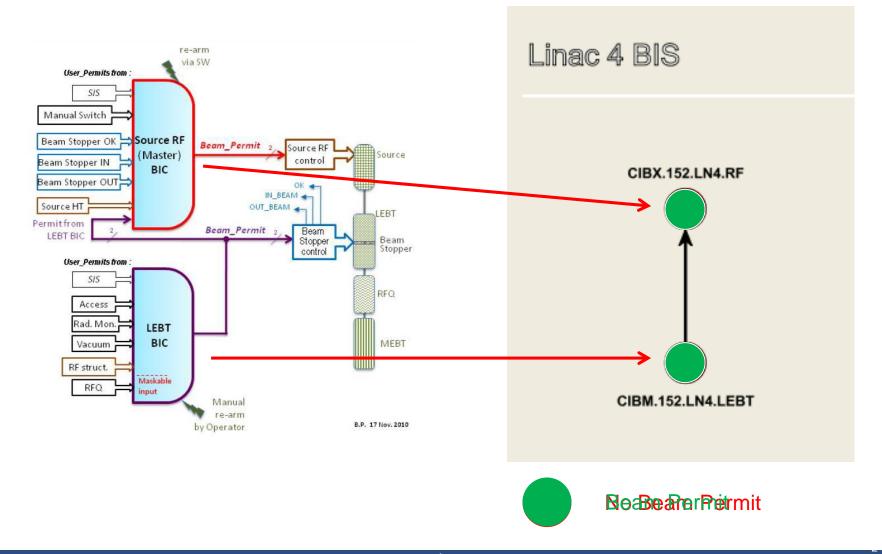




"RF Source" BIC

"LEBT" BIC

3MeV Test Stand: BIS Application



BIS Application: inputs view for "Source-RF" BIC



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BIS Application: the cycles view

- Same cycle user panel for one or multiple BICs,
- Information on masked/disabled/OK/NOK inputs,
- Future functionalities: filter, pause, and charts:
 - Signal evolution around injection and extraction
 - From W10 to AMC timing events!

	<u>\$</u>		Test				_ = ×
	Channel Mapping				17		
	CH0: CIBX.152.LN4.RF	UT CH1 : CIBM.152.LN4.LEBT OUT					
	Time		User cycle	CH0		CH1	
Last acquired user cycle :	25/07/12 10:37:54.463000	ISOGPS	0	NOT_OK	0	ок	0
	25/07/12 10:37:49.456000	AD	0	ок	0	ОК	0
	25/07/12 10:37:44.451000	TOF	0	ОК	0	ОК	0
	25/07/12 10:37:39.444000	TOF	0	<u>0K</u>	0	NOT_OK	0
	25/07/12 10:37:34.440000	AD	0	NOT_OK	0	OK	0
	25/07/12 10:37:29.437000	ISOGPS	0	<u>OK</u>	0	ОК	0
	25/07/12 10:37:24.432000	SFTPRO	0	ОК	0	NOT_OK	0
	25/07/12 10:37:19.428000	SFTPRO	0	NOT_OK	0	ОК	0
	25/07/12 10:37:14.423000	TOF	0	NOT_OK	0	NOT_OK	0
	25/07/12 10:37:09.419000	ISOGPS	0	NOT_OK	0	NOT_OK	0
	25/07/12 10:37:04.415000	ZERO	0	NOT_OK	0	NOT_OK	0
	25/07/12 10:36:59.409000	ISOGPS	0	NOT_OK	0	NOT_OK	0
	25/07/12 10:36:54.234000	AD	0	ок	0	NOT_OK	0
+	·			·			

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Outstanding issues?

- We have to clarify or to pay attention to the following points:
- Cables:
 - EN/EL has defined priorities for LS1 => missing cables will be not installed in 2013?
- BLM connections:
 - for the Linac4 and TL zones, 8 different signals are expected but only 2 can be provided by the BLM crate. Discussions with BLM team are on going to find a solution.
- AQN connections (i.e. Power Converters Interface for current measurements):
 - Proposal made in Nov'11 promising but still no Eng. specification
 - A prototype would have been helpful for the "Test BIC"
- Signals from Dumps:
 - Implementation details not known; EDMS specification expected
- Software Interlocks
 - Who will manage them? BE/OP?
- Machine Commissioning (see next slide)

Commissioning phases

- BIS layout has been defined for a complete Linac4 installation but:
 - For each step of the commissioning phases, a different part of the BIS shall be (safely) "masked"
 - The User_systems (like RF) have also to adapt their User_Permit signals to the commissioning progression.
 - Some elements will be only present during commissioning phases (like emittance measurement line); some others (BLM) will be ineffective at 3 MeV...

The above constraints have to be discussed and later on have to clearly described in a dedicated specification.

The end date of L4 commissioning is mid-2105. It will be followed a long reliability run in 2016. But Linac4 will also back-up solution in case of problems with Linac2 in the period between the two long shut-downs

=> impact on BIS has to be also studied.

Summing-up

- BIS solution (Hw & Sw) re-used for interlocking Linac4, Transfer Lines and Booster
- Simplified solution to interface the Beam_Permit signals
- BIS ready for 3MeV Test Stand commissioning
 - Additional "Test BIC" will be installed in Booster
- Possible issue for new cables
- Some points have to be clarified with some User_Systems (BLM, Power-Converters, Dump)
- New version of Engineering specification will go soon to "Check procedure"
- Commissioning steps and their impact on BIS have to be detailed in a dedicated document

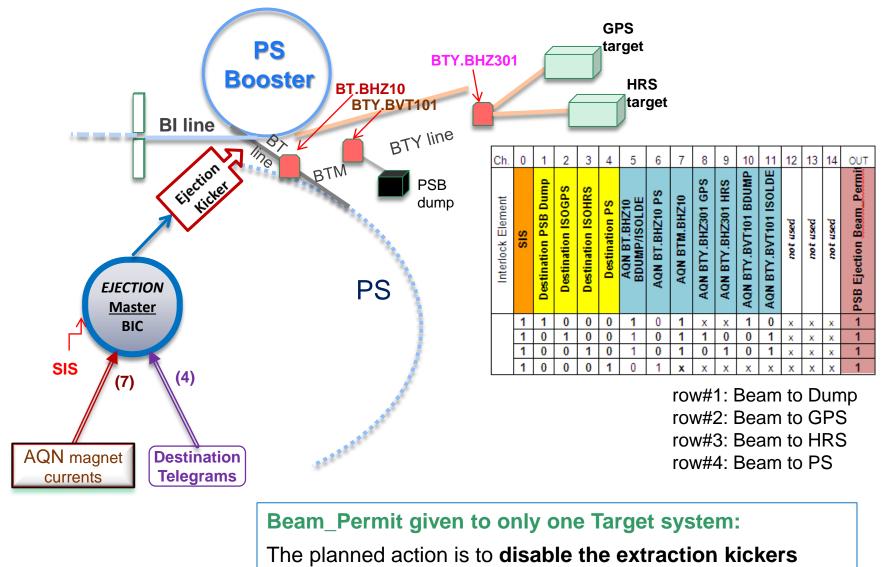


Fin

Thank you for your attention

Spare slides

Interlocking of the Booster Ejection line



drawing not to scale

User Systems connected to BIS (Booster part)

L4 systems

L4 Slave BIC L4T, LT & LTB

systems

4 Transfe Slave BIC LBE/LBS

LBS Slave BIC

LBE Slave BIC

Injection

PSB Inj. BIC

Choppers Master BIC

Source R Master BIC

Low Energy part systems

> AQN magne currents

User Systems	PSB Injection Slave BIC	PSB Slave BIC
Software Interlock	•	•
RF		•
Beam Loss Monitors		••••
"Watch Dog" (i.e. BCT)	••	
Vacuum		•
Beam Stopper	•	
BCM	● (H°/H⁻)	
Power Converter status	●● BI.SMV & BI.BSW	●●● MPS, BE.BSW, & BE.SMH
Pulse Magnets	● ● Distributor, BI.KSW	
Injection Foil Status	●	