

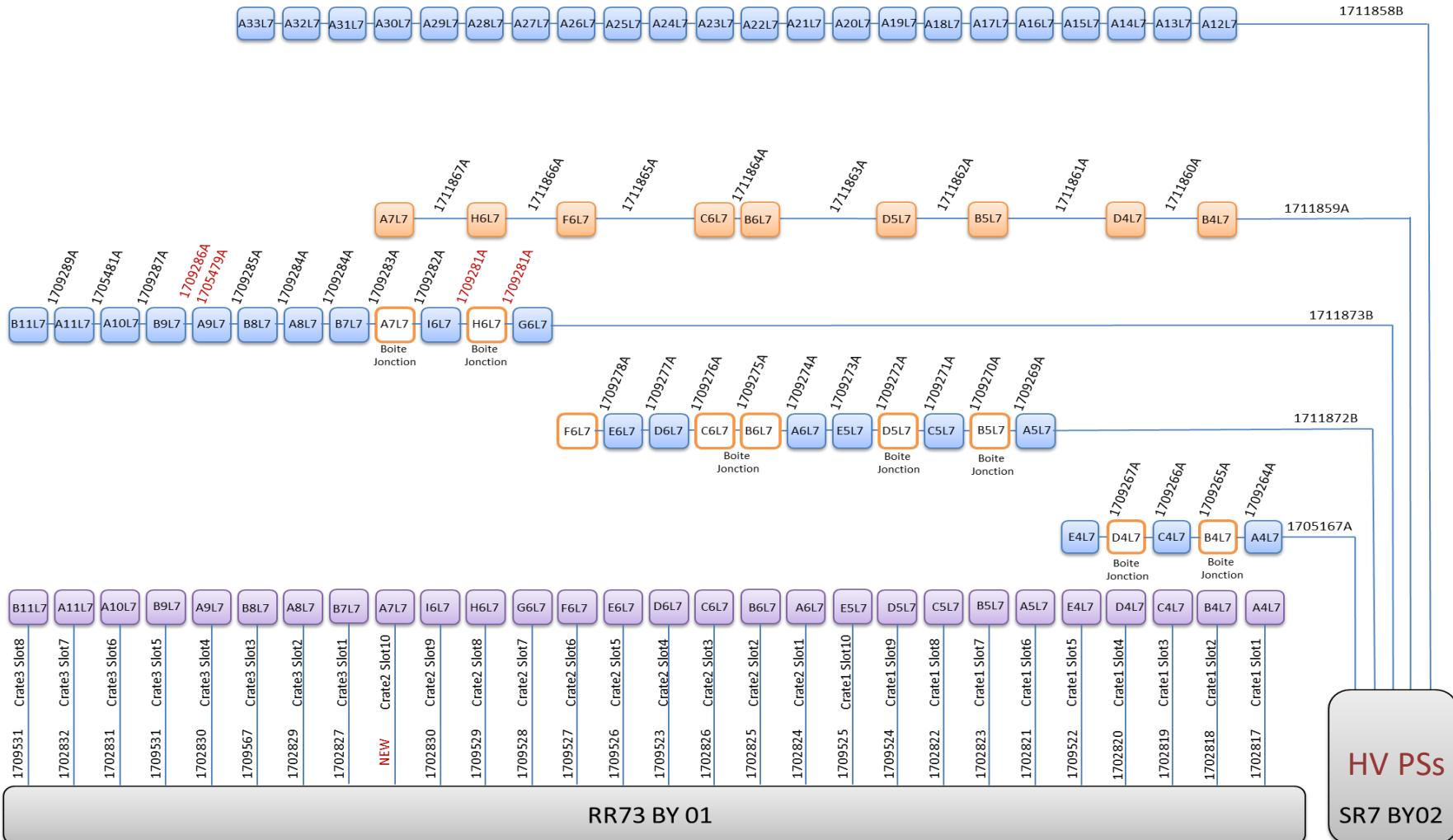
# Proposal for modification of HV interlock

Ewald Effinger for BL section

# Motivation for the changes in the BLM HV distribution

- HV cable cut in IP4 due to work on the surface (morning of 12.4.2011)
  - > Error wasn't detected, since check was not enabled!
  - > Problem detected with the sanity check (HV modulation)!
- HV Software interlock implemented (14.4.2011)
  - > reaction time of 10s on all racks
- Several beam dump trigger due to HV in 4.2011-7.2011
  - > Modification of the reaction time in center crates in IP3/7 to 60s (22.7.2011)
- SIS triggered after the BLM\_SR7\_C\_HV\_STATUS went to Fault [21/04/2012]
  - > Losses starting at 12:28 and continuously increasing until the dump
- SIS triggered after the BLM\_SR7\_C\_HV\_STATUS went to Fault [21/04/2012]
  - > Slow losses in IR7 on TCP.R7 for beam 2;
  - > Integration over long time provoked again a trip of HV on the BLM
  - > HV threshold on 10 BJBHT changed (TS#1)
- SIS triggered after the BLM\_SR7\_C\_HV\_STATUS went to Fault [19/06/2012]
  - > BLM high voltage through SIS interlock - high losses (but none reaching dump threshold) during MD
- SIS triggered after the BLM\_SR7\_C\_HV\_STATUS went to Fault [07/08/2012]
  - > SIS BLM HV-status interlock with high losses in P7 at end of squeeze
- SIS triggered after the BLM\_SR7\_C\_HV\_STATUS went to Fault [10/08/2012]
  - > The SB was dumped by the SIS HV BLM in IP7 due to the loss of CMS solenoid.
  - > Adding of suppressor diode and 10k resistor parallel to filter resistor! (TS#3)
- No trigger and HV warning detected in the modified HV boxes since change!
- Increase from 100kW to 500kW of continuous losses in IR 7
- Decrease quantity of warnings due to HV levels

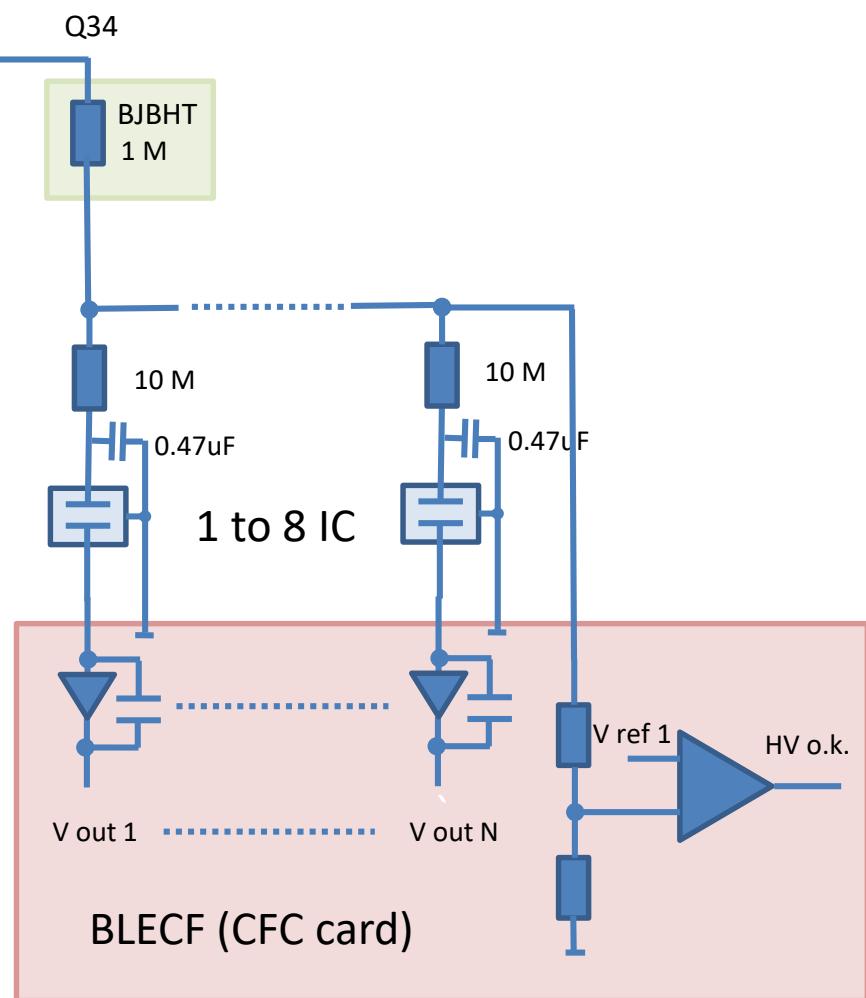
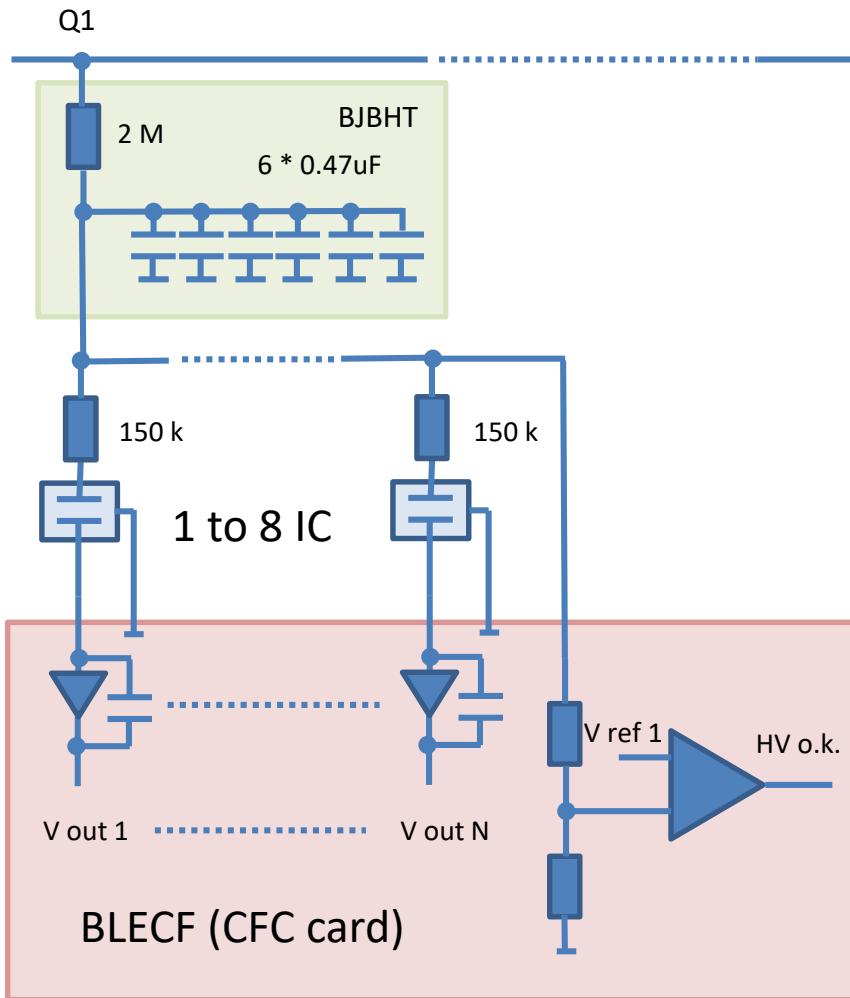
# HV distribution (e.g. IP7 left)



# HV Supply in the surface building SR

- Two Henzinger HV supplies per IP
  - Specification V out = 0-3000V, I max = 20mA
  - One HV supply is operational ( $V_{\text{Nominal 1}} = 1450V$ )
  - Second as backup HV ( $V_{\text{Nominal 2}} = 1440V$ )
  - Maximal total current:  $I_{\text{HV max}} = 40\text{mA per IP} \Rightarrow 740\text{Gy/s}$
- Different HV nets are in place
  - Separation of low and high loss areas
  - Diodes used to separated HV nets (cross talk)
- HV is controlled by BLECS (combiner card)
  - Voltage and Current feedback of both supplies are monitored and logged!

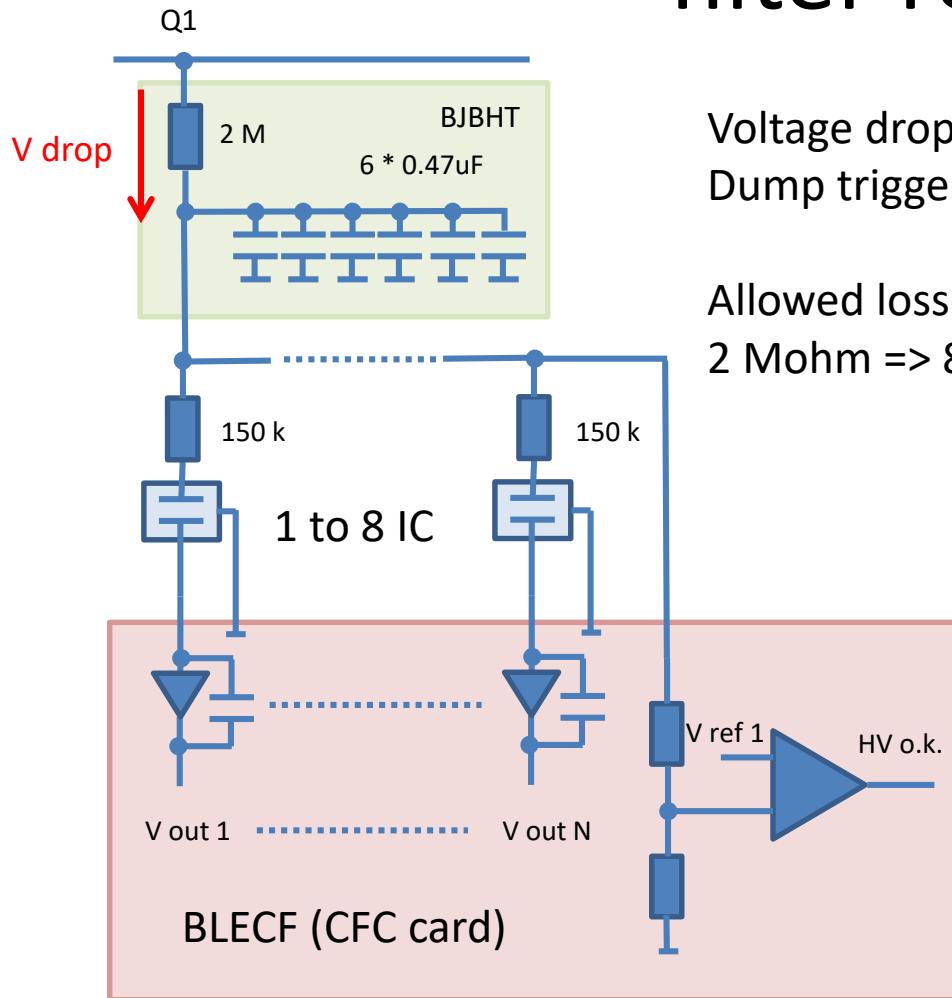
# Two different high voltage schemes



HV scheme in high radiation area (e.g. collimation)

Standard HV scheme (e.g. arc)

# HV SIS trigger due to HV drop on the filter resistor



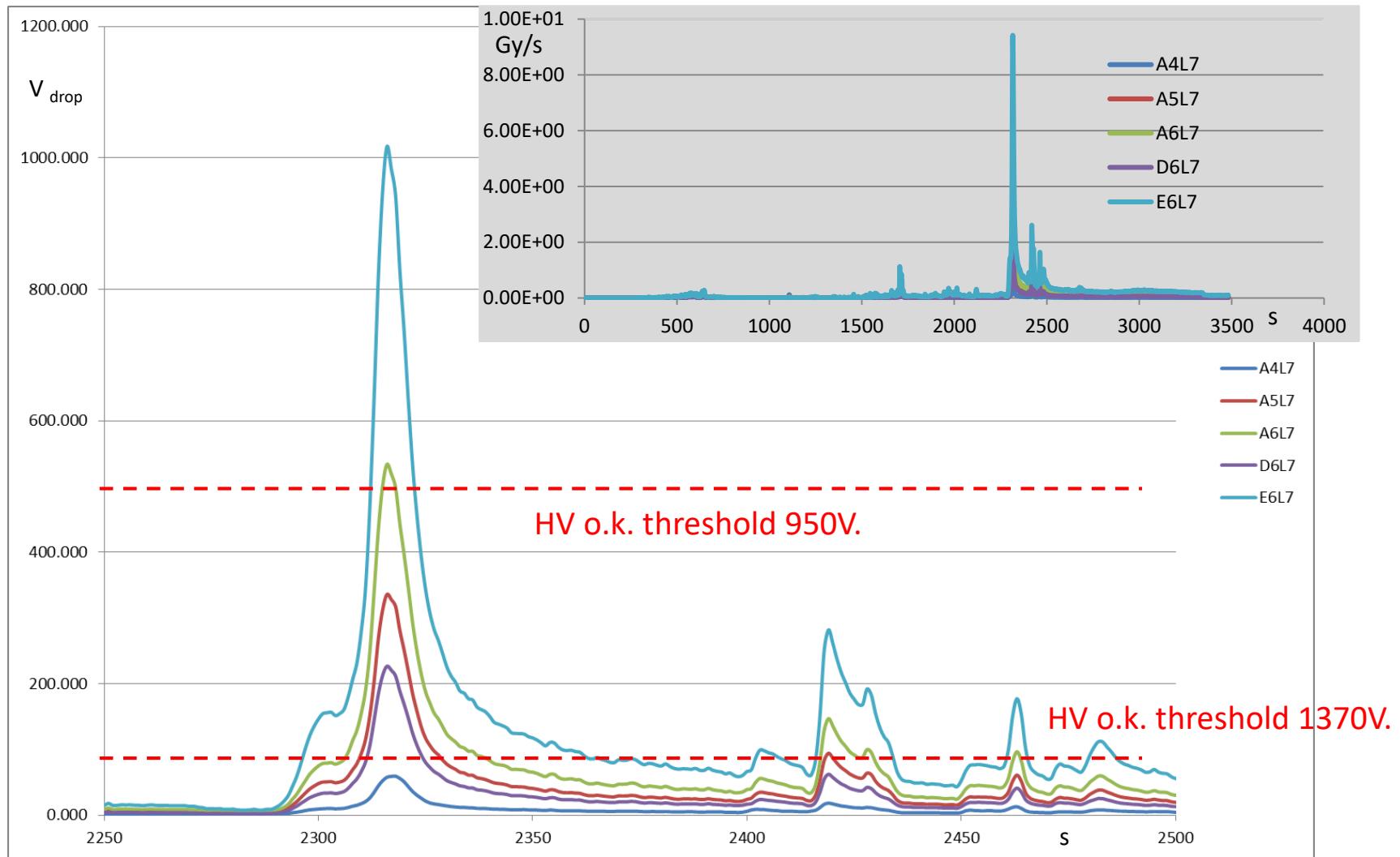
Voltage drop due to current in filter resistor  
Dump triggered if  $V \text{ drop} \geq 80\text{V}$  for 10s or 60s

Allowed losses without dump trigger ! HV Th = 1370V  
 $2 \text{ Mohm} \Rightarrow 80\text{V}/2\text{M} = 40\mu\text{A} \Rightarrow 0.74\text{Gy/s}$

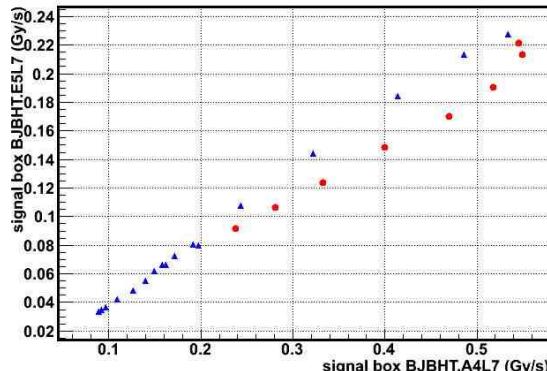
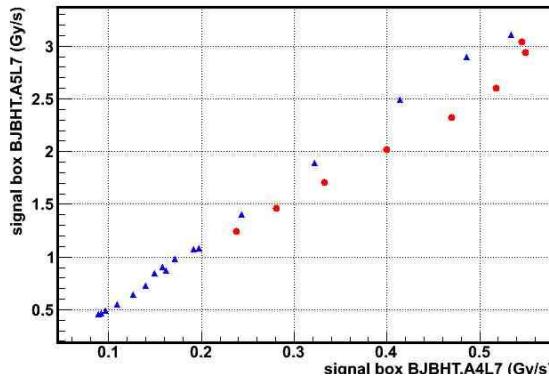
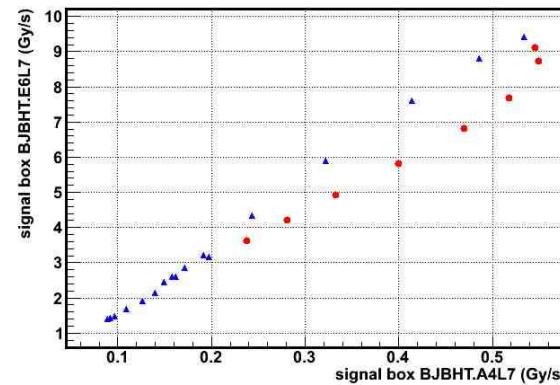
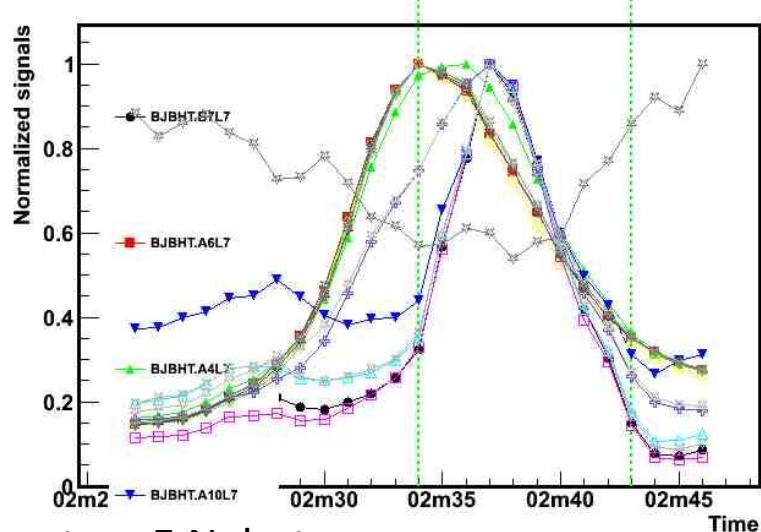
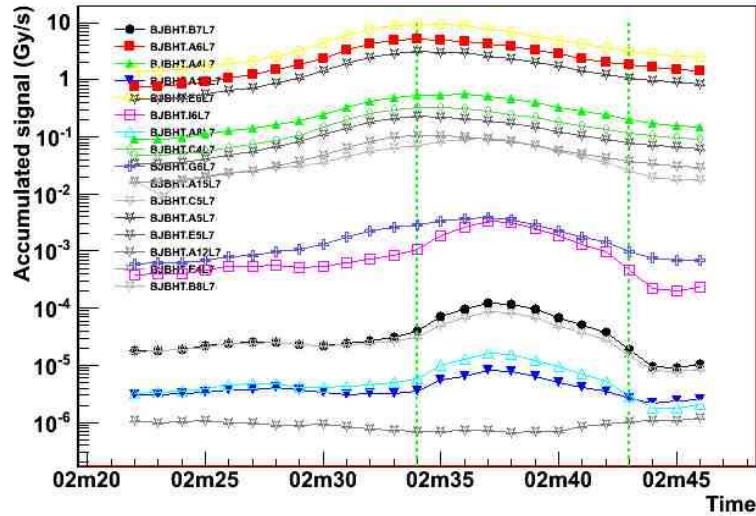
Maximum given due to design (theoretical, for 8 ICs!)  
 $1450/(2\text{M}+18.75\text{k}) = 0.72\text{mA} \Rightarrow 13.2\text{Gy/s}$   
 $\Rightarrow V \text{ drop} = 1436\text{V}$

HV scheme in high radiation area (e.g. collimation)

# Examples of the voltage drops during losses in IP7



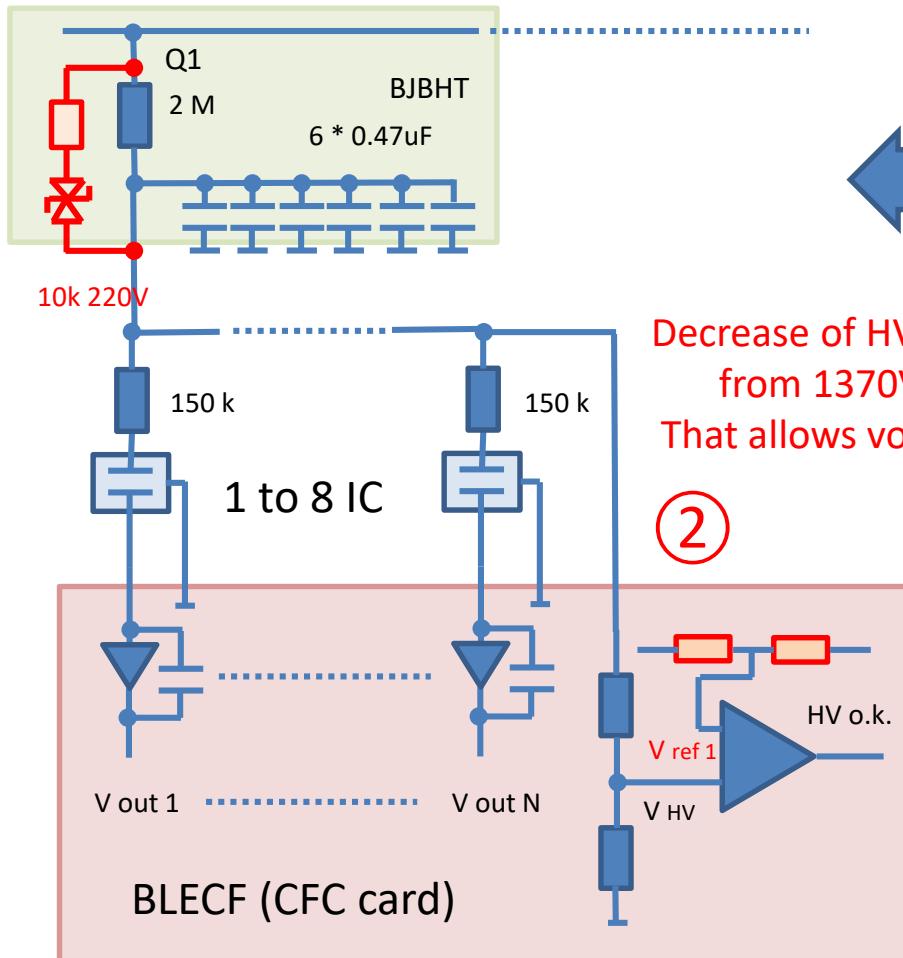
# Behaviour of boxes with HV drop



Courtesy E.Nebot

# Changes done since the HV SIS beam dump enabled in April 2011

- ③ Limitation of voltage drops to 220V by a suppressor diode, with not disturbing the system (e.g. modulation). (TS#3)



Increase of time from 10s to 60 in the center creates (7/2011)

Including Status bits

# Situation with the 3 changes in dedicated high radiation areas (filter in the BJBHT, collimation)

Change 1: (delay time increase)

$$t_{\text{trigger}} = 10\text{s} \text{ and } t_{\text{trigger}} = 60\text{s}$$

Change 2: (threshold change)

$$2 \text{ Mohm} \Rightarrow 80\text{V}/2\text{M} = 40\mu\text{A} \Rightarrow 0.74\text{Gy/s} ; \text{HV}_{\text{Thres.}} = 1370\text{V}$$

$$\text{Allowed losses without dump trigger ! HV}_{\text{Thres.}} = 950\text{V}$$

$$2 \text{ Mohm} \Rightarrow 500\text{V}/2\text{M} = 250\mu\text{A} \Rightarrow 4.62\text{Gy/s}$$

Change 3: (limitation of voltage drop)

$$\text{Allowed losses without dump trigger ! HV Th} = 950\text{V}$$

$$V_{10k} = 1450\text{V} - 220\text{V} - 950\text{V} = 280\text{V}$$

$$I_{\text{trigger}} = 280\text{V}/10\text{k} = 28\text{mA} \Rightarrow 518\text{Gy/s}$$

$$I_{\text{max}} = (1470\text{V}-220\text{V})/(10\text{k} + 18.75\text{k}) = 42\text{mA} \Rightarrow 792\text{Gy/s}$$

⇒ Due to the three changes a local loss of 518Gy/s per BJBHT for a time of 60s is needed to trigger a beam dump! (up 8 LHCBLMs connected to 1 BJBHT)

Values of maximal loss location:

Threshold@4TeV for 500kW losses = 53-143Gy/s ≤ 518Gy/s

Measured Losses @4TeV at 200kW and scaled to 500kW = 36Gy/s ≤ 518Gy/s

# Situation with the 2 changes in remaining areas (filter in the BJBHT, e.g. arc)

Change 1: (delay time increase)

$$t_{\text{trigger}} = 10\text{s}$$

Change 2: (threshold change)

$$1 \text{ Mohm} \Rightarrow 80\text{V}/1\text{M} = 80\mu\text{A} \Rightarrow 1.48\text{Gy/s}; \text{HV}_{\text{Thres.}} = 1370\text{V}$$

$$\text{Allowed losses without dump trigger! } \text{HV}_{\text{Thres.}} = 950\text{V}$$

$$1 \text{ Mohm} \Rightarrow 500\text{V}/1\text{M} = 500\mu\text{A} \Rightarrow 9.26\text{Gy/s}$$

$$I_{\text{max}} = 1450\text{V} / (1\text{M} + 1.25\text{M}) = 644\mu\text{A} \Rightarrow 11.93\text{Gy/s}$$

=> Due to the two changes a local (8 LHCBLMs connected to 1 BJBHT) loss of 9.26Gy/s for a time of 10s is needed to trigger a beam dump!

Threshold in arc:  $10\text{mGy/s} \leq 9.26 \text{ Gy/s}$

Threshold in LSS (not in collimation) =  $500\text{mGy/s} \leq 9.26 \text{ Gy/s}$

# Conclusion for HV distribution of the BLM System

- a) SIS Dump request
  - In IP 1,2,4,5,6,8 = 10s (under HV o.k level)
  - In IP 3 and 7 = 60s (under HV o.k level)
- b) Change of HV o.k. Threshold
  - Changes of divider on **all BLECF card ( $\sim 750$  cards in the tunnel)** to have identic BLECFs
  - Not sufficient for constant losses of 36Gy/s ( $\Rightarrow 500\text{kW}$ ), **c) is needed!**
- c) Additional 220V suppressor diode and **20k** resistor
  - Modification **dedicated (HV box with filer capacitors)** area with high loss needed
  - **No influence** on modulation test
  - Voltage drop decreased
  - **Threshold@4TeV for 500kW losses = 53-143Gy/s  $\leq 259\text{Gy/s}$**
  - **Measured Losses @4TeV at 200kW, scaled to 500kW = 36Gy/s  $\leq 259\text{Gy/s}$**
  - By using a 20k power dissipation is decreased in the diode and resistor
  - No changes in FPGA firmware in the BLECS card
- d) Failure mode for suppressor diode
  - Diode becomes high- ohmic  $\rightarrow$  Situation like with a & b (dump trigger can occur)
  - Diode has short-circuit  $\rightarrow$  Detected by modulation test!
  - **Protection of the machine is always ensured!**
  - Irradiation test for suppressor diode up to 1MGray foreseen

# Additional Slides





# Calculation for 220V suppressor and 20k

Change 3: 220V suppressor diode and 20k resistor

Allowed losses without dump trigger ! HV Th = 950V

$$V_{20k} = 1450V - 220V - 950V = 280V$$

$$I_{trigger} = 280V/20k = 14mA \Rightarrow 259Gy/s$$

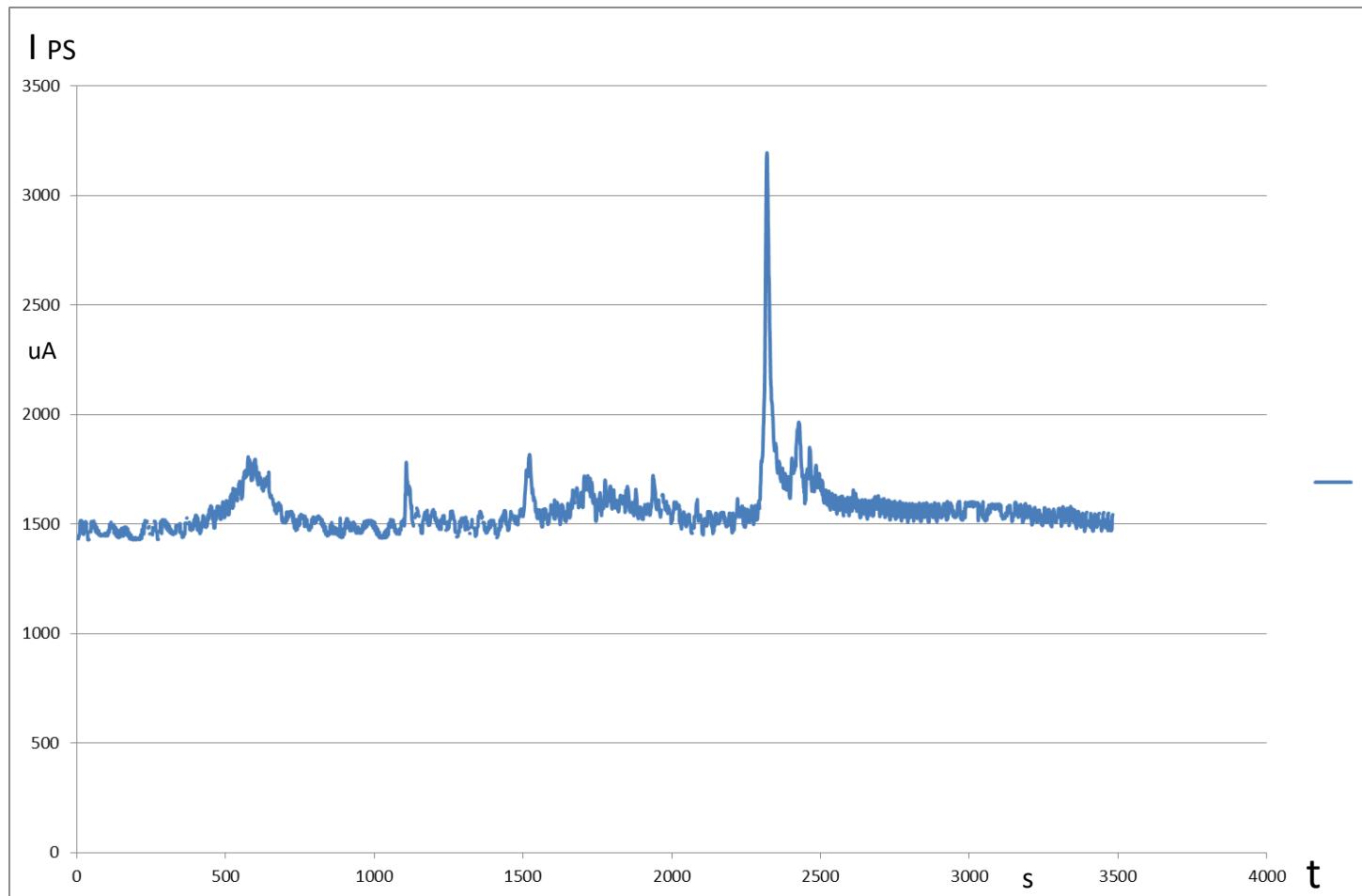
$$I_{max} = (1470V-220V)/(20k + 18.75k) = 32.2mA \Rightarrow 597Gy/s$$

Due to the three changes a local loss of 259Gy/s for a time of 60s is needed to trigger a beam dump! (up 8 LHCBLMs connected to 1 BJBHT)

Threshold@4TeV for 500kW losses = 53-143Gy/s  $\leq$  259Gy/s

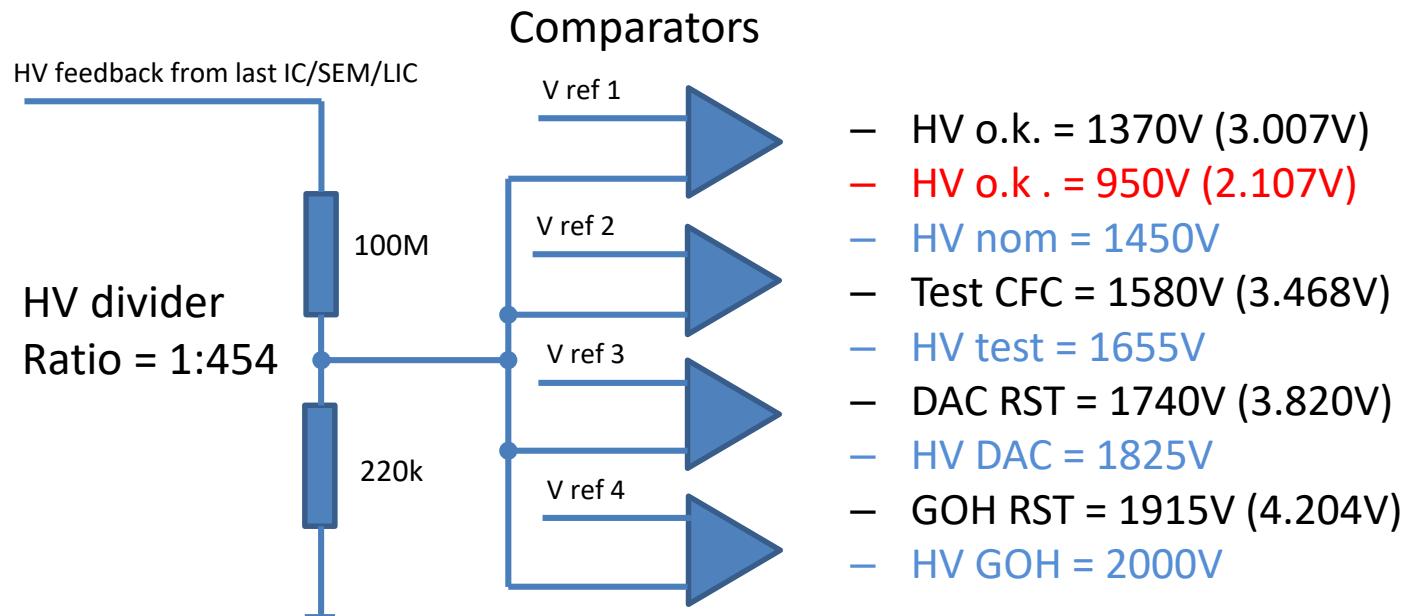
Measured Losses @4TeV at 200kW and scaled to 500kW = 36Gy/s  $\leq$  259Gy/s

# Current in the high voltage supply in point 7



# HV monitoring with comparator in the BLECF (CFC card)

- Nominal HV  $\approx 1450\text{V}$  (IP depending see table below)
- Average margin of  $= 80\text{V}$  (Divider tolerance depending see drawing below)



# HV levels per IP

Command	HV nominal		TEST_CFC		DAC_RST		GOH_RST				FPGA_RST				HV Startlevel		Results from Scan
	240s		240s		240s		240s				20min (15min + 240s to enter in GOH_RST)				for Scan		
Value	[V]	bits	[V]	bits	[V]	bits	[V] min	bits	[V] nom	bits	[V] min	bits	[V]	bits	[V]	bits	
IP1	1445	31566		0	1806.7	39467		0		0		0		0	1200	26214	
IP2	1454	31763	1638	35782	1800	39321	1984	43340	2000	43690	1984	43340	2000	43690	1200	26214	16.09.09
IP3	1453.63	31755	1639.67	35819	1807.63	39488	1972.61	43092	2000	43690	1972.61	43092	2000	43690	1200	26214	22.09.09
IP4	1431.6	31273	1615.56	35292	1784.52	38983	1990.25	43477	2000	43690	1990.25	43477	2000	43690	1200	26214	02.10.09
IP5	1456.77	31823	1642.3	35876	1807.7	39489	1977.35	43195	2000	43690	1977.35	43195	2000	43690	1200	26214	31.08.09
IP6	1459.94	31892	1648.7	36016	1816.07	39672	1984.31	43347	2000	43690	1984.31	43347	2000	43690	1200	26214	02.10.09
IP7	1440.27	31463	1624.2	35481	1789.16	39084	1974.04	43123	2000	43690	1974.04	43123	2000	43690	1200	26214	12.10.09
IP8	1441.38	31487	1621.53	35422	1748.02	38185	1964.37	42912	2000	43690	1964.37	42912	2000	43690	1200	26214	02.10.09

# Improvement of HV levels with new HV divider (foreseen to exchange all HV dividers while LS1)

