

Figure 1: Front-end Motherboard Block Diagram showing the location of the COLDATA ASIC

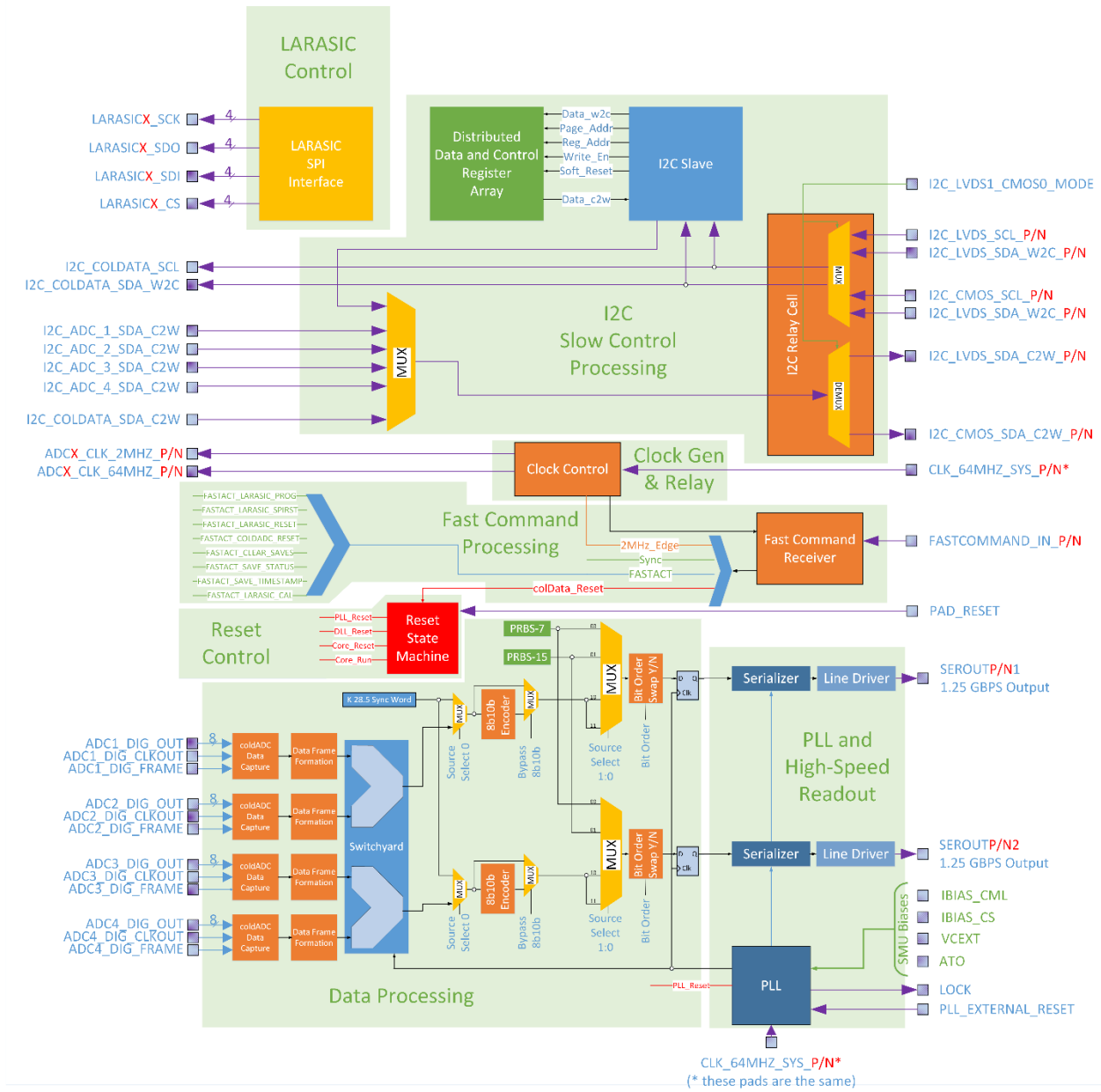


Figure 2: The COLDATA Block Diagram highlighting the major processing elements of the ASIC