

TWEPP 2022 Topical Workshop on Electronics for Particle Physics

Wednesday 21 September 2022

Programmable Logic, Design Tools and Methods (09:00 - 10:00)

-Conveners: Salvatore Danzeca; Johan Alme

time	[id] title	presenter
09:00	[67] DART28-FPGA implementation study for future high-speed links	WANOTAYAROJ, Chaowaroj
09:20	[181] Developments in hls4ml and its application	MITREVSKI, Jovan
09:40	[12] Integration of FPGA RDMA into the ATLAS Readout with FELIX in High Luminosity LHC	VASILE, Matei

Programmable Logic, Design Tools and Methods (11:20 - 12:20)

-Conveners: Salvatore Danzeca; Johan Alme

time	[id] title	presenter
11:20	[107] Evaluation of GBT-FPGA for Timing and Fast Control in CBM experiment	SIDORENKO, Vladimir
11:40	[155] An FPGA-based readout chip emulator for the CMS ETL detector upgrade	WU, Jinyuan
12:00	[46] Embedded Neural Networks on FPGAs for Real-Time Computation of the Energy Deposited in the ATLAS Liquid Argon Calorimeter	AAD, Georges