

TWEPP 2022 Topical Workshop on Electronics for Particle Physics

Thursday, 22 September 2022

Thursday posters session (16:40 - 18:00)

[id] title	presenter	board
[85] STRV – A radiation hard RISC-V microprocessor for high-energy physics application	WALSEMANN, Alexander	
[105] SEU injection framework for radiation-tolerant ASICs, a formal verification approach	LUPI, Matteo	
[114] Future developments of radiation tolerant sensors based on the MALTA architecture	Mr DOBRIJEVIC, Dominik	
[131] Design and readout architecture of a monolithic binary active pixel sensor in TPSCo 65nm CMOS imaging technology	CECCONI, Leonardo	
[143] Second generation Monitoring of Pixel System (MOPS) chip for the Detector Control System (DCS) of the ATLAS ITk Pixel Detector	AHMAD, Rizwan	
[158] Maintaining data integrity in a cryogenic SoC ASIC driving 25 m cable at 160K and 87K with gigabit data rates	GUPTA, Aseem	
[180] Analog performance of the CROC-V1 pixel readout chip for the CMS Phase-2 Tracker Upgrade	PAPADOPOULOS, Alkiviadis	
[192] BigRock, a Fast Timing Front End for Future Pixels in a 28 nm CMOS Process	ZHANG, Zhicai	
[23] A 14 Gbps VCSEL Driving ASIC in 55 nm for NICA Multi-Purpose Detector Project	ZHAO, Cong GUO, Di	
[61] A radiation hard bandgap voltage reference for the ARCADIA project	TRAVERSI, Gianluca	
[90] A 65nm Dual Mode 1.25/7 GHz PLL with CML Line Driver for ALICE ITS3 and EIC Applications	MATHEW, Soniya SEDGWICK, Iain	
[113] Fast prototyping of Front End ASICs and validation of associated DAQ with a Cadence Protium platform.	Mr BERTOLONE, Grégory	
[154] A four-channel front-end readout ASIC for high hit rate gas detector	QIAO, Yuxin GAO, Chaosong	
[160] MUX64, an analogue 64-to-1 multiplexer ASIC for the ATLAS High Granularity Timing Detector	Mr XU, Zifeng	
[175] RISC-V processor evaluation with Cadence Protium platform	MOREL, Frederic	
[190] Fast Tracking Read-Out Electronics for ATLAS sMDT Detectors in 65nm CMOS	SHAH, Syed Adeel Ali	
[77] Thermal Annealing in Silicon Photonics Ring Modulators	LALOVIC, Milana	
[174] The Optosystem: validation and testing of the high-speed optical-to-electrical conversion system for the readout of the ATLAS ITk Pixel upgrade	Mr DAL SANTO, Daniele MUNOZ SANCHEZ, Francisca	
[19] bPOL48V, rPOL48V and linPOL48V: 48V-input-rated rad-hard POL converters for HEP experiments	ANTOSZCZUK, Pablo Daniel	
[133] Modern high-availability multi-stage power distribution system for the CMS phase-2 upgrade.	STACHON, Krzysztof	

[124] Influence of High-Frequency Magnetic Fields on Noise Behavior of CMS 2S Module Prototypes	ROEWERT, Nicolas Maximilian	
[34] System tests of the ATLAS ITk planar and 3D pixel modules	MUELLER, Roman	
[98] Qualification tests of the PASTTREC readout ASIC for straw tube detectors	MOLENDIA, Aleksandra	
[65] Pre-Production Testing of the HCCStar at Penn for the ATLAS ITk Detector	MCGOVERN, Bobby	
[137] LTDB, the Lar Trigger Digitizer Board: from design to mass production for ATLAS Liquid Argon Calorimeters Phase-1 Upgrade	GRABAS, Aude Marie	
[41] Radiation tolerance studies of the HV-Mux GaN FETs for the HL-LHC ATLAS ITk Strip Detector	GUTIERREZ ZAGAZETA, Luis Felipe	
[74] Testing of the HCC and AMAC functionality and radiation tolerance for the HL-LHC ATLAS ITk Strip Detector	GUTIERREZ ZAGAZETA, Luis Felipe	
[161] Radiation studies performed on the High Luminosity ATLAS TileCal link Daughterboard	VALDES SANTURIO, Eduardo	
[50] A Dedicated and Versatile System for Testing the Radiation Hardness of Various Integrated Circuits	Dr PLACINTA, Vlad-Mihai	
[38] System Integration of ATLAS ITK Pixel DCS ASICs	QAMESH, Ahmed	
[187] Exploring FPGA in-storage computing for Supernova Burst detection in LArTPCs	MITREVSKI, Jovan	
[92] The LHCb Vertex Locator fast calibration	FERNANDEZ PRIETO, Antonio	
[8] FELIX: the new ATLAS readout system from Run 3 to High Luminosity LHC	WENSING, Marius	
[11] ATLAS LAr Calorimeter Commissioning for LHC Run-3	ZANG, Jiaqi	
[52] The new readout system of the ALICE Zero Degree Calorimeters in LHC Run 3	MAZZA, Gianni	
[58] Thermal and Humidity Monitoring in "Cold" Detector Control and Safety System	KAPIC, Amar	
[66] Timing and Synchronization of the DUNE Neutrino Experiment	CUSSANS, David	
[128] CMS tracker phase-2 upgrade module noise and mitigation	ZOGRAFOS, Angelos	
[194] A Readout System for Monolithic Pixel Sensor prototypes towards the ITS3 upgrade of the ALICE Inner Tracking System	SARRITZU, Valerio	
[86] High Voltage consolidation of the temperature measurement channels of the LHC superconducting current leads	TRIKOUPIS, Nikolaos	
[125] Readout Electronics for High Intensity Range Particle Beam Monitoring	THALMEIER, Richard	
[182] A high performance and low-cost multipurpose Cosmic Ray Detector	CAZAR RAMIREZ, Dennis	
[191] LAPPD operation using FASTIC and PETSYS ASICs	SELJAK, Andrej	
[15] Status of the Level-0 ATLAS Barrel Muon Trigger for High-Luminosity LHC	MORODEI, Federico	
[81] Data-Driven Design of the Belle II Track Segment Finder	NEU, Marc	
[111] The NA62 liquid krypton electromagnetic calorimeter fast readout implementation, commissioning and data taking performance	AMMENDOLA, Roberto	
[71] Characterisation of the first digital modules with RD53B-CMS readout chips for the Phase-2 Upgrade of the CMS Inner Tracker	MISSIROLI, Marino	
[75] RAFAEL : A Clock and Data Fan-Out ASIC for CMS HL-LHC Upgrades	GUILLOUX, Fabrice	