Optimisation of electron spin qubits in electrically driven multi-donor quantum dots A. Sarkar^{*,1,2}, J. Hochstetter¹, A. Kha¹, X. Hu³, M.Y. Simmons⁴, R. Rahman¹, D. Culcer^{*,1,2}

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Fig.1: a) STM imaging of the qubit, b) in-plane gate control

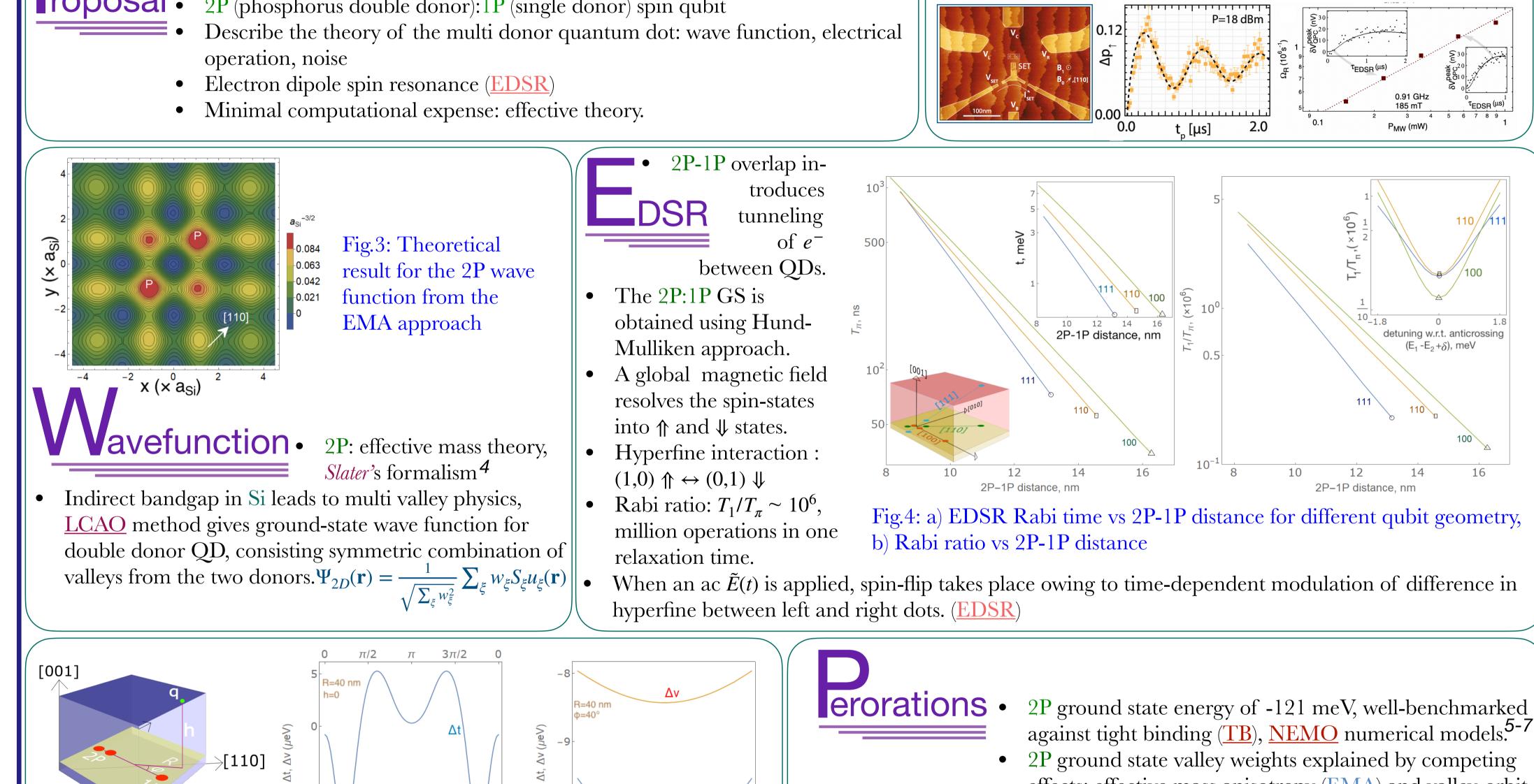
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otivation • 2P:1P qubit with Rabi freq. 1.2 MHz, $T_2^* = 295 \, ns, \, 0.8 \, ns$ two-qubit gate ⁷

- Donors offer atomic-scale precision placement, orientation specified properties (e.g. [110] qubit axis sees suppression of exchange oscillation)
- Scalable semiconductor multi-qubit chips possible.²
- Hyperfine interaction between the qubit electron spin and Pdonor nucleus provides strong spin-orbit coupling (advantage over micromagnet engineering): $H_{hf} = \mathscr{A}_0 \sum \delta(\mathbf{r} - \mathbf{R}_i) \vec{I}_i \cdot \vec{S}^3$

Fig.2: a) STM imaging of 2P-1P qubit, b) Rabi oscillation, c) hyperfine mediated EDSR



2P (phosphorus double donor):1P (single donor) spin qubit

- 2P ground state valley weights explained by competing effects: effective mass anisotropy (EMA) and valley-orbit coupling (VOC).
- Complete in-plane gate control is possible, under a global out-of-plane magnetic field B_{z} an in-plane ac electric field $\tilde{E}(t)$ induces qubit spin-flip.
- The spin-flip **EDSR** mechanism relies on contact hyperfine coming from the donor nuclear spins.
- $T_{\pi} \sim 50 \, ns$, fast qubit rotation; $T_1/T_{\pi} \sim 10^6$, excellent Rabi ratio.
- Qubit geometry determines the best EDSR for when qubit axis || [111] and best Rabi ratio for when qubit axis \parallel [100]. Noise simulation produces the detrimental defect position to be $\theta = 40^{\circ}$, h=0, R=30 nm.
- Such charge noise lead to decoherence of the qubit state $\uparrow \rightarrow \downarrow$.

between the 2P and 1P dots as well as the self-energies of the QDs.

Time of decoherence (T_2^*) from charge noise can be modelled as Random

3π/4

charge defect angle ϕ

Charge defect in the vicinity of the physical qubit can modify the tunneling

Δv

7π/4

 $5\pi/4$

c) noise effect vs. height of charge defect.

-10

Fig.5: a) charge defect schematic b) angular noise effect of charge noise

-50

-25

charge defect height h, nm

25

50

Telegraph noise:
$$\left| \frac{1}{T_2^*} \right|_{PTN} = \frac{32\zeta^2 \tau k_{ge}^4}{\hbar^2 \delta \varepsilon^4} \left(\frac{4t\Delta t}{\delta \varepsilon^2} + \frac{\epsilon \Delta v}{\delta \varepsilon^2} \right)^2$$
,

-10

>[110]

[100]

Ί

- Noise is the most detrimental at particular angular orientation, and is minimal at the qubit plane, supporting the in-plane gated control.
- Charge noise leads to EDSR single qubit gate error of 2%
- Efficient two qubit exchange gate of 3 GHz.

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13.

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