

CMS ME1/1 Electronics Upgrade

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Summary

The project involves the design and installation of new electronics for the 72 inner ME1/1 Cathode Strip Chambers at CMS. This upgrade serves to eliminate the 3-fold ganging of cathode strips in this region and to prevent deadtime contributions at future high luminosities. We present the status of the Digital Cathode Front-End Boards (DCFEB), Data Acquisition Motherboards (DMB), Trigger Motherboards (TMB), and Low-Voltage Distribution Boards (LVDB) and outline the testing and integration plans.

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Session Classification: Calorimeters and Muon upgrade for phase