

System-on-Chip (SoC) Interest Group Meeting

- SoM survey
- Useful pointers

SoM Survey

- **Scope of the survey:**

Generally, by SoC we understand an integrated chip that has an FPGA and a processor part, and by SoM we understand a SoC on a mezzanine card.

If you have a wider definition of SoM, please still reply to the survey - we are interested to hear about it.

- **Aim of the survey:**

Establish an overview of what is being used, and of what the different projects consider to be the pros/cons of the different solutions.

- **Return so far:**

Accelerator: 6, ATLAS: 3, CMS: 7, ATLAS&CMS: 1, Other: 1

Big thanks to all those who have replied!

In order to get more replies we extend the deadline to 13 May 2022.

Useful Pointers

- system-on-chip@cern.ch
- <https://mattermost.cern.ch/systemonchip>
- <https://systemonchip.web.cern.ch>
- <https://twiki.cern.ch/twiki/bin/view/SystemOnChip>
- <https://gitlab.cern.ch/soc>