The 31st International Workshop on Vertex Detectors

Tuesday 25 October 2022

produced in 65 nm TPSCo CMOS process

monolithic: (1) (13:35 - 15:35)

-Conveners: .Yoshiyuki Onuki

time	[id] title	presenter
13:35	[26] [D01] MuPix: an HV-MAPS for the Mu3e experiment	VIGANI, Luigi
	[38] [D02] Design and performance of the Monopix2 reticle-scale DMAPS with a column-drain read-out architecture	CAICEDO SIERRA, Ivan Dario
14:35	[39] [D03] Design and performance of RD50 DMAPS sensors for future colliders	SONNEVELD, Jory
15:05	[24] [D04] First Results of ATLASPix 3.1 Telescope	MENG, Lingxin
15:20	[21] [D05] Characterisation of neutron irradiated Digital Pixel Test Structures	BECHT, Pascal

monolithic: (2) (16:00 - 17:30)

-Conveners: .Yoshiyuki Onuki

time	[id] title	presenter	
16:00	[72] [D06] R&D status of monolithic SOI pixel sensor for vertex detector	ISHIKAWA, Akimasa	
	[11] [D07] The monolithic ASIC for the high precision preshower detector of the FASER experiment at the LHC	FERRERE, Didier	
	[69] [D08] Development of the radiation hard MALTA CMOS sensor for tracking applications	PERNEGGER, Heinz	