

ITS Upgrade Studies - Working Group 4

Strip Detector Design and Implementation

ITS Upgrade Meeting, 29th November 2010

L. Musa

- ◉ Tasks
- ◉ Structure
- ◉ Discussion

Study and **Identify** suitable technologies and techniques for the detector implementation. **Define** the detector design and study its feasibility

- sensors, front-end chip, interconnectivity, electrical bus, ladder
- readout chip (WG4 \leftrightarrow WG3)
- power distribution and regulation (WG4 \leftrightarrow WG3)
- on-detector/off-detector communication (WG4 \leftrightarrow WG3)
- off-detector electronics (WG4 \leftrightarrow WG3)

Coordinate R&D activities

Prepare the “Pixel Detector Design and Implementation” chapter of the Technical Proposal

Members (>11)

CERN: G. Aglieri, C. Cavicchioli, F. Formenti, R. De Oliveira

Kharkov: tbd

Rome: F. Meddi

Torino: G. Mazza, A. Rivetti

Trieste: G. Contin, L. Bosisio, A. Rashevsky, I. Rashvskaya

Conveners: G. Contin, A. Rivetti

We warmly invite other members of the collaboration to join the ITS Upgrade WG3 to

- work on one or more tasks
- simply participate to the meetings and contribute to the discussions