Contribution ID: **6**                                                Type: **not specified**

# Data Readout for Advanced Designs (DRAD), an update

*Friday, 24 June 2022 11:30 (20 minutes)*

Particle detectors systems need state of the art Data Acquisition Systems as backend. DRAD (Data Readout for Advanced Designs) is a a new DAQ with a client-server data architecture able to handle up to 4 Hybrid Pixel Detectors simultaneusly. At the present, DRAD operates with the OC4SENS read-out chip but is easily adaptable to other pixel detectors. The DAQ is based on a System-on-Module (SoM) that includes FPGA logic fabric and microprocessors. The client-server architecture is enabled by a Linux operating system. The full DAQ is very compact, reducing the hardware load typically needed in particle tracking experiment, specifically during the compulsory particle telescope characterization.

**Primary authors:**    Mr BLANCO, Pedro (pblanco@us.es);   Dr HINOJO MONTERO, Jose María (ETSI-University of Sevilla);   Mr JIMÉNEZ, Jorge (ETSI-University of Sevilla);   Dr MILLAN, Rafael (ETSI-University of Sevilla);   Dr MUÑOZ, Fernando (ETSI-University of Sevilla);   PALOMO PINTO, Francisco Rogelio (Universidad de Sevilla (ES))

**Presenter:**   PALOMO PINTO, Francisco Rogelio (Universidad de Sevilla (ES))

**Session Classification:**   Monolithic devices