

Session Program

5-6 Sept 2022

1st MONOLITH Workshop on silicon sensors for timing and their applications

Timing with silicon

University of Geneva, Rue de l'École de Médecine 20, 1205 Genève, Seminar room AEM-074
Rue de l'École de Médecine 20, 1205 Genève

Monday 5 September

09:00

Timing with silicon

Session |

Location: University of Geneva, Rue de l'École de Médecine 20, 1205 Genève, Seminar room AEM-074, Rue de l'École de Médecine 20, 1205 Genève

09:00–09:10

Welcome — Coffee and croissants

Speaker

Giuseppe Iacobucci

09:10–09:30

Simulations + numerical calculations for fast timing sensors

Speaker

Werner Riegler

09:30–09:50

Design of fast CMOS in 65 nm TJ

Speaker

Walter Snoeys

09:50–10:10

Timespot project testbeam results and simulations

Speaker

Adriano Lai

10:10–10:50

Discussion

10:50–11:05

Coffee break

11:05–11:25

MONOLITH project simulations and testbeam results

Speaker

Magdalena Munker

11:25–11:45

Overview LGAD technology at FBK

Speaker

Giovanni Paternoster

11:45–12:05

AC-coupled LGAD with HPKK technology

Speaker

Koji Nakamura

12:05–12:45

Discussion

12:45