

13th International "Hiroshima" Symposium on the Development and Application of Semiconductor Tracking Detectors (HSTD13)



University of
Zurich^{UZH}



Vancouver, 6th December 2023

Time resolution of single pixel irradiated 3D devices up to $10^{17} n_{eq}/cm^2$
at 120 GeV SPS pion beams

Evangelos – Leonidas Gkougkousis

University of Zurich, CERN



•3D Sensors

Timing at Extreme Fluences

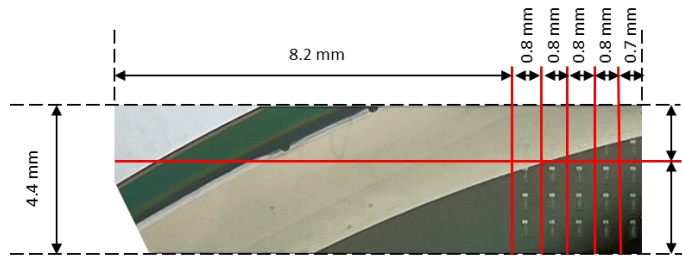
3D Sensors: Decoupling of charge generation and drift volume
(Standard columns, TimeSpot, Hex geometries ect.)

Pros

- High radiation tolerance up to several times $10^{16} n_{eq}/cm^2$
- Short drift distances with fast rise times
- Reduced Landau fluctuation, practically non-existent for perpendicular tracks

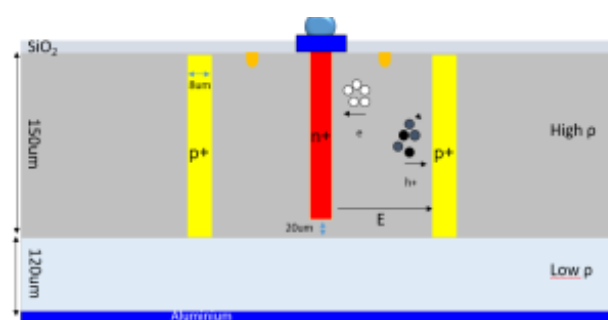
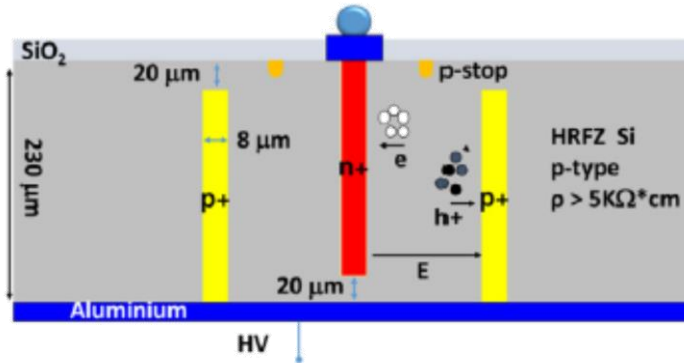
Cons

- Non-uniform field geometry
- Lower production yield
- Increased cell capacitance



Double Sided
(thicker, more expensive)

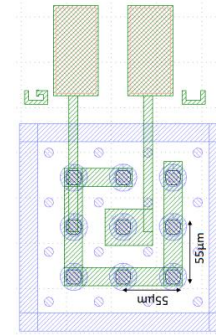
Single Sided
(thinner, simpler process)



ATLAS IBL Type

Double sided n-on-p process
Active thickness 190 μm (total thickness 230 μm)
High Resistivity (> 2 kΩm × cm) Fz silicon

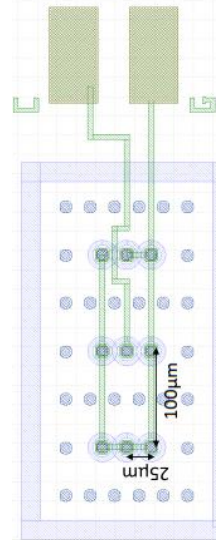
- ✓ Pixel Size $55 \times 55 \mu m^2$
- ✓ Capacitance $\sim 80 \text{ pF}$
- ✓ 10 μm column diameter
- ✓ Closest electrode distance: 19 μm



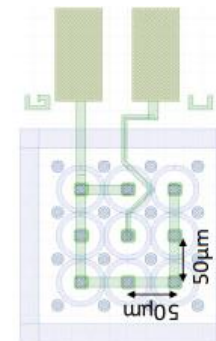
ATLAS Pre-Production type

Single sided n-on-p process
Active thickness 130 μm (total thickness 150 μm)
High Resistivity (> 2 kΩm × cm) Fz silicon

- ✓ Pixel Size $25 \times 100 \mu m^2$
- ✓ Capacitance $\sim 20 \text{ pF}$
- ✓ 8 μm column diameter
- ✓ Closest electrode distance: 35.5 μm

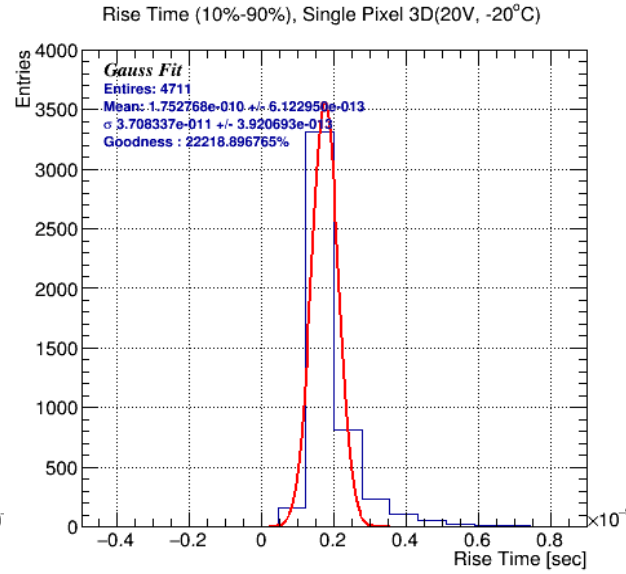
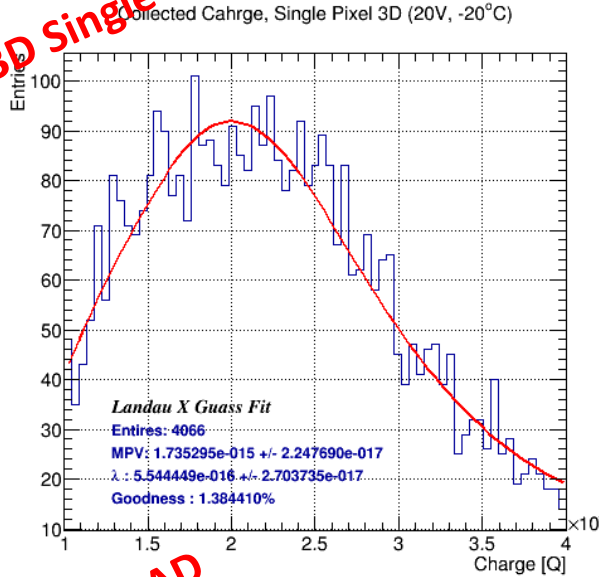


- ✓ Pixel Size $50 \times 50 \mu m^2$
- ✓ Capacitance $\sim 37 \text{ pF}$
- ✓ 8 μm column diameter
- ✓ Closest electrode distance: 19.3 μm



•3D Sensors - Timing

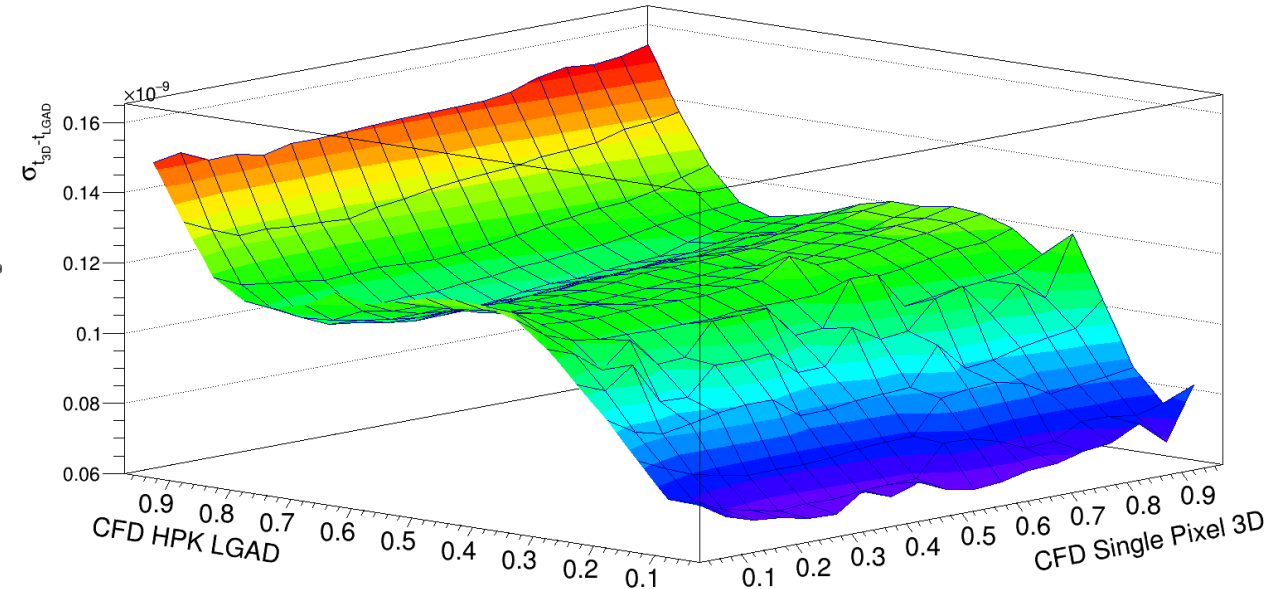
3D Single Cell



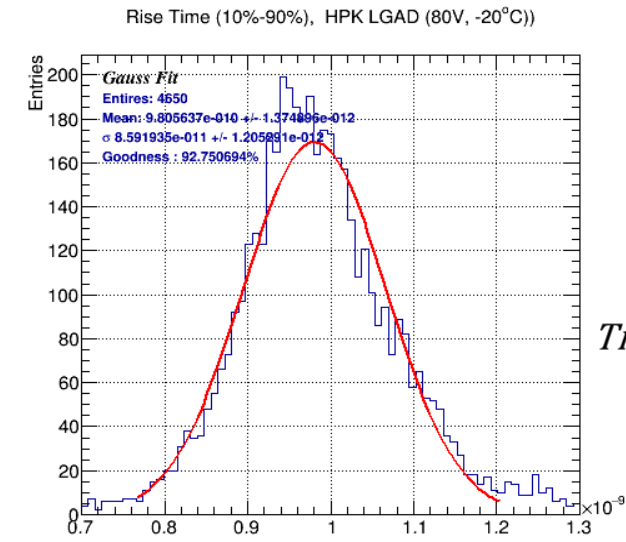
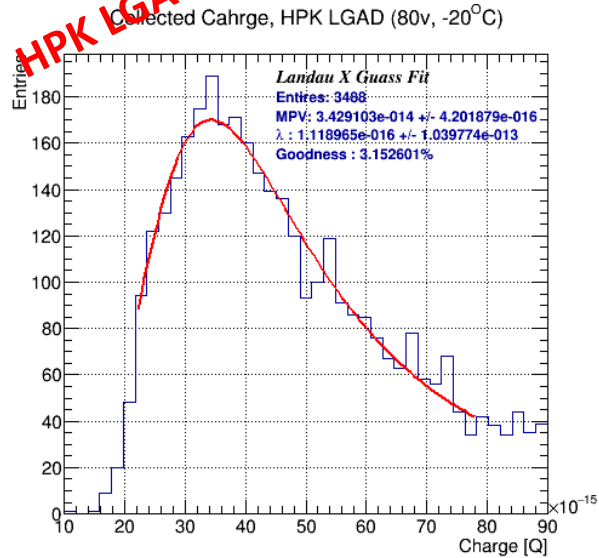
- Extremely fast rising edge (< 180 psec)
- Linear stable behavior with CFD, good SNR control

$$(\sigma_{Dut})_{CFD_{ij}} = \sqrt{(\sigma_{Tot}^2)_{CFD_{ij}} - (\sigma_{Ref}^2)_{CFD_i}}$$

CFD Map, LGAD - Single Pixel 3D (-20°C, 20V)



HPK LGAD



2D optimization plot – 0.5% binning

Time Resolution: $\sigma_{tot}^2 = \underbrace{\sigma_{timewalk}^2}_{\sigma_{Dist.}^2 + \sigma_{Landau}^2} + \underbrace{\sigma_{jitter}^2}_{\left(\frac{t_{rise}}{S/N}\right)^2} + \underbrace{\sigma_{conversion}^2}_{\left(\frac{TDC_{bin}}{\sqrt{12}}\right)^2} + \underbrace{\sigma_{Clock}^2}_{\text{Fixed Term } \sim 5-7 \text{ psec}}$

•3D Sensors – Signal Integrity

- Frequency of radioactive decay events follows Poisson law
- Record trigger time and convert to event frequency

Poisson Distribution:

$$f(n) = \mu^n * \frac{e^{-\mu}}{n!}$$

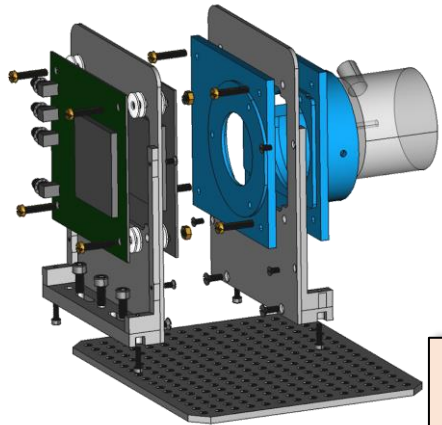
Where: **n** number of events in interval
μ mean
f(n) frequency

$n' = n * C$
Variable change
 $\mu = B/C$

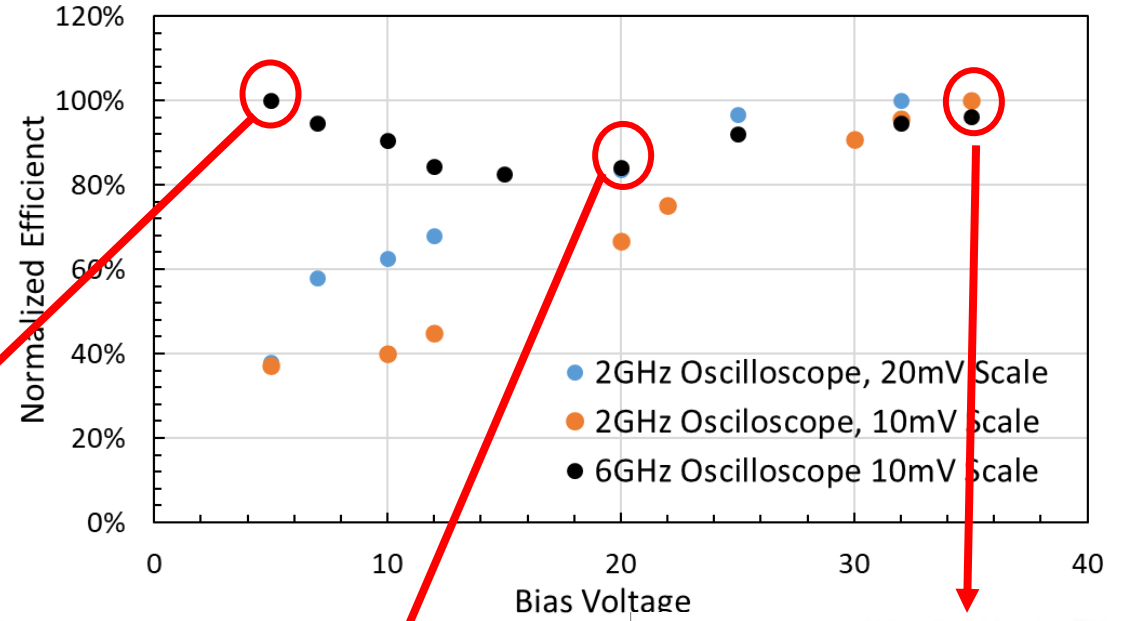
$$f(n') = A * \left(\frac{n'}{C}\right)^{B/C} * \frac{e^{-B/C}}{\Gamma\left(\frac{n'}{C} + 1\right)}$$

Where: **A** Normalization parameter
B/C mean
f(n') Scaled frequency

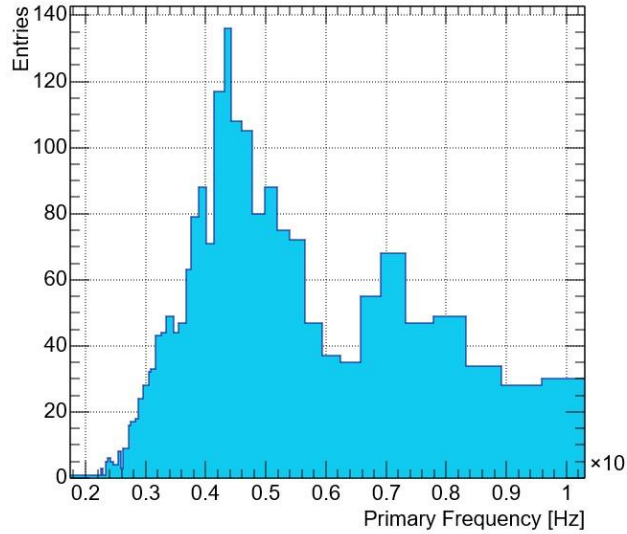
- Efficiency dependent on bandwidth
- Signal distribution in the Fourier domain highly depends on bias
- Minimum time over threshold effect for trigger latching of instrument affect efficiency



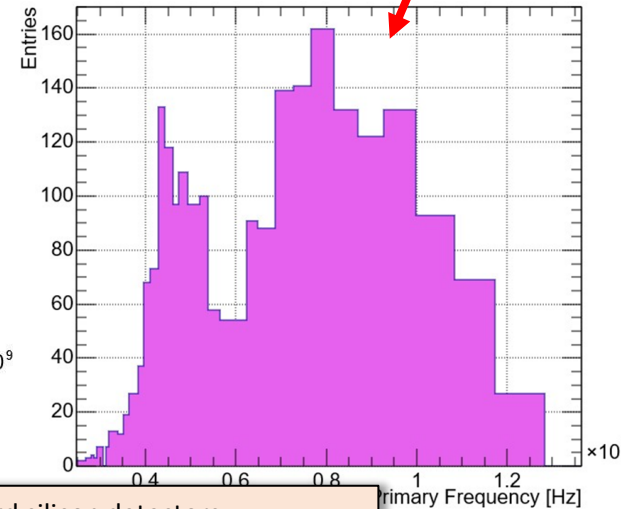
Relative Efficiency at -20°C



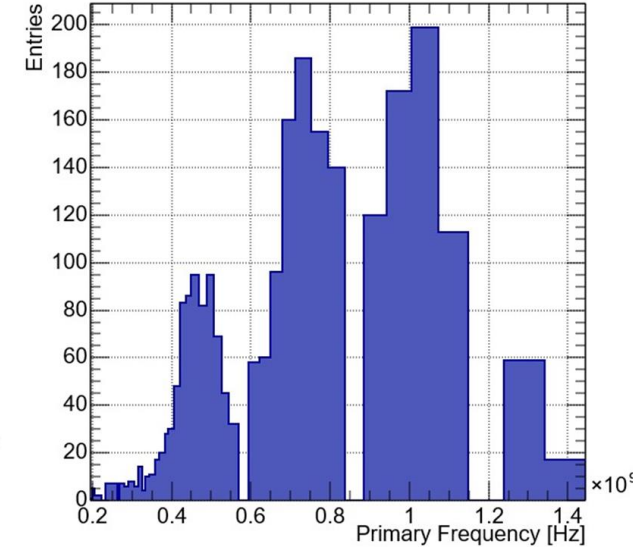
Signal FFT 5V, @ -20°C



Signal FFT, 20V @ -20°C



Signal FFT, 35V @ -20°C



V. Gkougkousis, 35th RD50 workshop on radiation hard silicon detectors
 “Efficiency estimation on irradiated LGAD with respect to sensor stability”

Planar Sensors

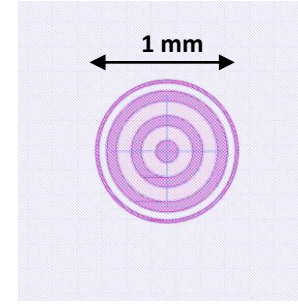
Sensors: CERN EP-R&D n-on-p planar sensor run with ADVACAM at 50, 100, 200 and 300 μm active thickness

Test Structures

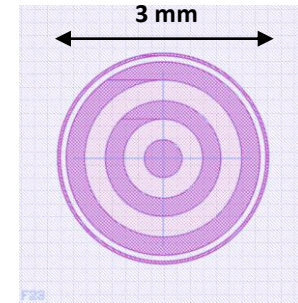
- Small diodes (3.14 mm² active area) Circular diodes for timing studies due to lower capacitance
- Big diodes (28.27 mm² active area) Circular diodes for radiation damage studies
- 5x5 Pixel matrix (0.003 mm² active area) for charge sharing and interpixel efficiency – timing studies

Issues

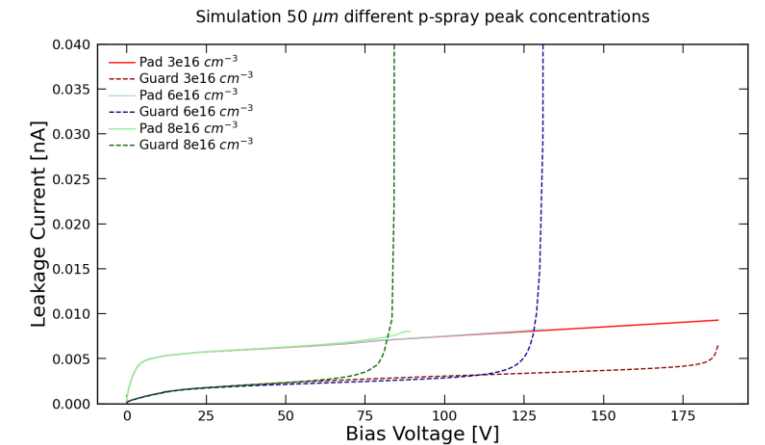
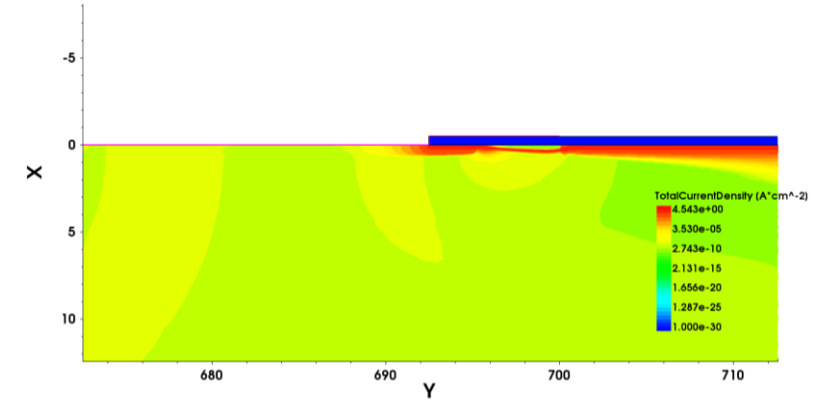
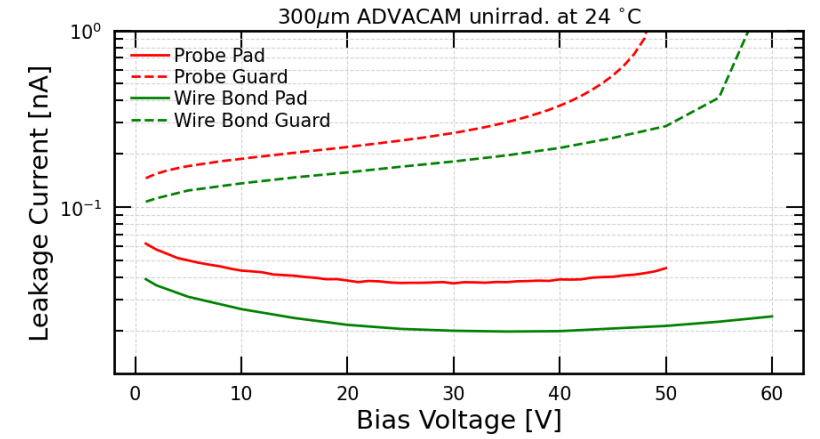
- Early breakdown due to high p-spray concentration leading to impact ionisation at the interface between p-spray and electrode implant
- Breakdown first visible in guard ring due to bigger interface region compared to pad



Small Diode



Big Diode



Irradiations

(both 3D and planar)

Neutron @ JSI (Ljubljana)

- ✓ $1 \times 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$
- ✓ $8 \times 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$
- ✓ $6 \times 10^{16} \text{ n}_{\text{eq}}/\text{cm}^2$
- ✓ $1 \times 10^{17} \text{ n}_{\text{eq}}/\text{cm}^2$

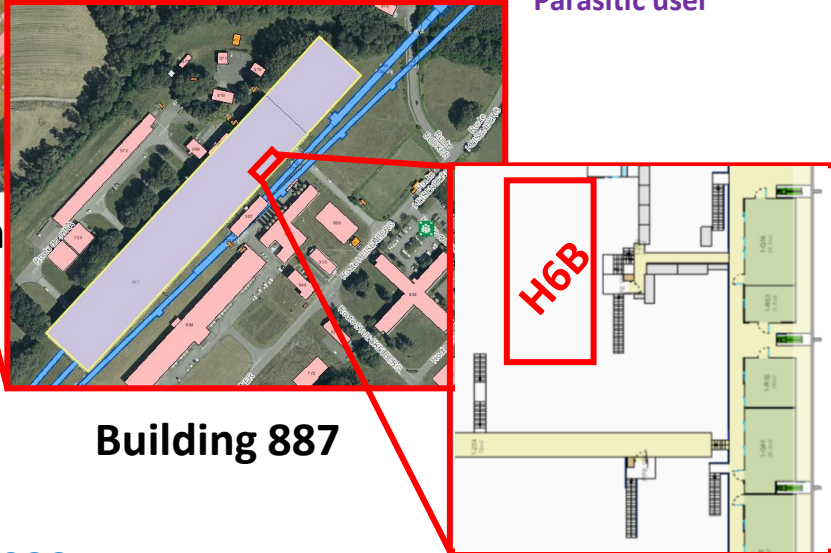
Proton @ PS

- ✓ $1 \times 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$
- ✓ $8 \times 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$
- ✓ $6 \times 10^{16} \text{ n}_{\text{eq}}/\text{cm}^2$
- ✓ $1 \times 10^{17} \text{ n}_{\text{eq}}/\text{cm}^2$

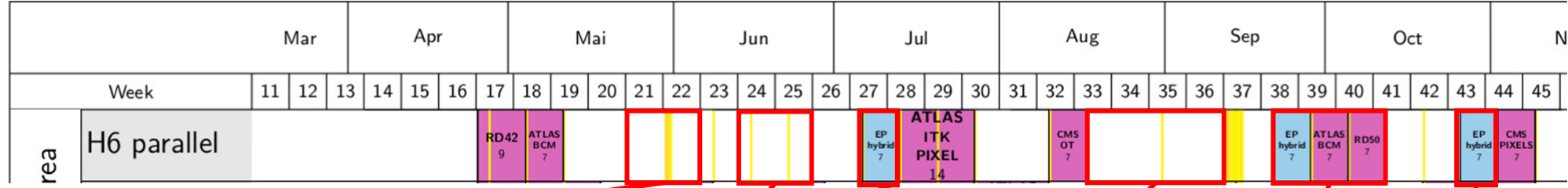
Part I - Test Beams



CERN Preveessin

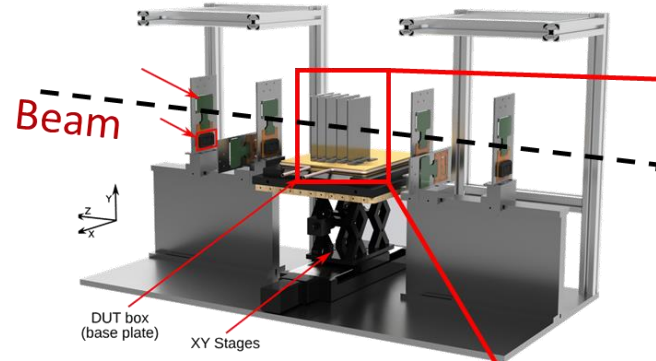


Building 887



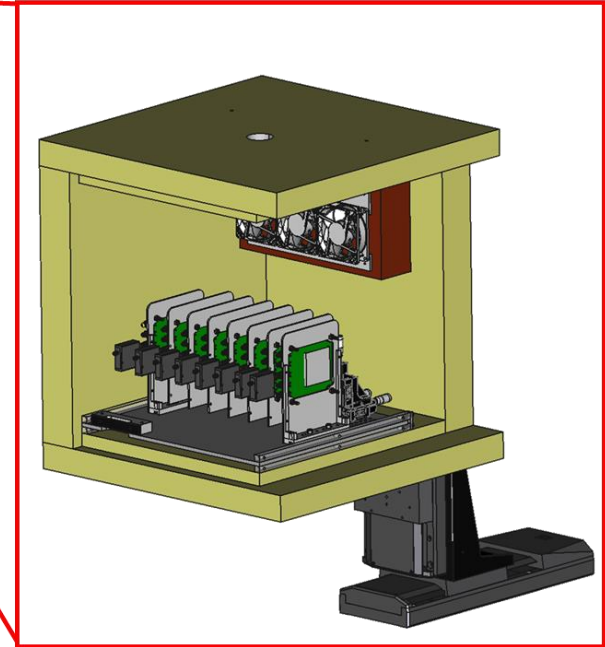
Primary user
Parallel user
Parasitic user

25 May – 8 June
15 – 29 June
6 July – 13 July
17 August - 14 September
21 September - 12 October
26 October – 2 November



The Setup

- AIDA Telescope
- Custom Cold Box
- DUTs on individual motorized individual stages
- Pixelated alignment & ROI plane

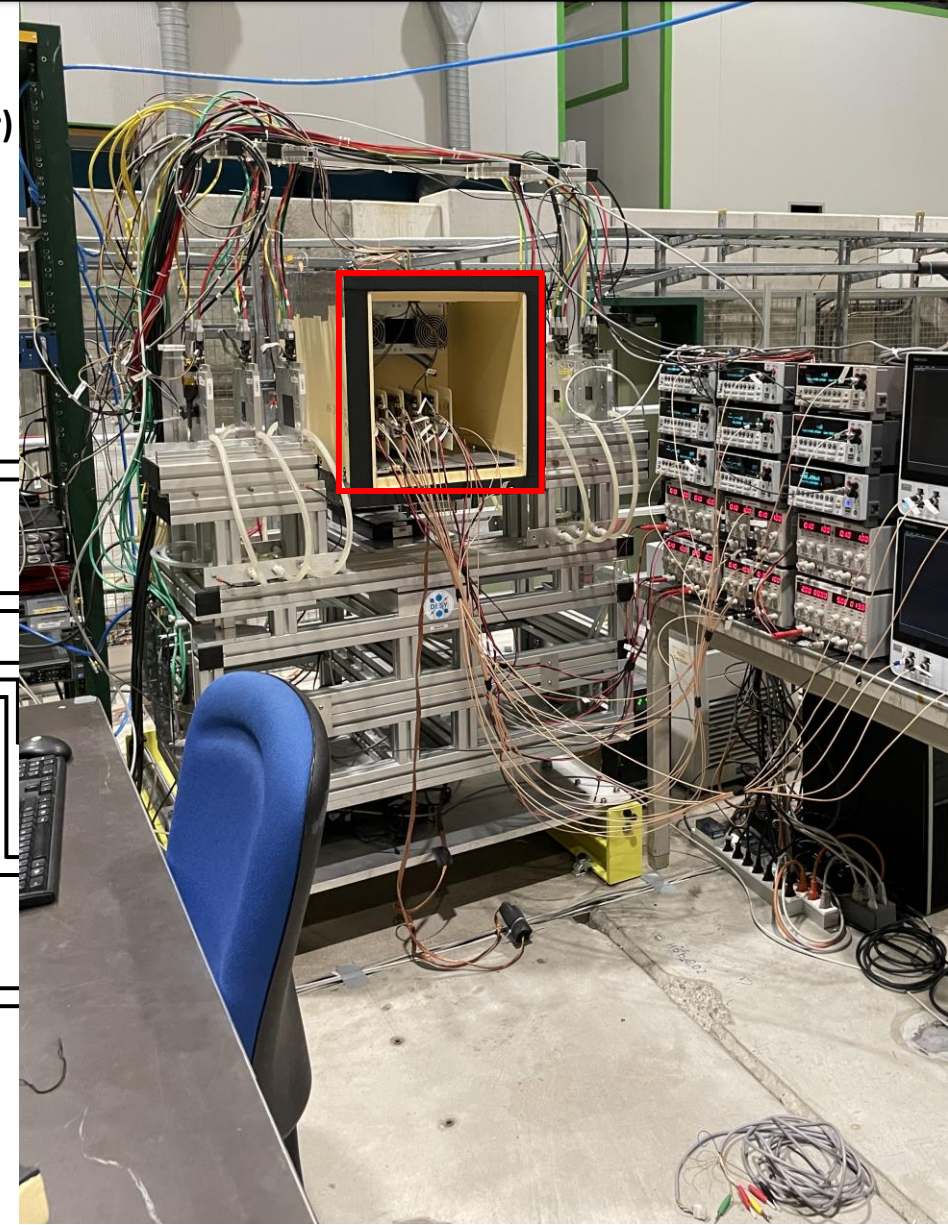
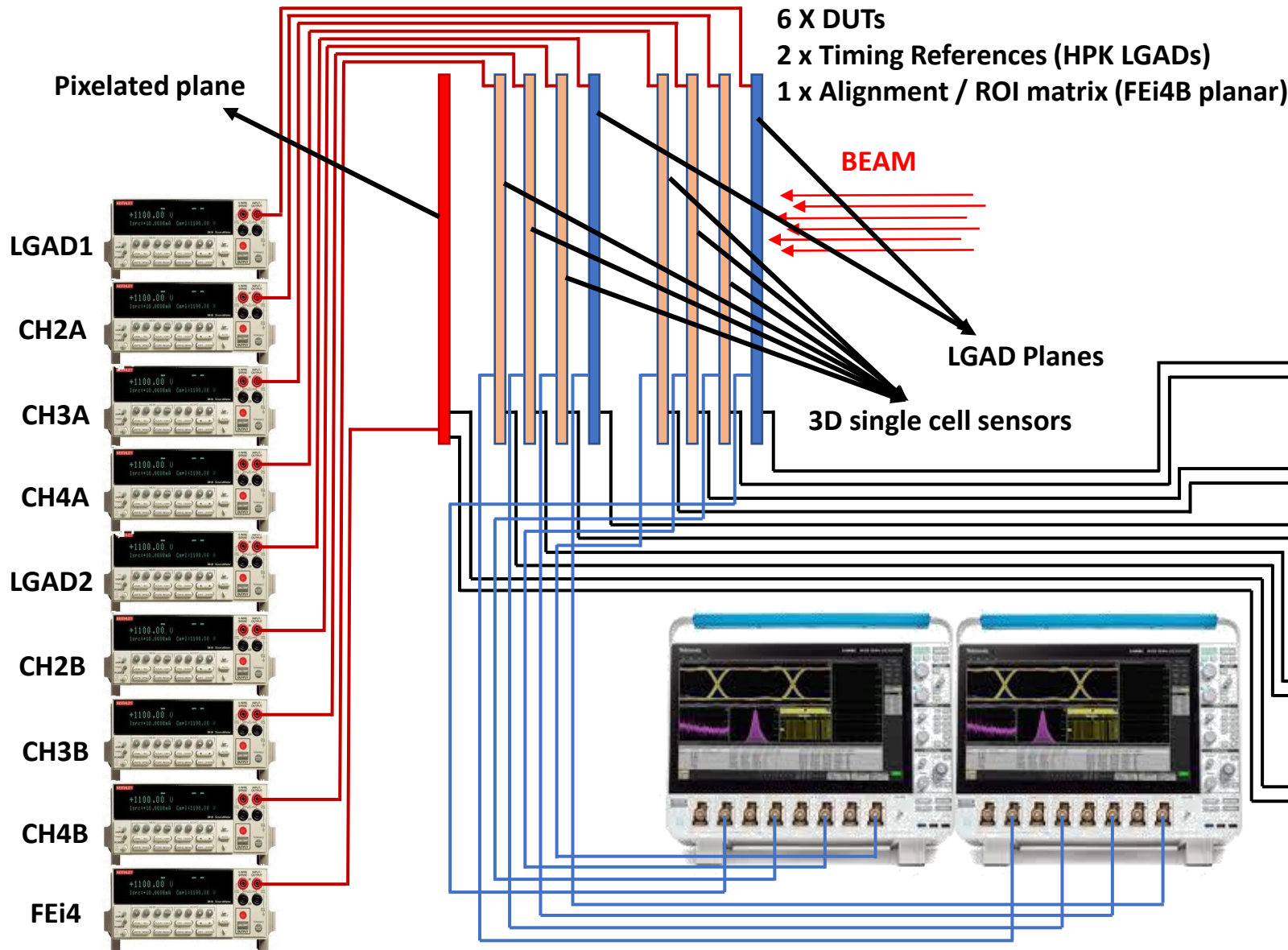


Tet Beams 2022

- Several periods but only two as primary user
- Main target irradiated Planar / 3D sensors
- No / Limited possibility of extension
- Extensive infrastructure developments

• Test Beam Configuration

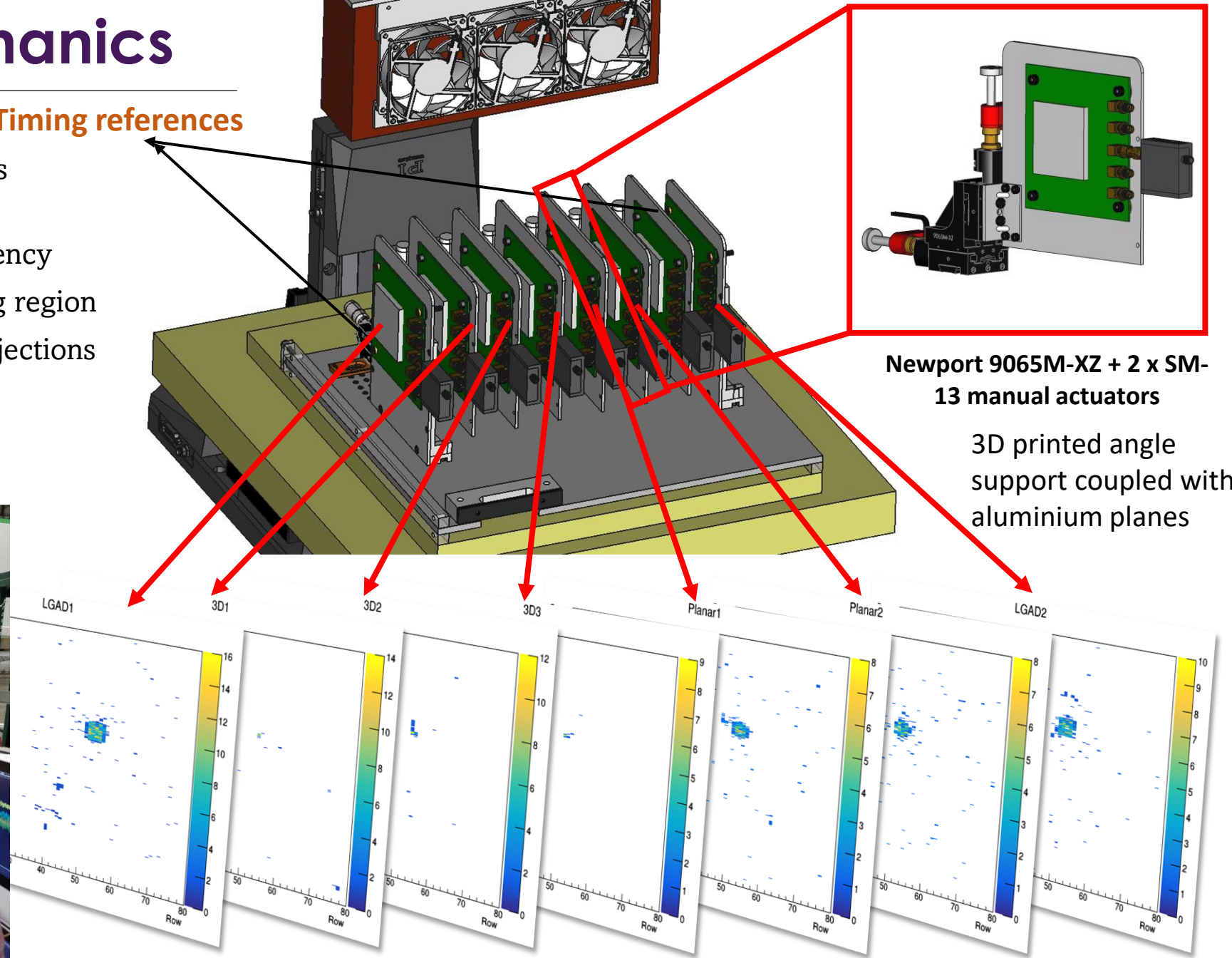
V. Gkougkousis, 40th RD50 Workshop on Radiation hard semiconductor devices for very high luminosity colliders
“Time resolution of single cell 3D devices on SPS pion beams”



• Alignment & Mechanics

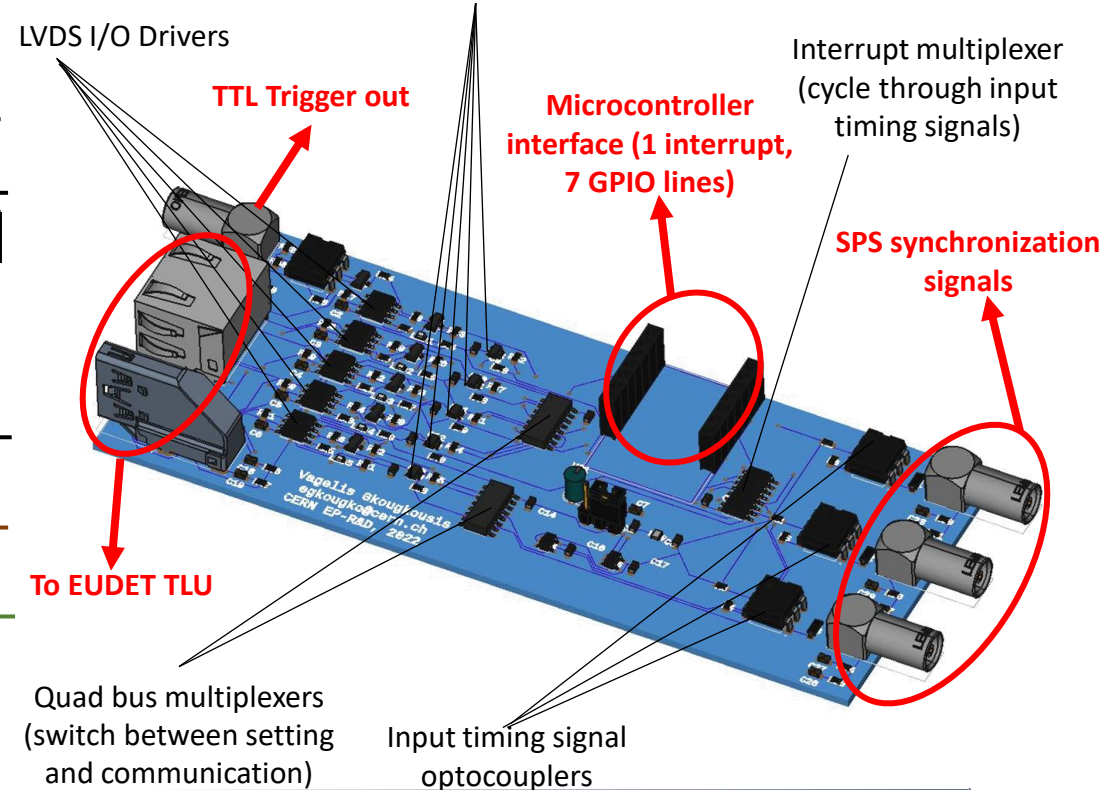
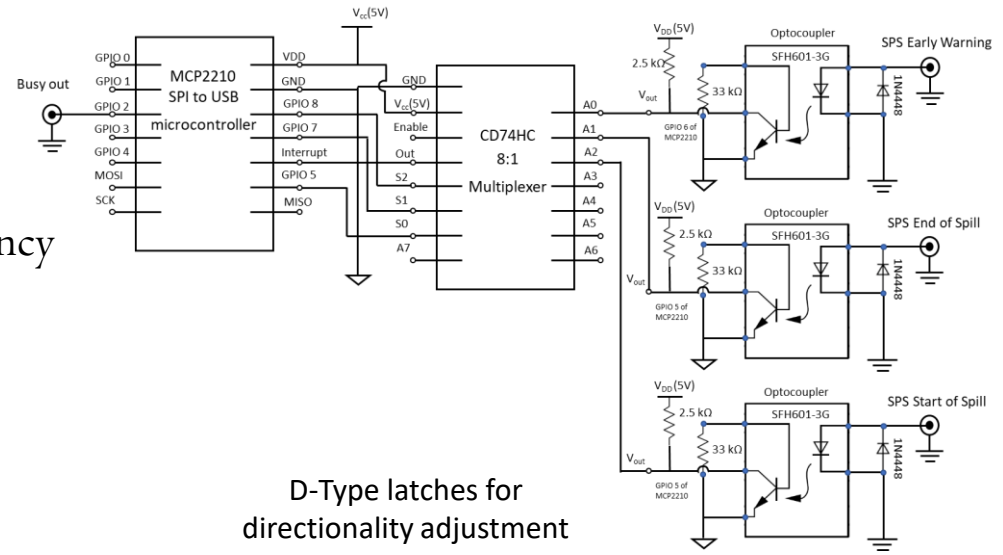
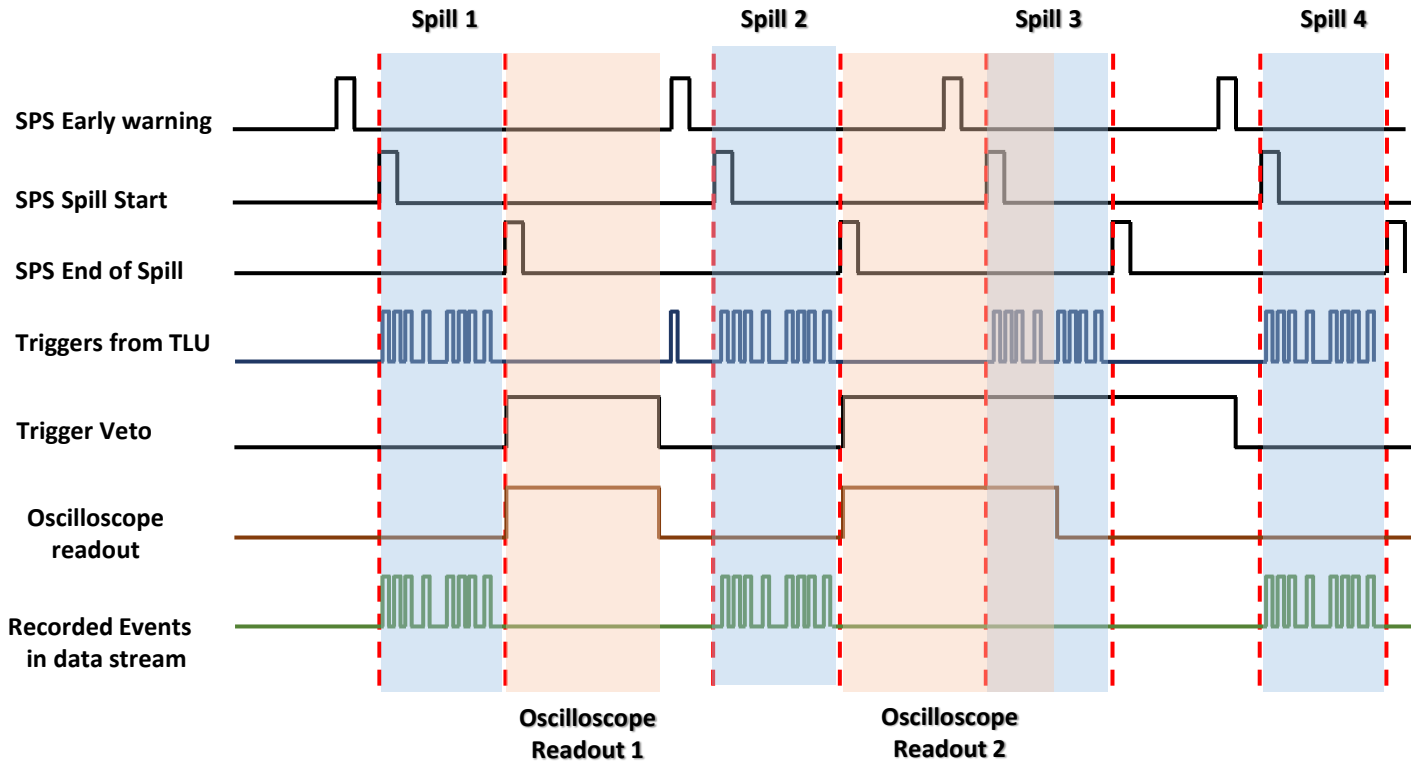
HPK LGAD Timing references

- Coincidences between DUTs and LGADs required for timing
- Alignment crucial to increase data efficiency
- Efficiency defined by largest overlapping region
- Micrometric on-line alignment using projections on FEi4 matrix
- ROI defined in addition to other trigger conditions

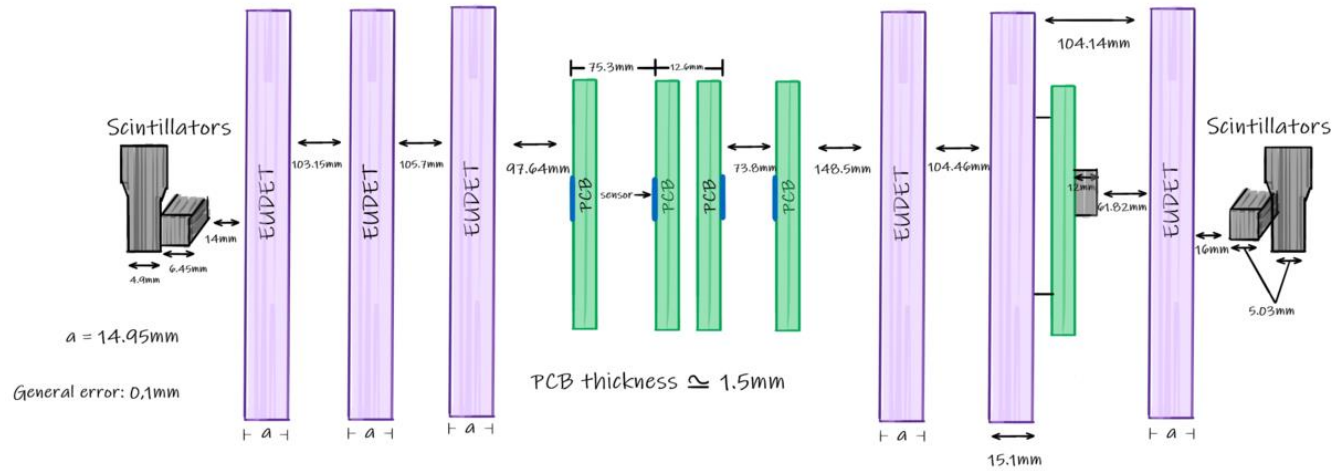


•Trigger Interface Board (TiB)

- Oscilloscope in fast readout mode with binary format
- Event readout only between SPS-spills or when event buffer full to increase efficiency
- TLU Synchronization by vetoing data taking during read-out
- RJ-45 or HDMI for EUDET TLU communication (EUDET 2 compatible)
- **Versatile design**, I/Os **Reconfigurable** and microcontroller **Reprogrammable** via USB

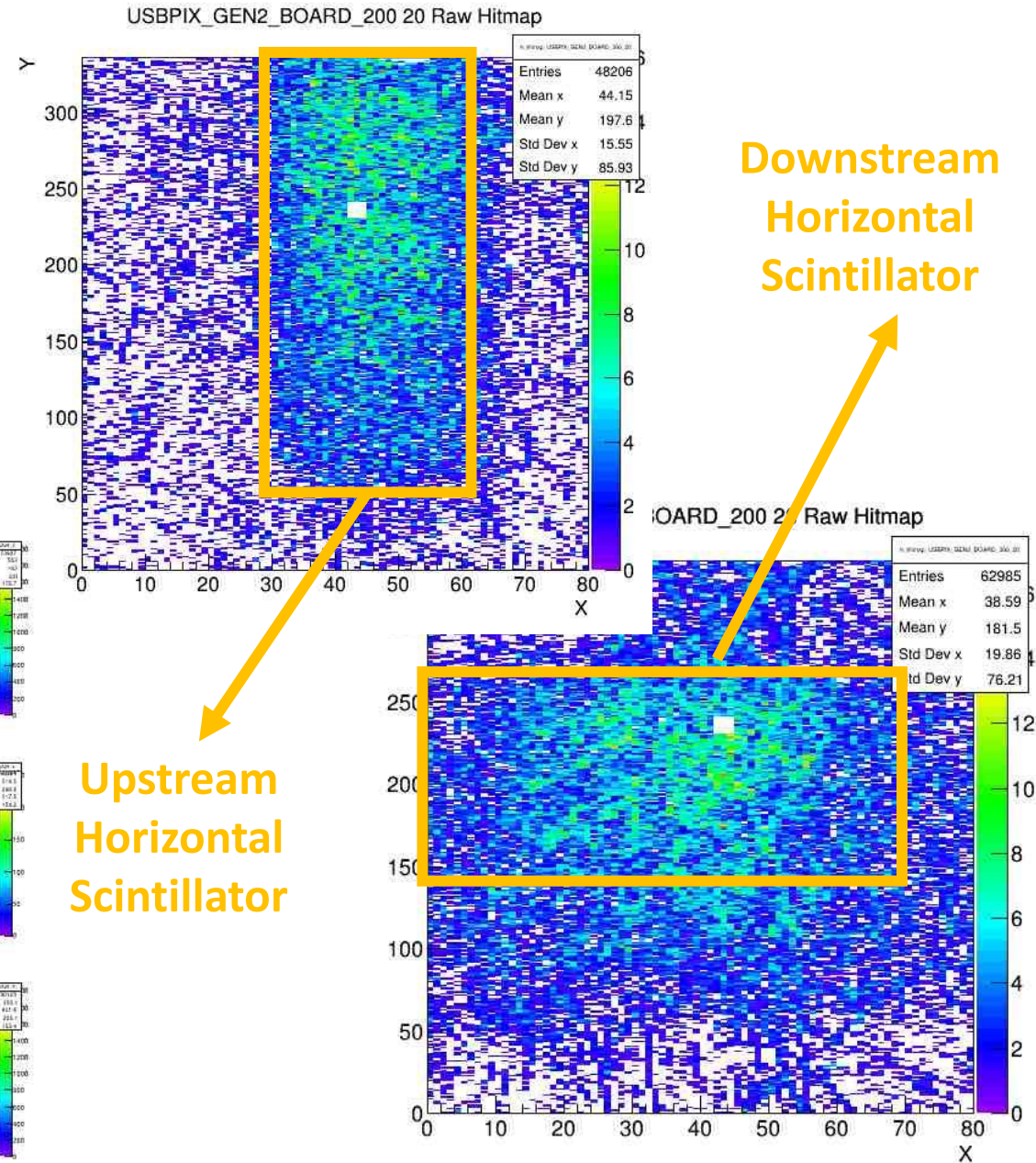
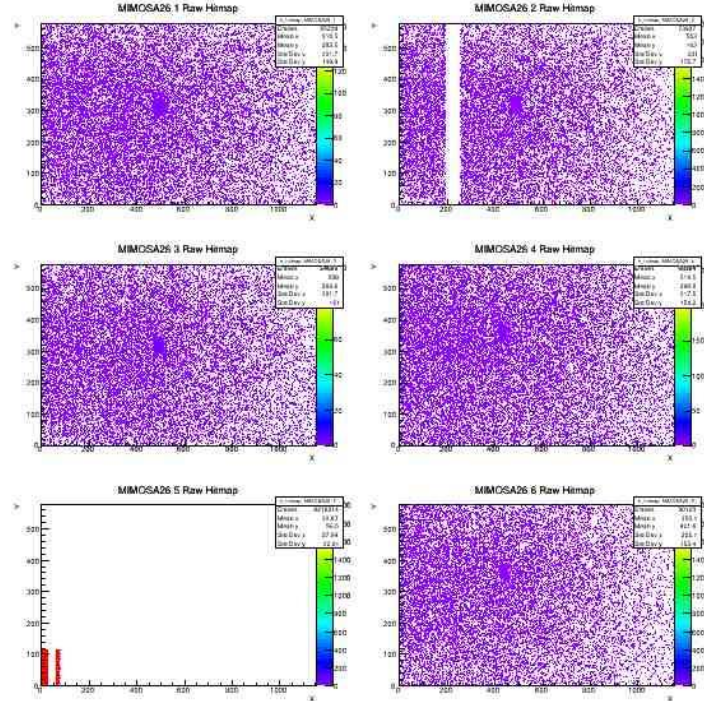


Tracking and ROI



Telescope Planes

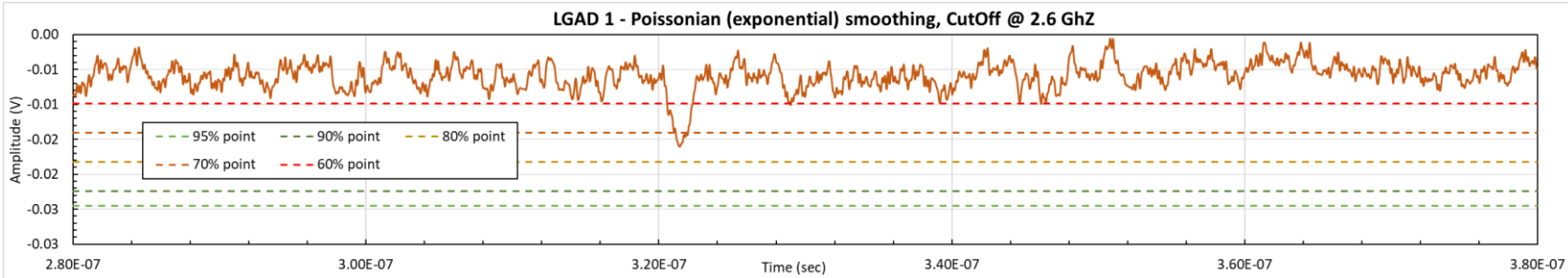
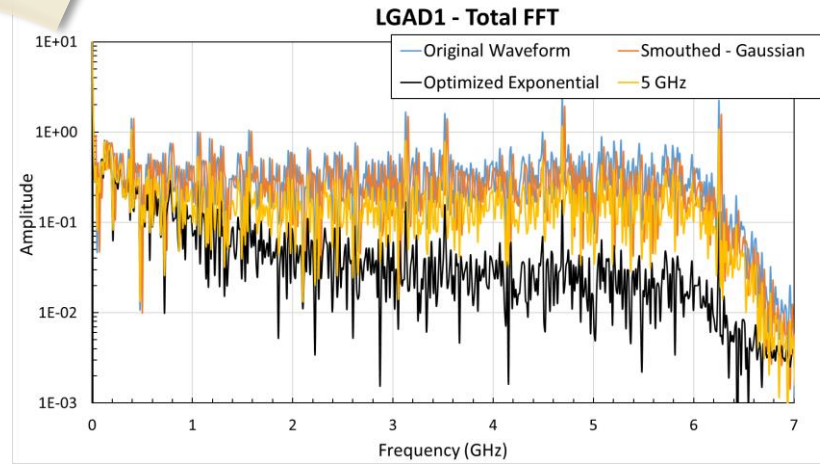
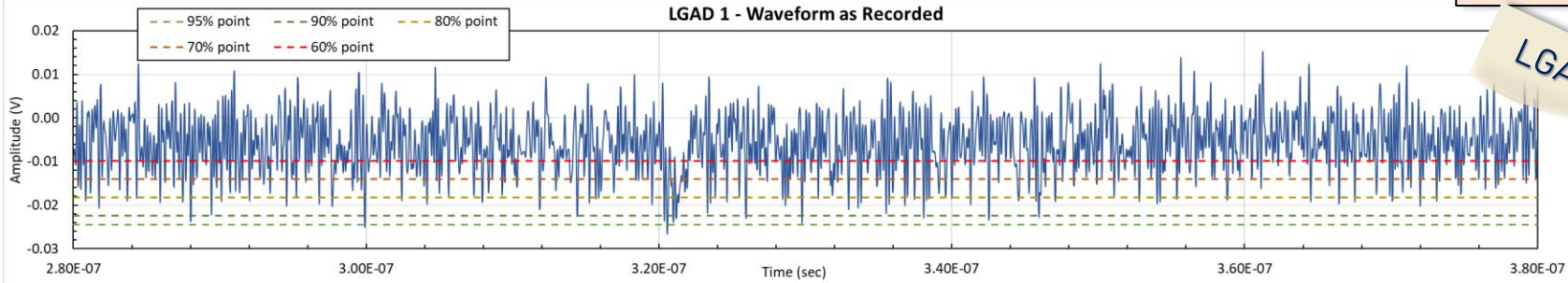
- 6 MIMOSA planes for tracking
- Plane no. 5 known to be bad
- 5 μm – 7 μm tracking resolution
- Estimated acquired number of events $\sim 1\text{M}$
- Limited beam control as parasitic user
- Suffer from low intensity and low data rates of EUDAQ



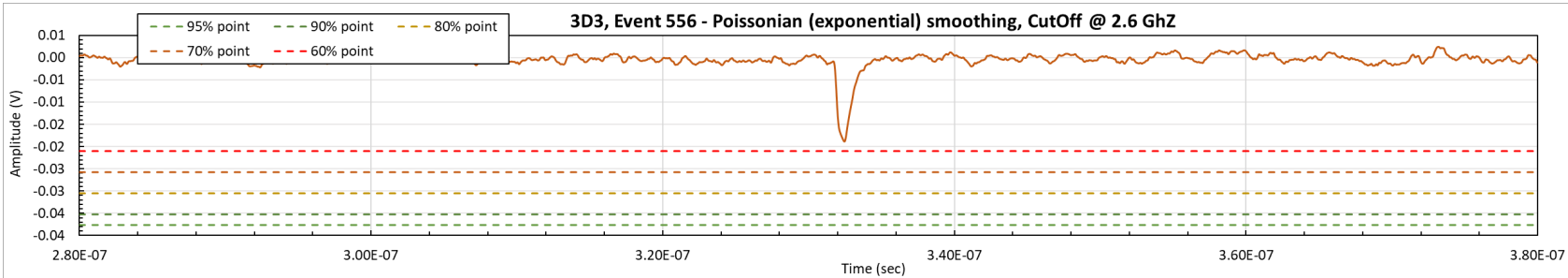
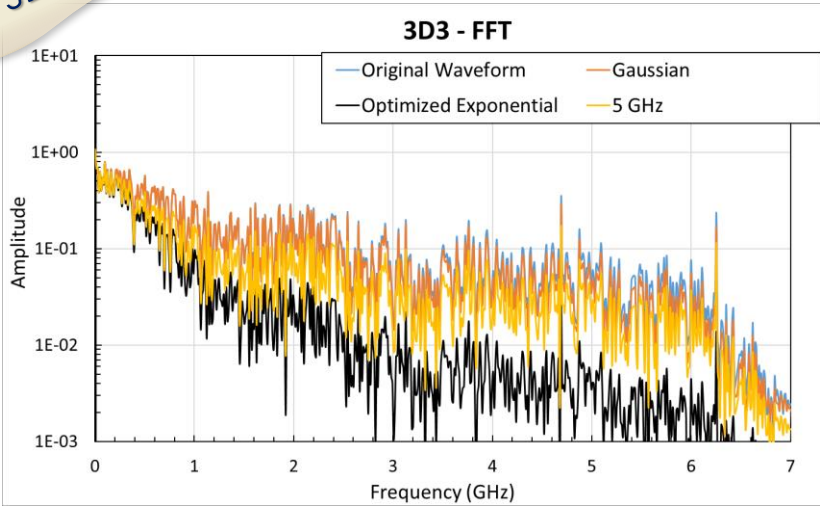
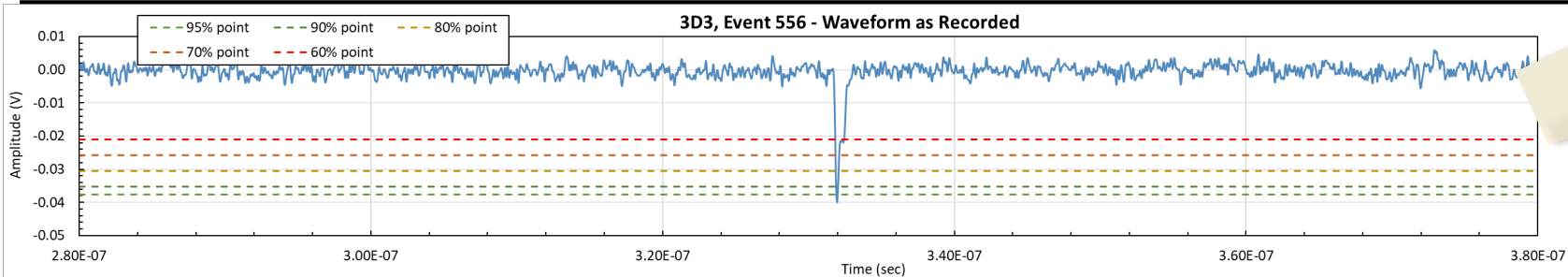
The importance of bandwidth

V. Gkougkousis, Ultrafast imaging and tracking Instrumentation, Methods and Applications Conference - ULITIMA 2023
 "Radiation damage effects overview on Low Gain Avalanche Diodes"

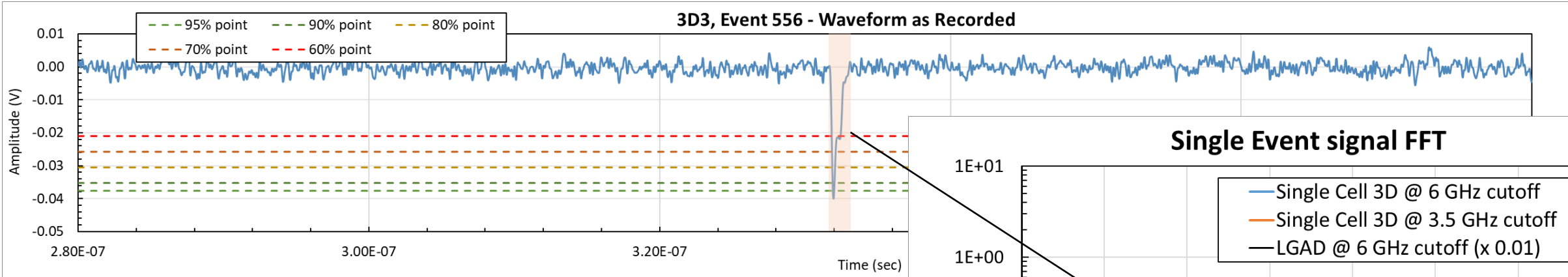
LGADs



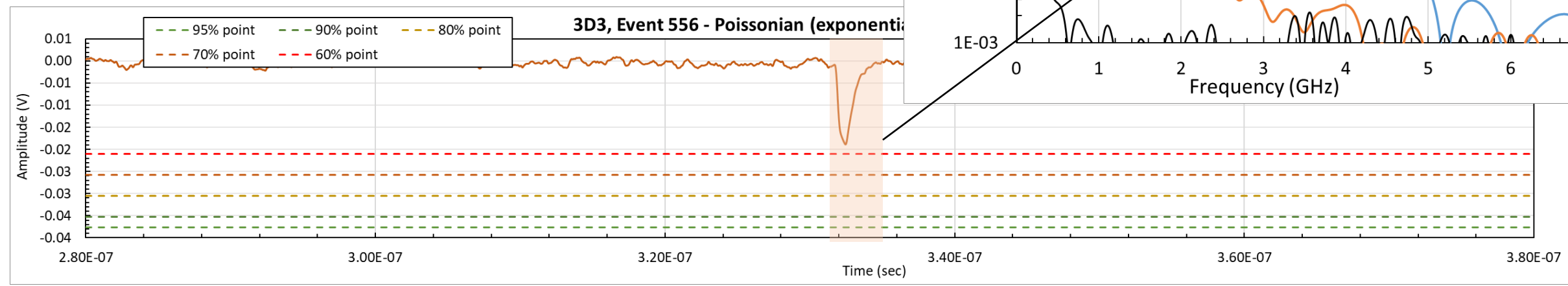
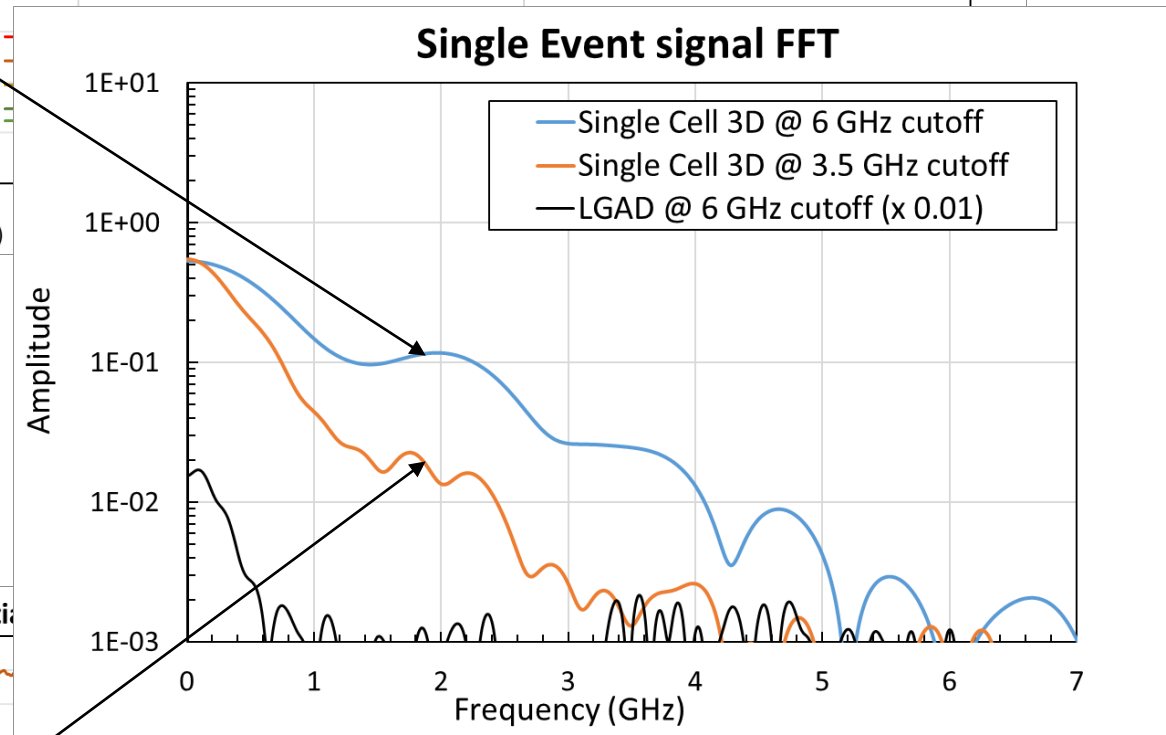
3Ds



The importance of bandwidth



- **60 % less amplitude**
- **20 % more charge**
- **If bandwidth not correct, we are probing electronics transfer function**



•Single Event Burn-out

Single Event Burn-Out was also observed in 3D sensors but in much higher fields with respect to LGADs ($\sim 25 \text{ V} / \mu\text{m}$) and on the $1\text{e}16$ fluence but not $1\text{e}17$

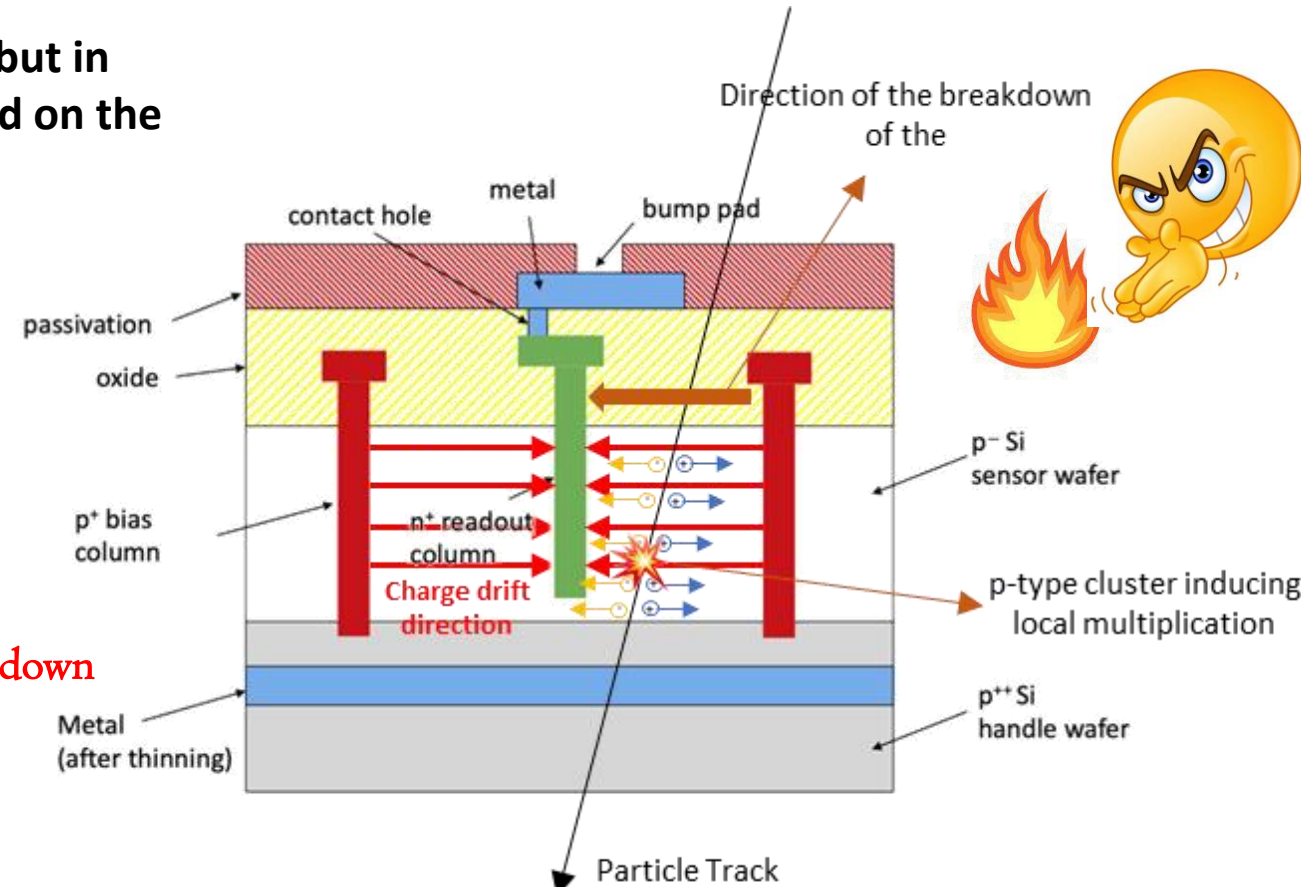
Geometry

- Charge generation and charge drift directions not parallel, very high angles
- Drifting charges do not add up to the avalanche created at the ceed point

- Much smaller capacitance (20-80 fF), smaller stored energy in the sensor to be released once conductive channel forms ➔ Recoverable Breakdown

Irradiation

- Effect observed when concertation of defects high enough statistically
- BUT**
- Carrier lifetime (trapping) should be sufficient to allow for the avalanche to evolve

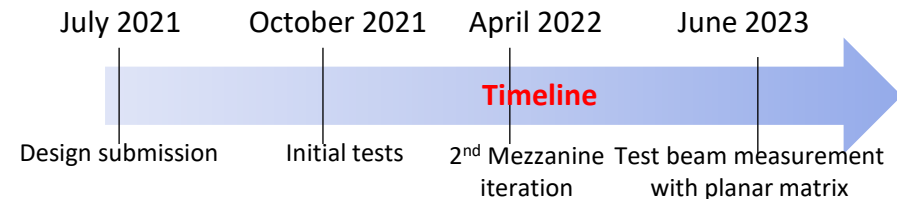


Perfect storm

- LGADs ➔ $\sim 1\text{e}15$
- 3Ds ➔ $\sim 1\text{e}16$

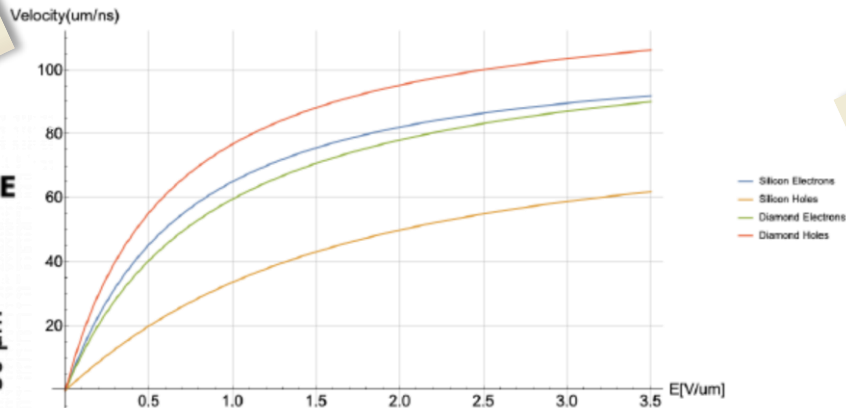
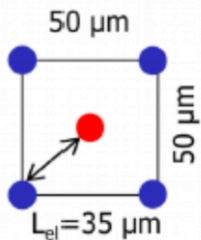
V. Gkougkousis, "LGAD Safety and Stability Concerns" – HGTD Sensor Meeting April 2018
[CERN-OPEN-2023-017](https://cds.cern.ch/record/2811171/files/CERN-OPEN-2023-017)

•16 Channel Board



The issue....

50x50 μm², 1E



The solution

- ❖ High frequency multichannel versatile board
- ❖ Mezzanine design for fast sensor interchangeability
- ❖ Suitable for matrices (AC-LGAD applications) but also for single pad devices

- Assuming a linear field dependence and a -15 V operation point at 35 μm column distance:

$$|E| \cong 0.43 \text{ V}/\mu m$$

- Estimating drift velocity for electrons:

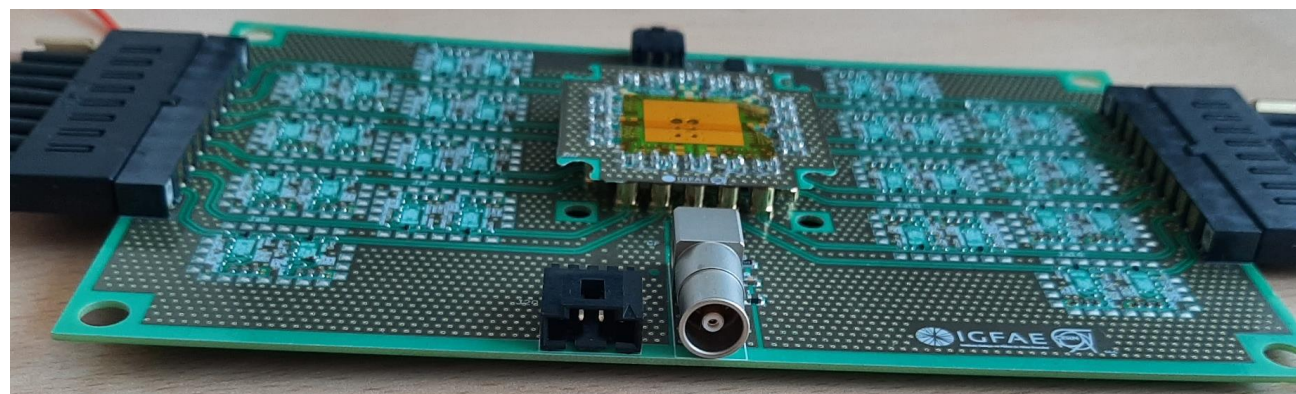
$$v_{drift}^e = \frac{\mu_{0,e} \times E}{\left[1 + \left(\frac{\mu_{0,e} \times E}{v_{sat}^e} \right)^{\beta_e} \right]^{1/\beta_e}}$$

with $v_{sat}^e = 107 \mu m/ns$, $\mu_{0,e} = 1417 \frac{cm^2}{Vs}$, $\beta_e = 1.109$

$$v_{drift}^e \approx 41.4 \mu m/ns$$

- Extrapolated Rise time and Frequency:

$$t_{Rise} \approx \frac{1}{3} \times t_s = \frac{1}{3} \times \frac{d/2}{v_{drift}^e} \approx 140 \text{ psec} \Rightarrow \mathbf{2.3 \text{ GHz}}$$

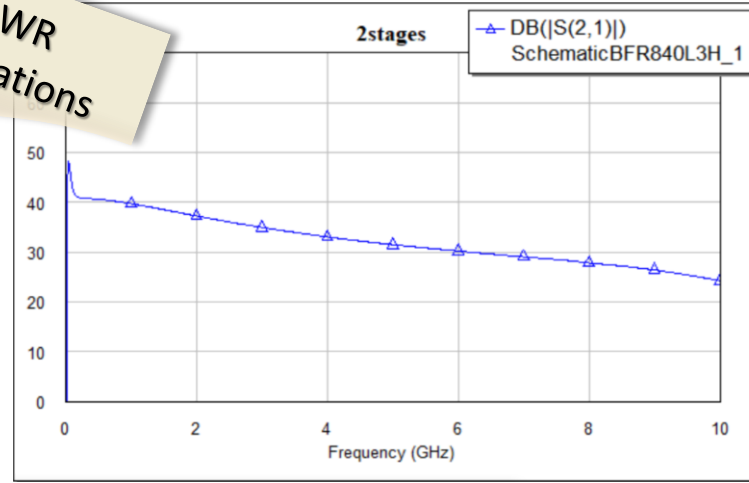
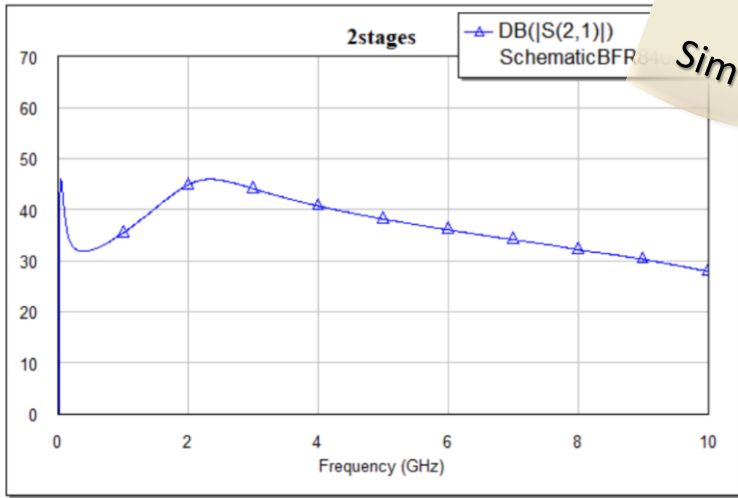


• Simulations and performance I

Initial design

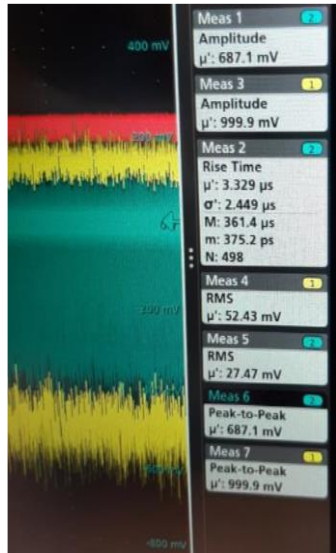
Modified –Uniform design

AWR Simulations

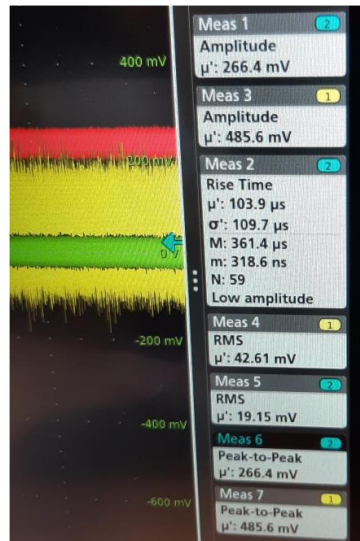


- Optimized design for uniform response with frequency
- No sharp gain change discontinuities
- No undershoot/overshoot observed
- Gain moderated to ~ 70 for a two-stage configuration
- 20% Higher SNR than UCSC board (with both stages)
- 2 x SNR with respect to UCSC board + miniCircuits second stage amplifier
- On going energy and transimpedance simulation

With signal injection

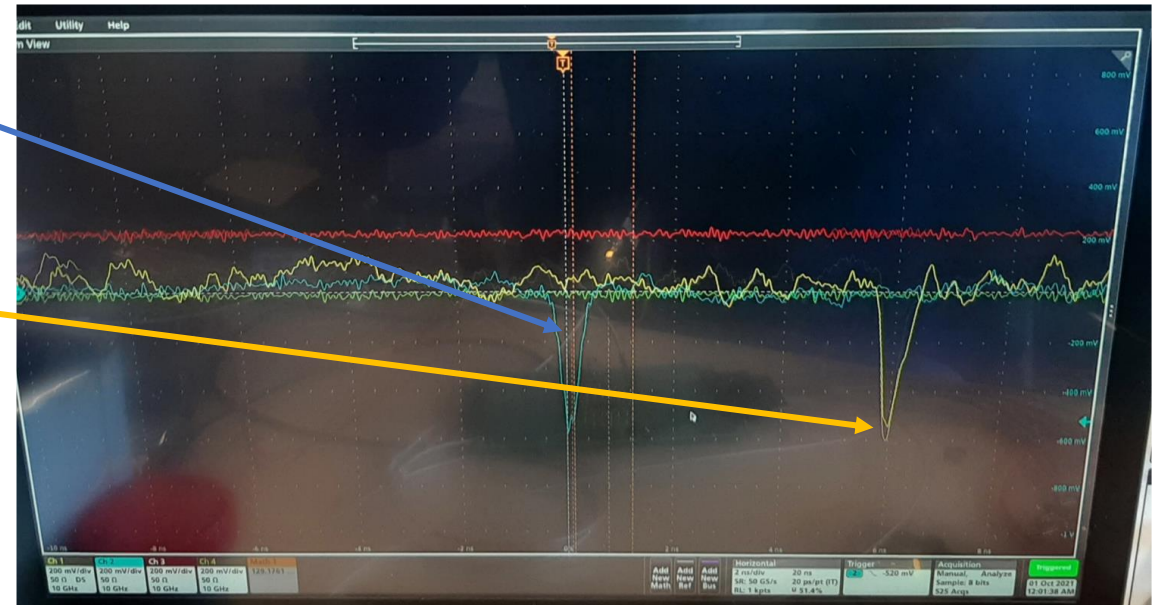


Without signal injection



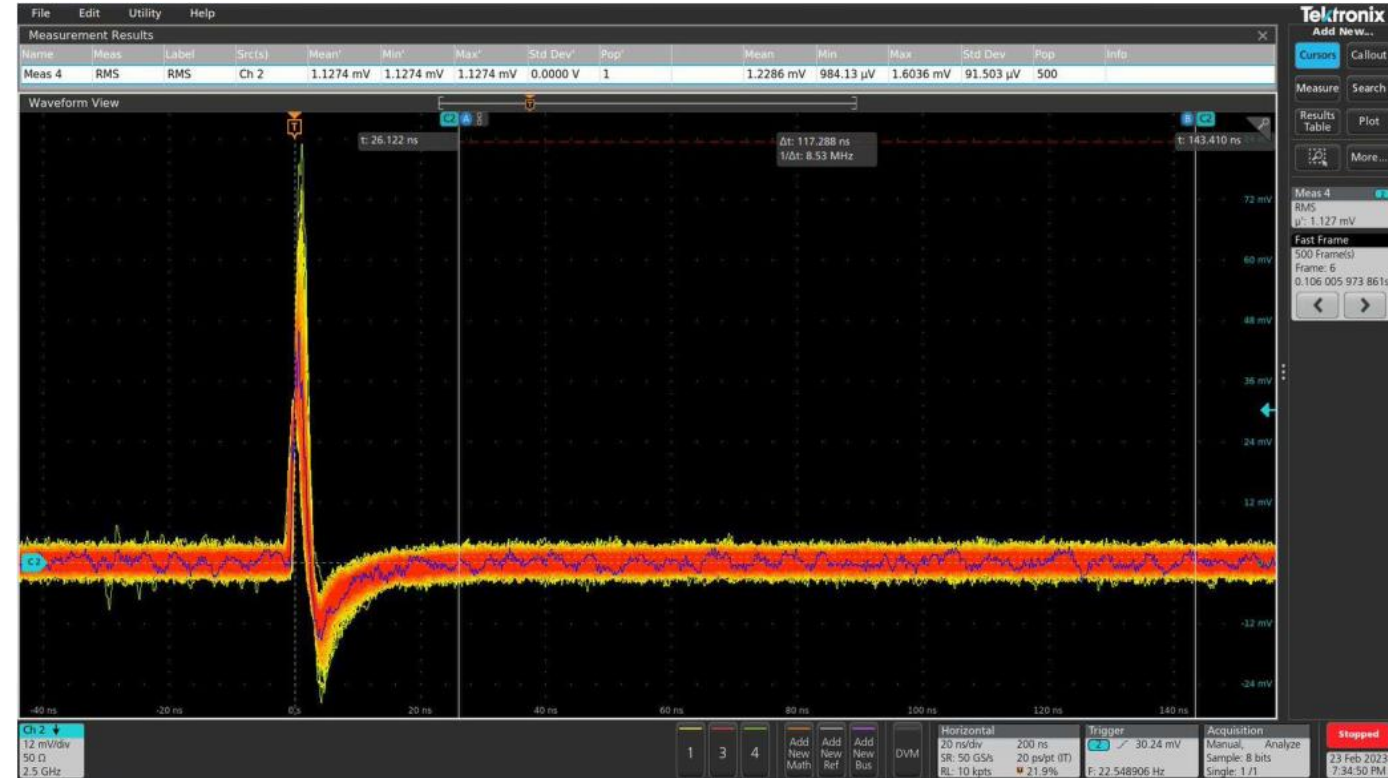
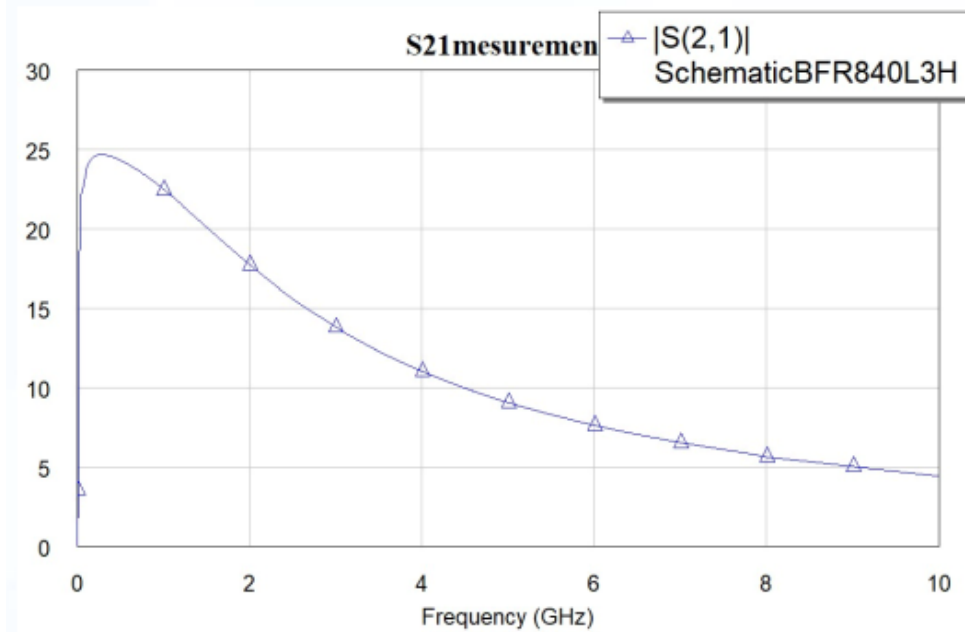
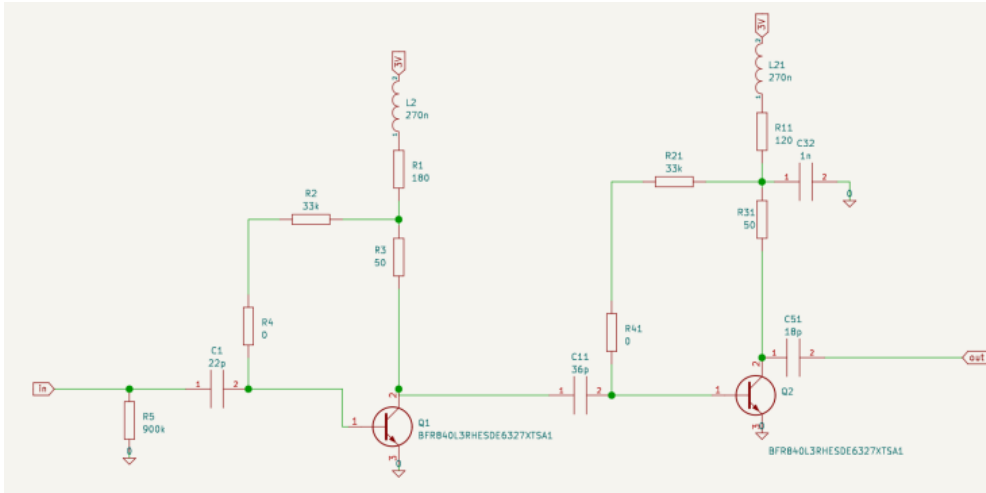
Blue: 16 channel board

Yellow: UCSC board (only one stage)



• Simulations and performance II

Edgar Lemos Cid , 18th Trento Workshop on Advanced Silicon Radiation Detectors
“Multichannel board for picosecond timing measurements of silicon sensors”



- Mean noise (\sim RMS) of 1.2 mV for a gain of \sim 70
- Tested with a $55 \times 55 \mu\text{m}$ 3D double sided sensor of $230 \mu\text{m}$
- Not frequency optimized for this sensor geometry with fast dropout at lower scale
- Leads to bipolar signal due to the increased trans-impedance at lower frequencies

• Conclusions

3D Pixels - Planar measurement campaign

- Several productions under investigation of different pixel size and thickness
- ***Estimate field non-uniformity impact on time resolution vs pixel size***
- ***Determine minimal acceptable thickness for time resolution applications (SNR)***
- ***Investigate effects after irradiation up to $1e17 n_{eq}/cm^2$ in protons and neutrons***

Primary Goals

Test-Beam Setup

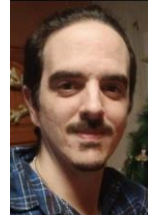
- ***Trigger Interface board:*** Versatile, allows interfacing any acquisition instrument with EUDET
- ***Control Software:*** Polymorphic UI with seeming-less multi-instrument support
- ***Cooling:*** XPS cold box with web interface temperature controllable system @ -18°C
- ***Mechanics:*** Micrometric alignment with individual DUT stages
- ***Analysis Framework:*** Advanced framework with signal shapes, iterative re-fitting and shape-based noise rejection

•Special Thanks to all the students who participated

2022 Test beam Periods



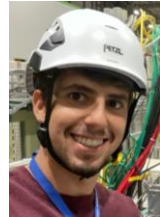
Jakob Haimberger
(CERN, TU Wien)



Oscar Ferrer
(CNM Barcelona)



Hanae Tilquin
(Imperial College
London)



Efren Rodriguez
(IGFAE)



Eloi Pazos Rial
(CERN)



**Alexandros
Athanasios Kapelios**
(CERN, Uni Glasgow)

2021 Test beam Periods



Efren Rodriguez
(IGFAE)



Marius Halvorsen
(CERN, Oslo University)



George Petrogiannis
(IFAE Barcelona)



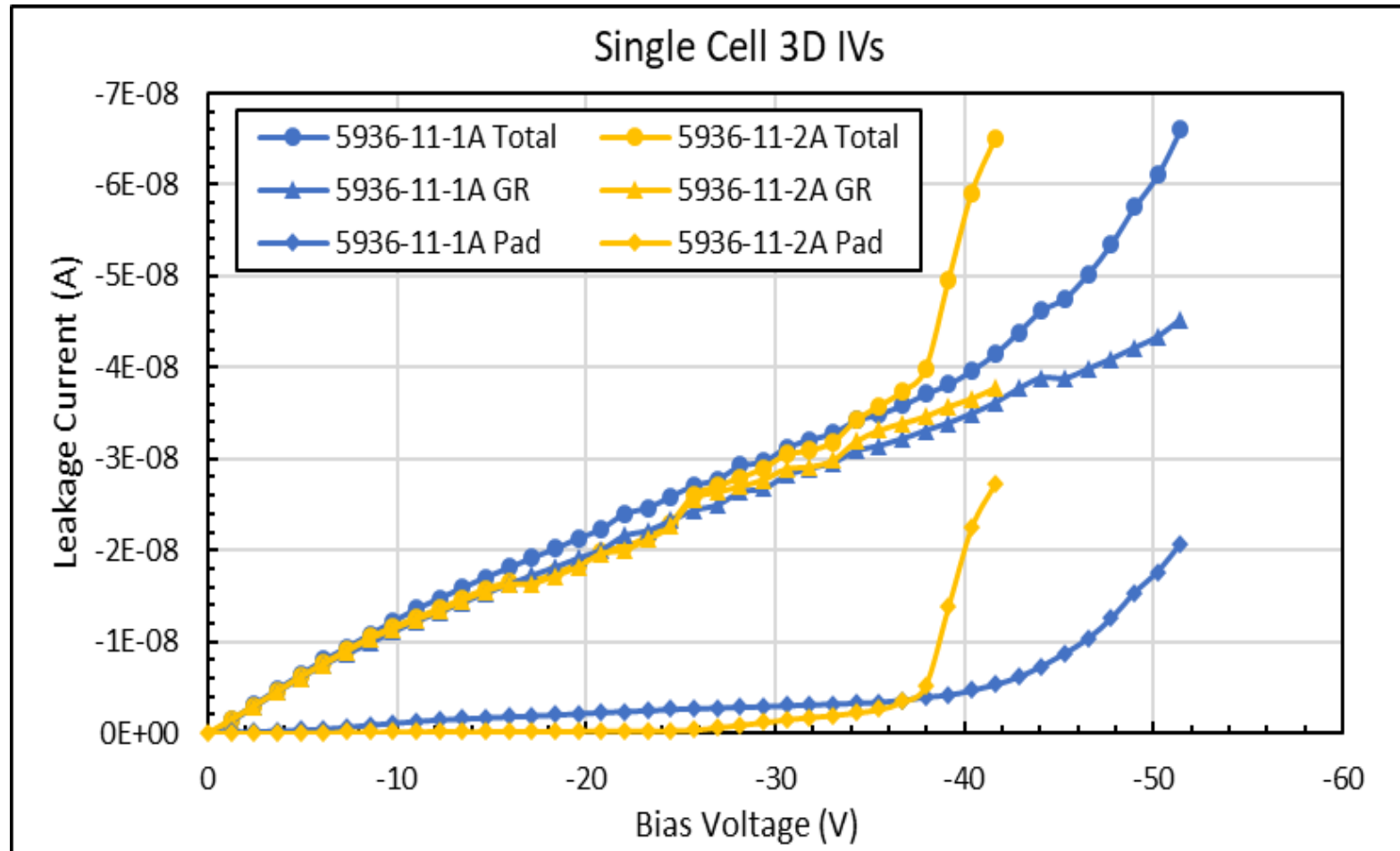
Pablo Fernández
(IFAE Barcelona)

•Backup



• Introduction

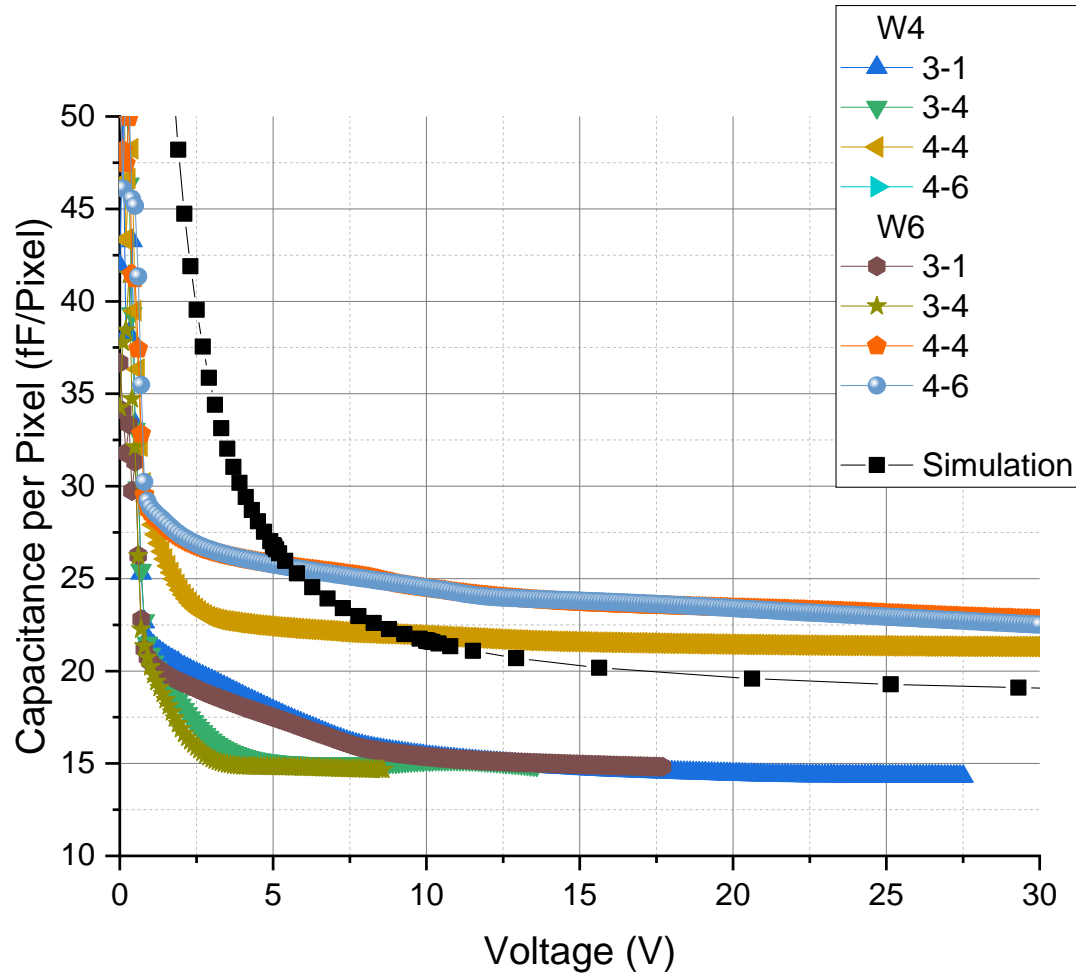
Test Geometry



- **Compliance:** 60 μ A
- **MAX Volt:** 60V
- **Voltage Step:** 1V
- **Setting time:** 2 sec
- **Averaging:** 5 measurements
- **Temperature:** 20°C

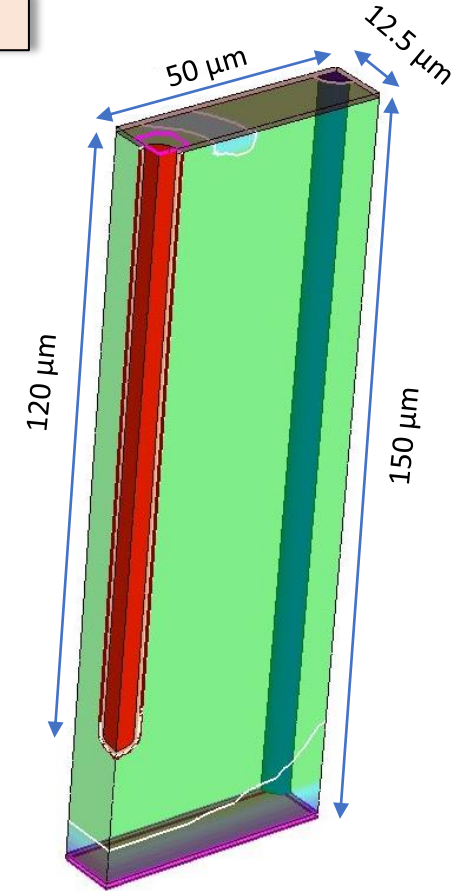
• Introduction

Capacitance



Oscar Ferrer, CNM Barcelona

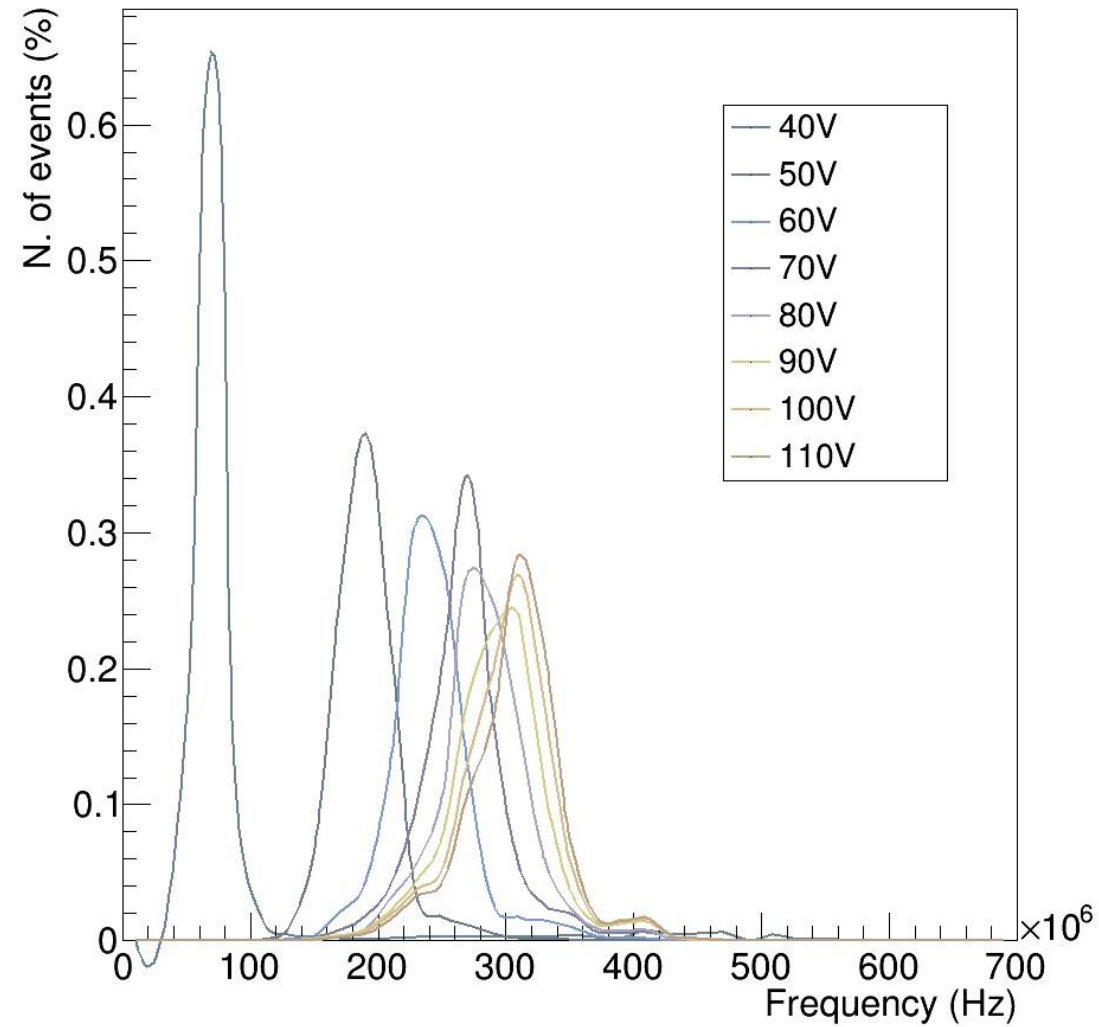
Simulated pixel:



- Single-Sided
- 150μm active thickness
- 120μm deep n+ columns
- Column diameter: 8μm
- P-stop radius: 12.5μm
- ¼ column simulated → applied mirror symmetry (computational time savings)

•Signal Evolution with bias in LGADs

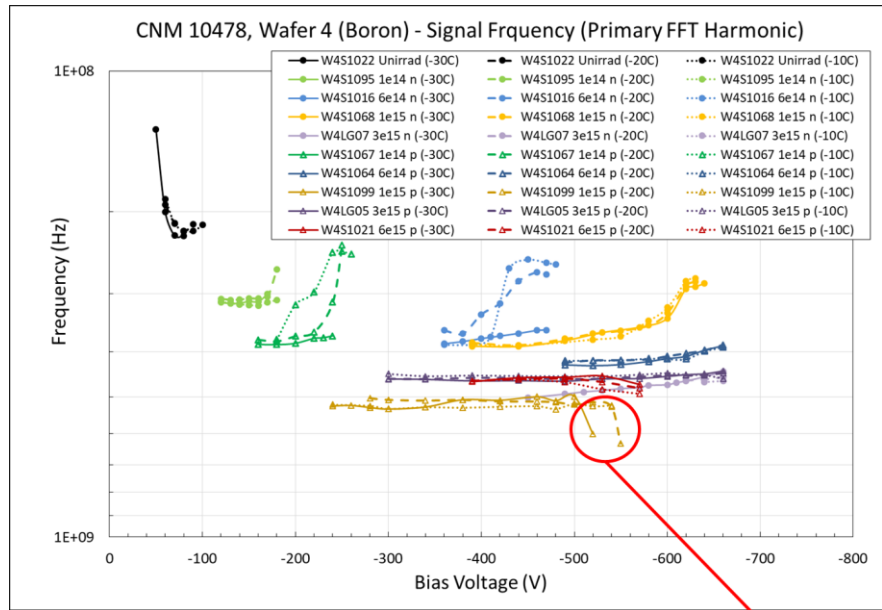
Signal FFT - 1e14n, -30C



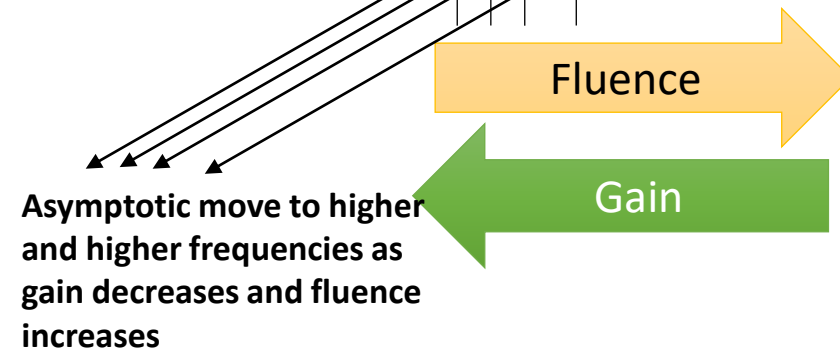
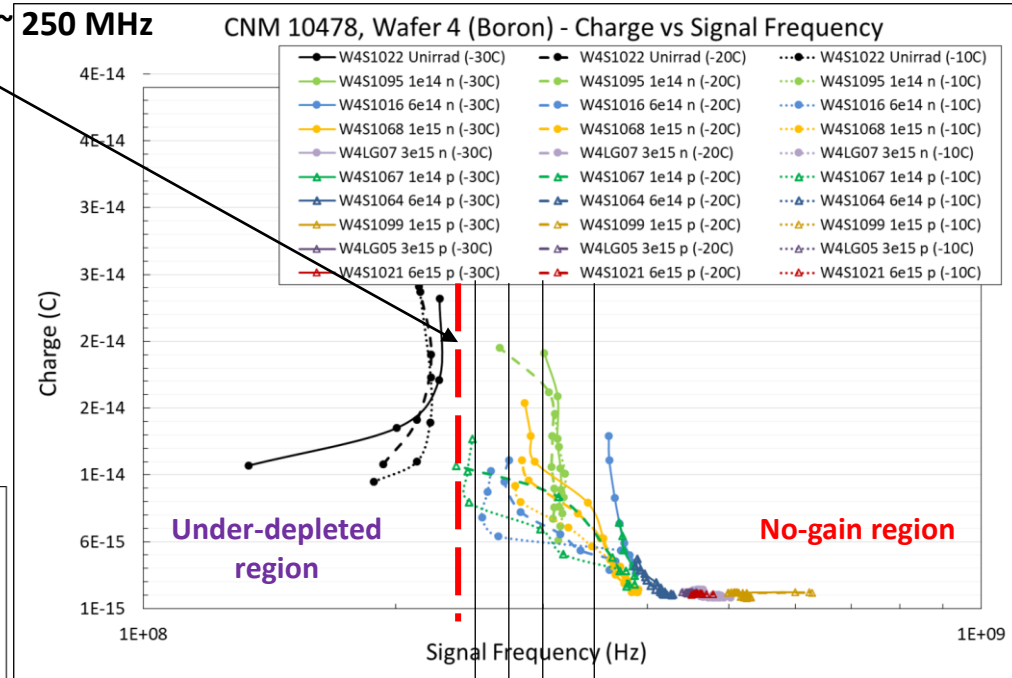
•Signal Analysis LGADs

FFT

- ✓ FFT vs Voltage presents an asymptotic behavior towards a frequency
- ✓ Asymptotic frequency depends on fluence and remaining gain
- ✓ Signal frequency increases with voltage and decreases on the onset of multiplication

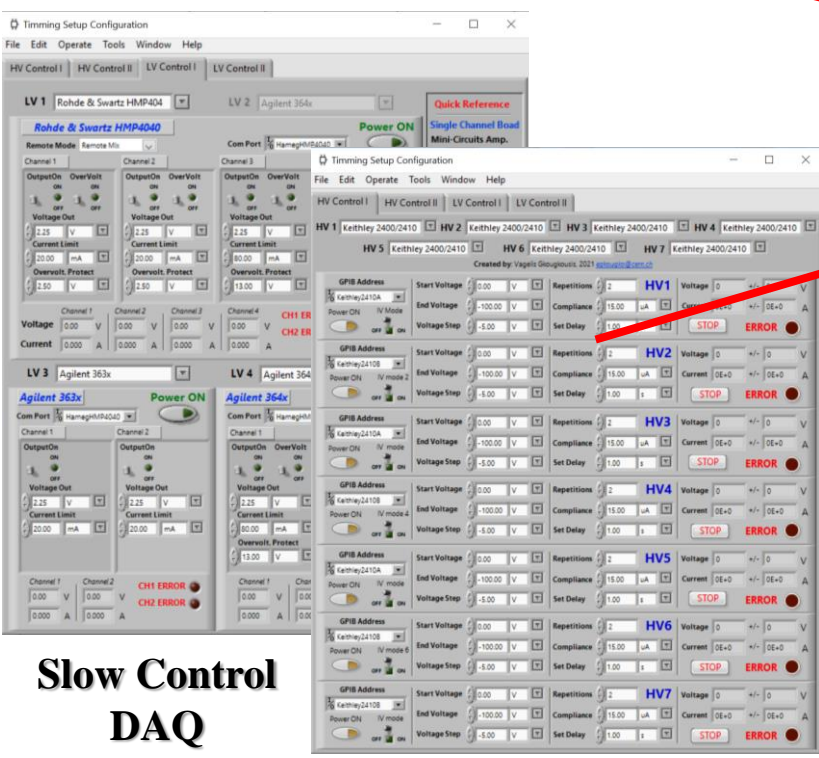
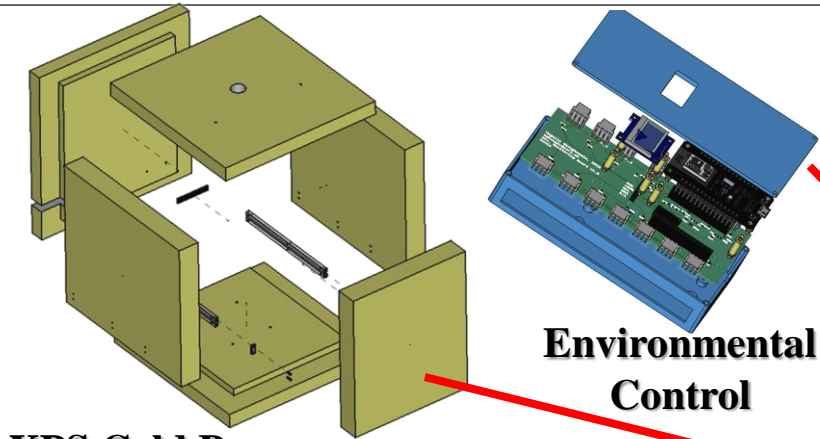


High Frequency noise, sensor in breakdown

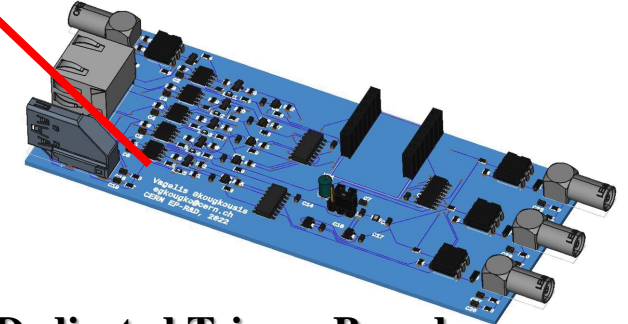
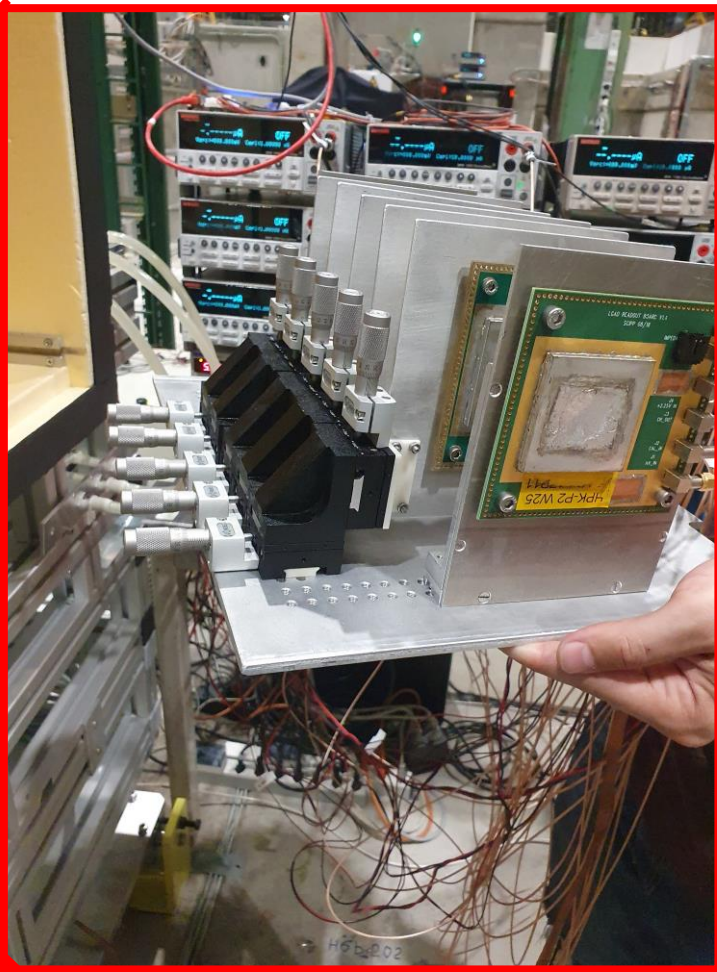
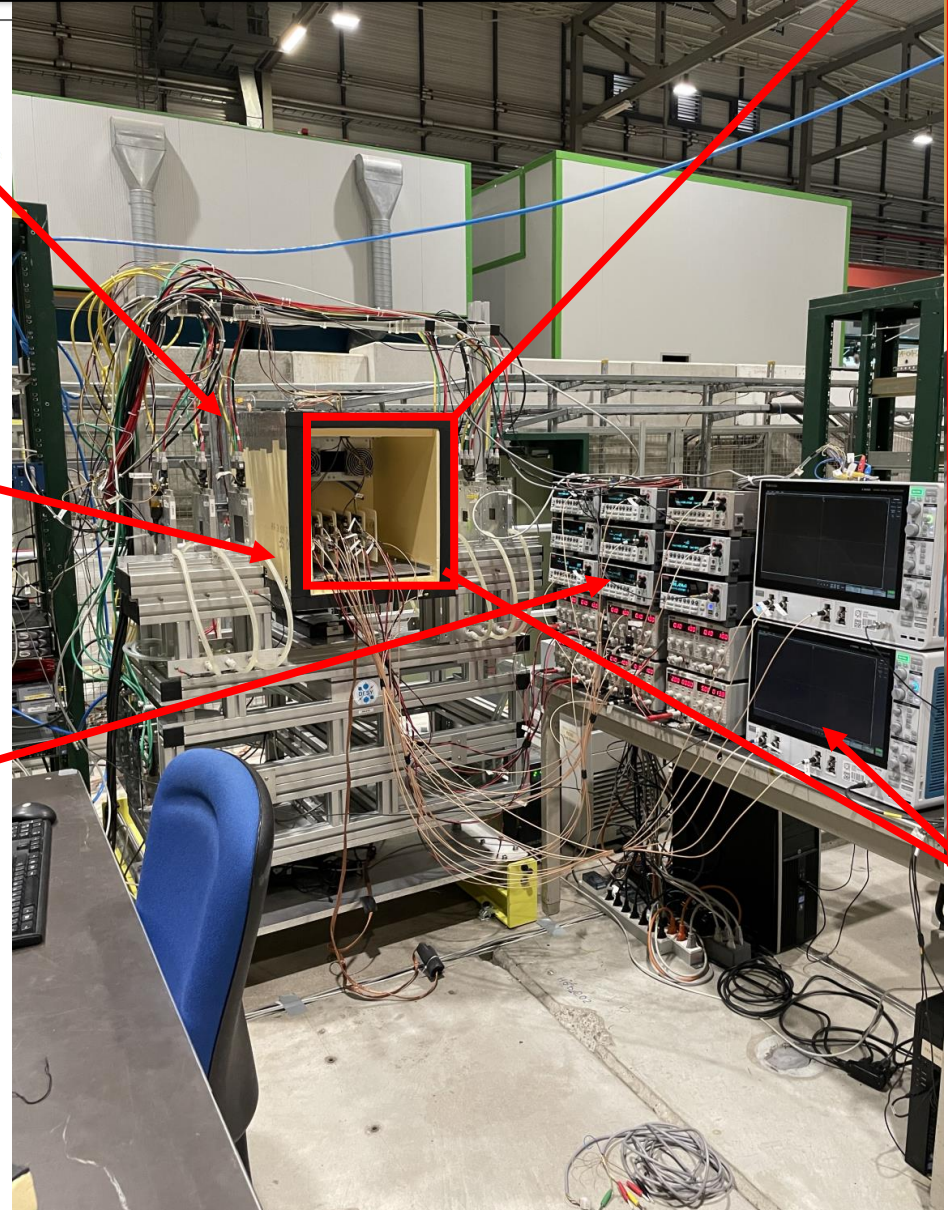


•Setup @ SPS

V. Gkougkousis, 10th Beam Telescopes and Test Beams Workshop
"Tracking the time: Single pixel 50 μ m pitch 3D cell time resolution map"

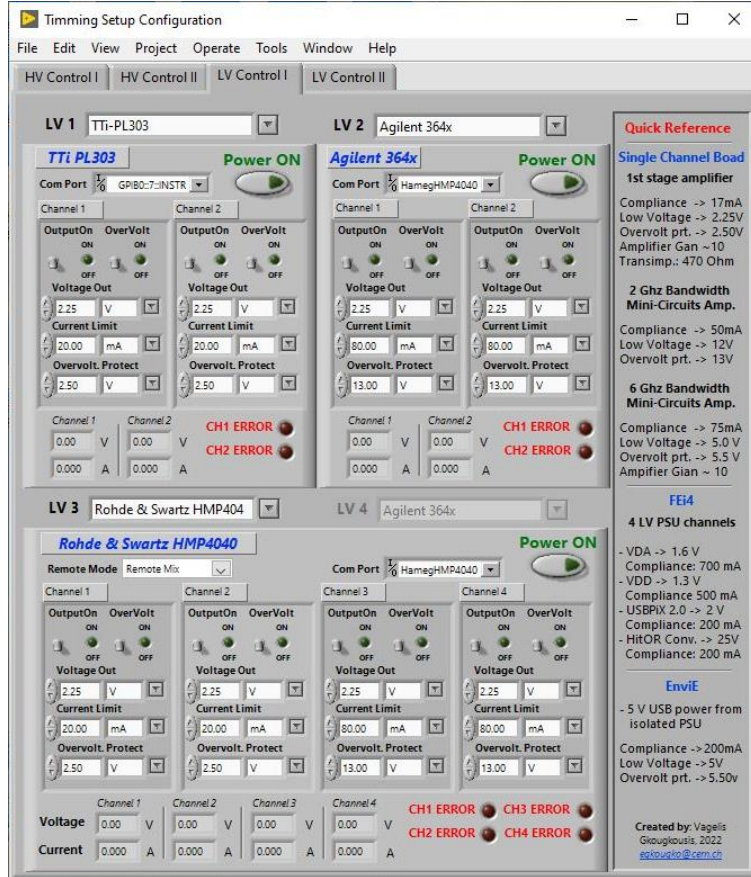


Slow Control DAQ

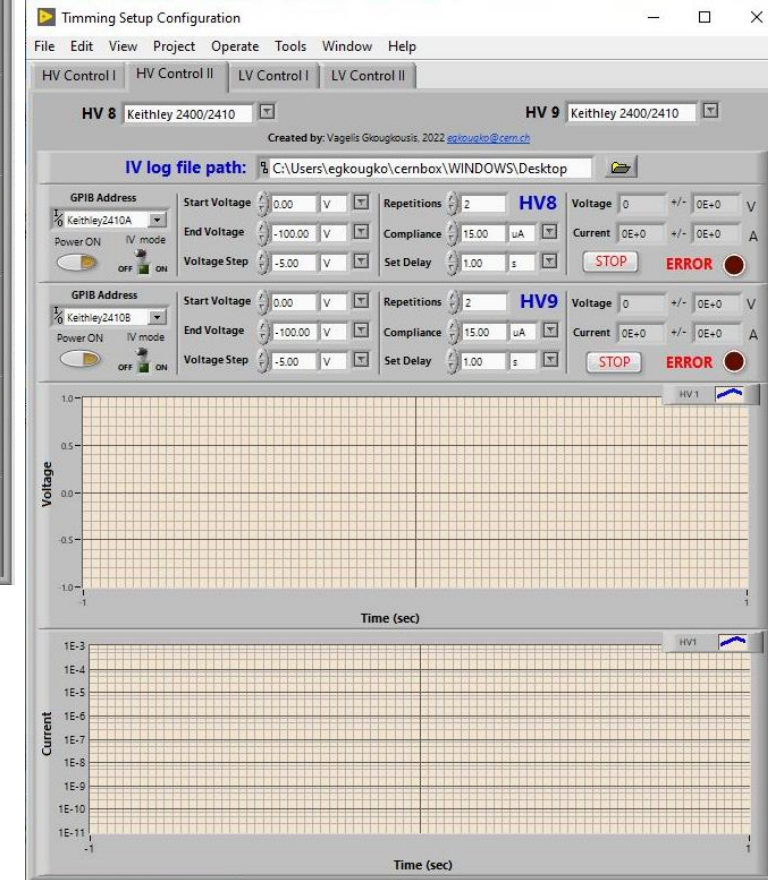


•HV & LV Control/monitoring

➤ Precompiled executable available on GitLab: [here](#)



Labview based



Multi-model Support with Polymorphic UI



- 9x HV channels
- 16x LV channels
- Constant monitoring & logging
- Live protection

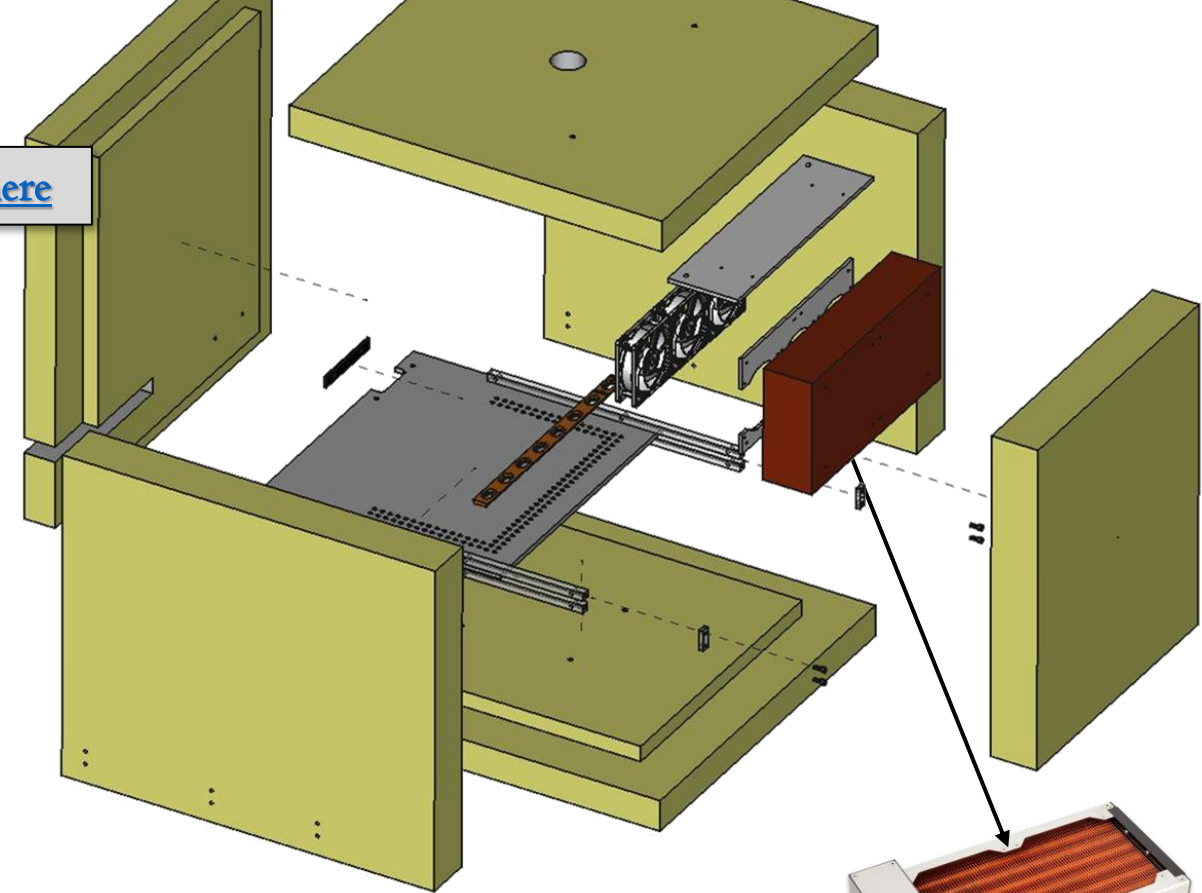
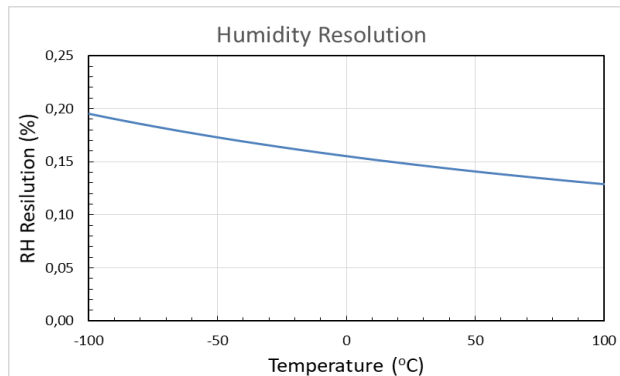
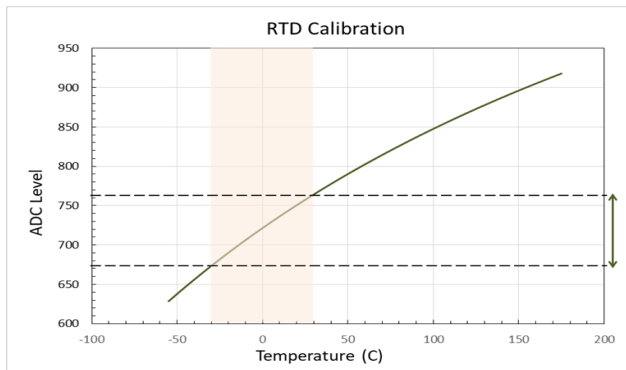
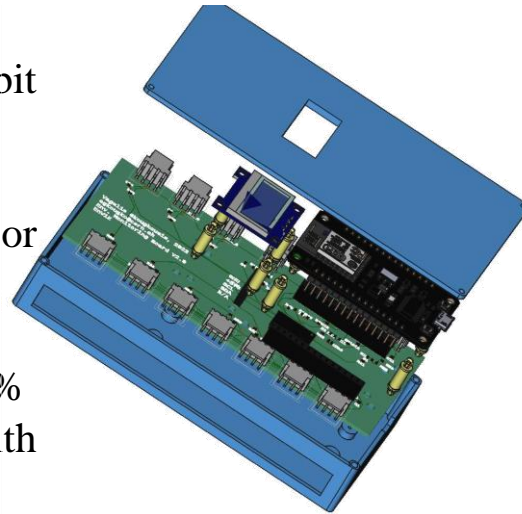
•Temperature Regulation

- Running at a crisp **-18 °C**
- Glycol cooling with temperature feedback - Labview control
- Humidity regulation though N₂ feeds

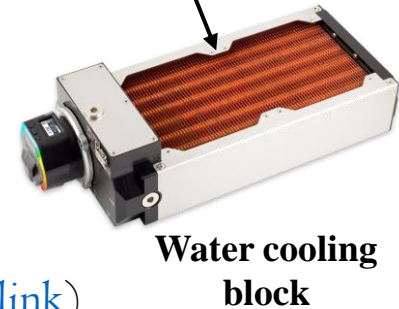
➤ EnviE GitLab with schematics: [here](#)

Environmental Expander V2.0 (EnviE)

- ESP8266 based with integrated 10-bit ADC, I2C and WiFi 802.11b
- Integrated OLED 128X64 pixel screen
- High precision voltage dividers and sensor decoupling
- ARDUINO / LoUA core web interface
- Temperature resolution of 0.8 °C ± 0.06 %
- Humidity resolution 0.1 % with temperature compensation

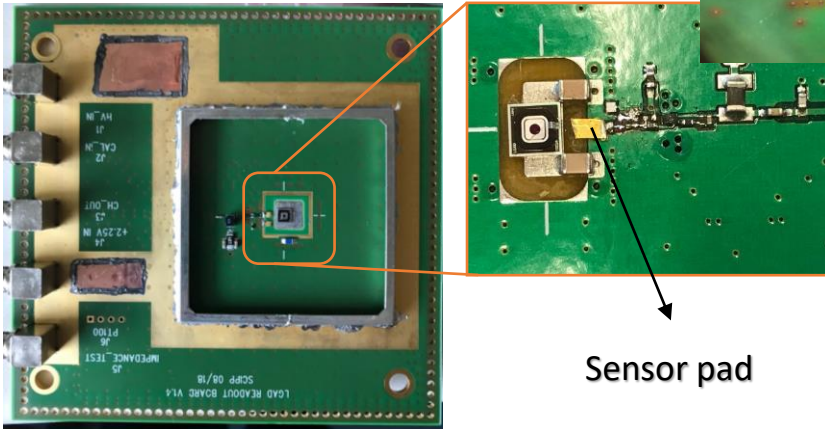
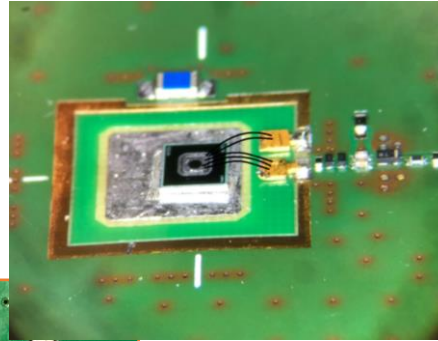
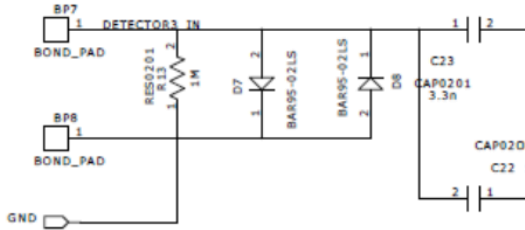


- 6 c.m. thick XPS foam insulation
- Outer dimensions of 50 x 48 x 48 cm³
- Use of commercial water-cooling block ([link](#))
- 3 x Axial Fan DC 80x80x25mm 24V 111.6m³/h – low temperature tested to -20°C ([link](#))
- Total cost ~ 400 CHF



Water cooling block

First Stage amplifier

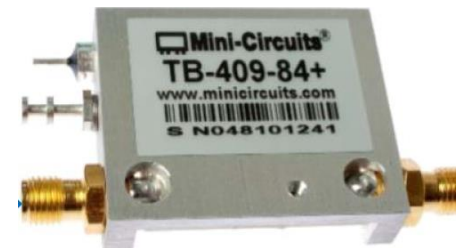


Sensor pad

- High frequency SiGe (~12 GHz) common emitter first stage charge amplifier (470Ω trans-impedance)
- Fully enclosed faraday cage surrounding sensor
- Mean sensor + amplifier noise < 1.8 mV
- Use of identical sensors for calibration and comparison

Second Stage amplifier

- Mini-circuits (Gali 52+) Gallium arsenate voltage amplifier with a 2 GHz bandwidth for LGADs
- Mini-circuits (ZX60-V63+) 6 GHz microwave voltage amplifier for 3D and planar planes
- Amplification factor of ~ 10 at 12 and 5 V respectively
- Amplifiers mounted directly on the boards and placed inside the cold box



Gali 52+
GaAs , <2Ghz, 50Ω



ZX60-V63+
50 - 6000 MHz, 50Ω

• Analysis Framework

➤ Code available on git: <https://gitlab.cern.ch/egkougko/lgadutils>

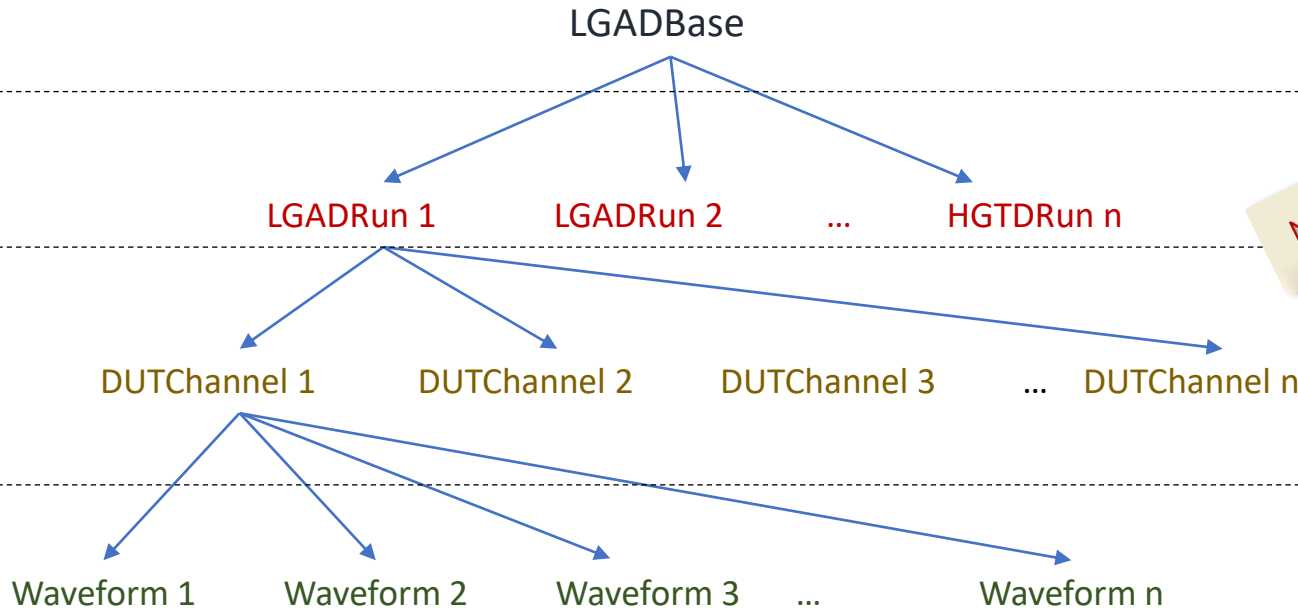
➤ Four main classes with dedicated header and implementation files, one wrapper class handling user interaction

- **LGADUtils** → Wrapper to handle user I/O and pass arguments
- **LGADBase** → Basic framework function and infrastructure
- **LGADRun** → Timing resolution, CFD maps, multi DUT operations
- **LGADChannel** → Mean pulse shape, mean pulse properties form entire run
- **WaveForm** → Single Waveform properties and time walk corrections
- **Bonus: LGADSel** → **Selector Class with auto-set 64 channel support**

C++ 11

- Iterative re-fitting and re-binning algorithm
- Fitting of discrete and variable binning quantities
- Bayesian uncertainties at efficiency level
- Event by event FFT transimpedance correction

4th level 3rd level 2nd level 1st level



$N/(dV/dt) \cdot T_{Rise}/SNR$

Quantity	Applied Fit type
Min, Max voltage :	Gauss, Gauss x Landau fit
Start, stop, min, max indices :	Gauss, Gaussian fit
Noise / pedestal :	Gaussian fit
Min, Max, Rise, Trigger time :	Gaussian fit
Charge, dV/dT, <u>Jitter</u> , ToT:	Gauss x Landau fit
FFT:	Variable bin Gaussian

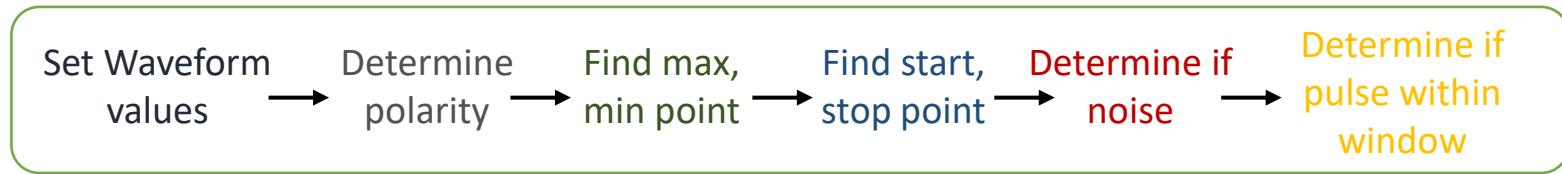
•Event by Event Strategy

File: WaveForm.cxx

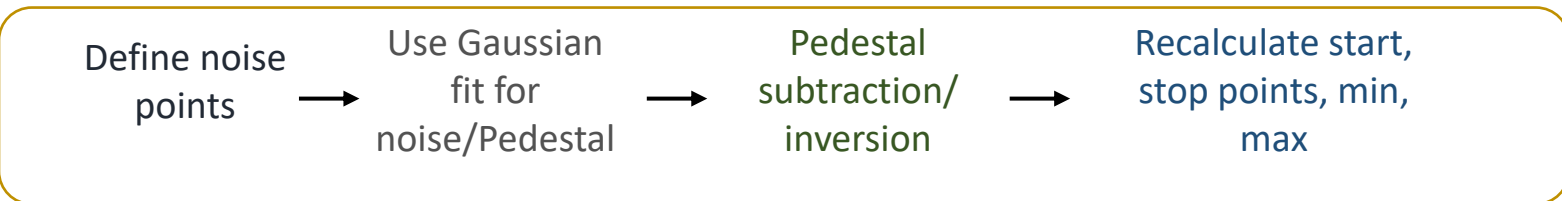
- A four sequential step analysis approach
- Analysis escalates in a pyramid structure

Five preliminary sequential steps before we even start looking at the waveform

Step 1



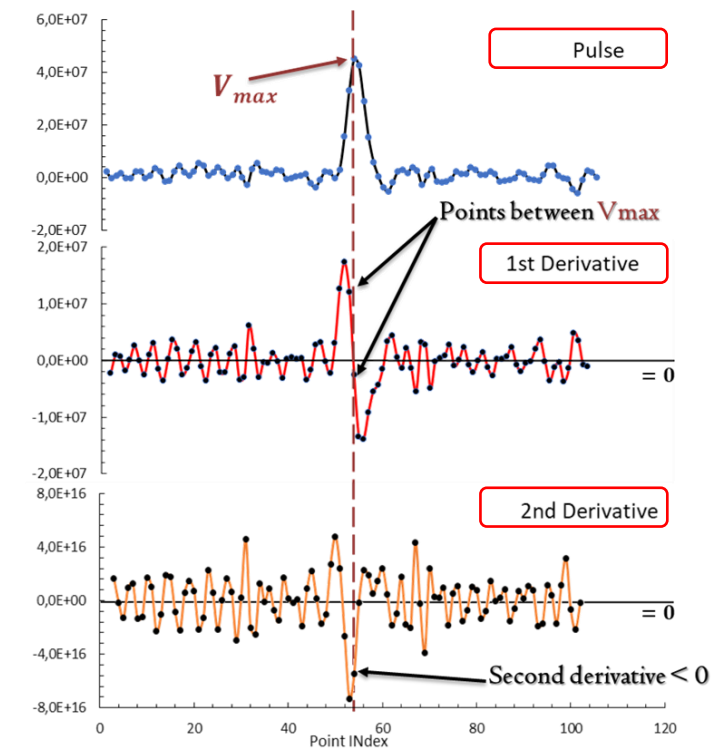
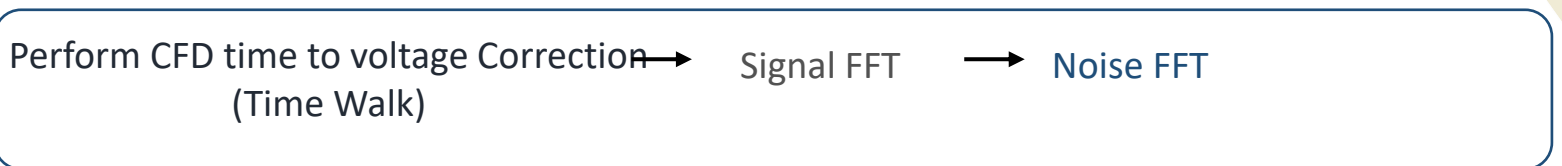
Step 2



Step 3



Step 4



- If $SD_{maxima} > SD_{minima}$
 $D_m(max) > D_m(min)$
 - or $SD_{maxima} < 1.05 * SD_{minima}$
 $D_m(max) > D_m(min)$
- } Positive polarity

IsNoise Algorithm

- Number of points with $0.8 \cdot V_{max}$
- If $N_{points} > 2$ test 0.7, 0.6 & $0.5 \cdot V_{max}$ to account for wavy waveforms
- If $N_{points} > 2$ then require $dN_{points} < 8/12/16/20$

Iterative Re-fitter & signal templates

File: LGAD Fits.cxx

- Centralized fitter engine for all fits
- Fully automated, including limits, method and Minuit minimization
- 36 Iterations per fit with limits and bin size variation to determine best combination
- Over-binning protection, automatic variable discreteness test
- Variable binning for FFT, frequency histograms
- Supported ROOFit, Standalone Minuit, Integral optimization or Shape

Template Method

- Point by Point projection of all time-walk corrected (though CFD) signal pulses
- Landau X Gauss fit on projected point by point distribution
- Extraction of a “characteristic” signal composed of the MPVs of the Point by point projection fits
- RooKeyPdf for analytical description of signal
- Re-iteration on all events and fit of each waveform with the extrapolated analytical signal description
- Re-calculate all quantities

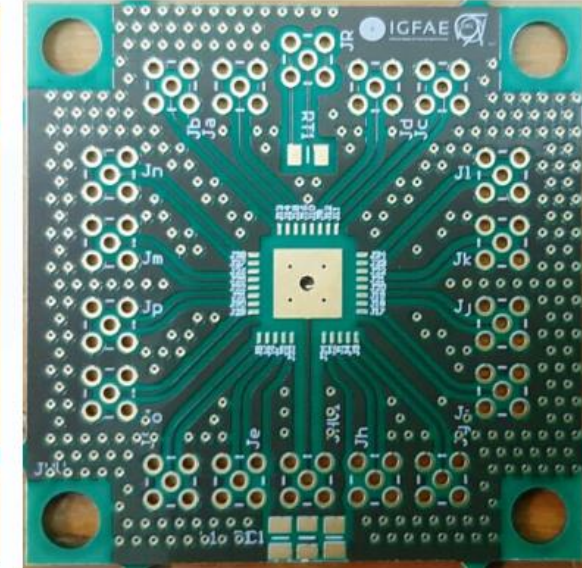
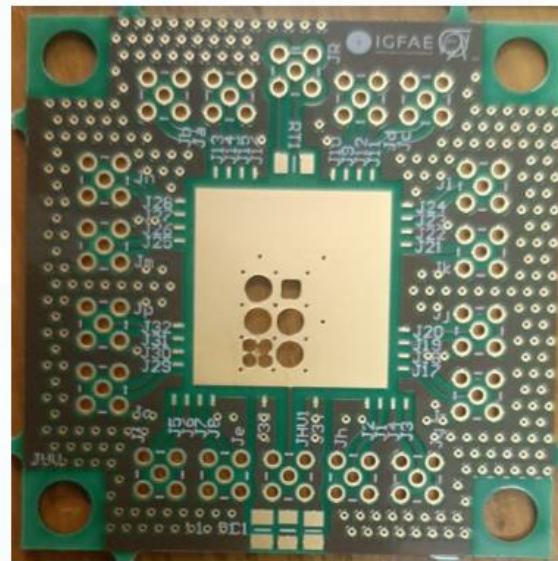
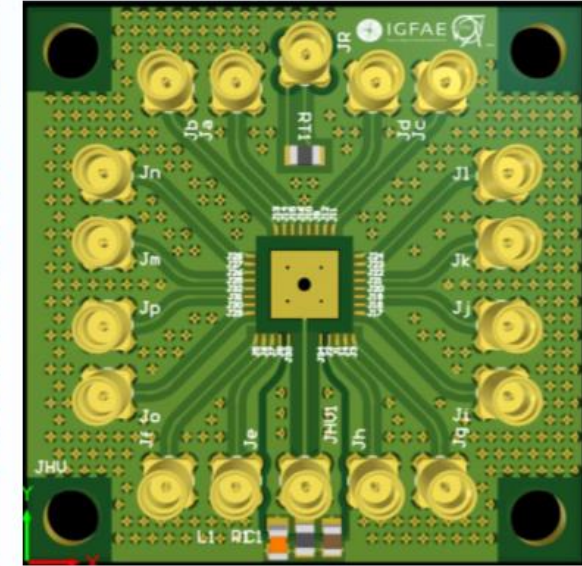
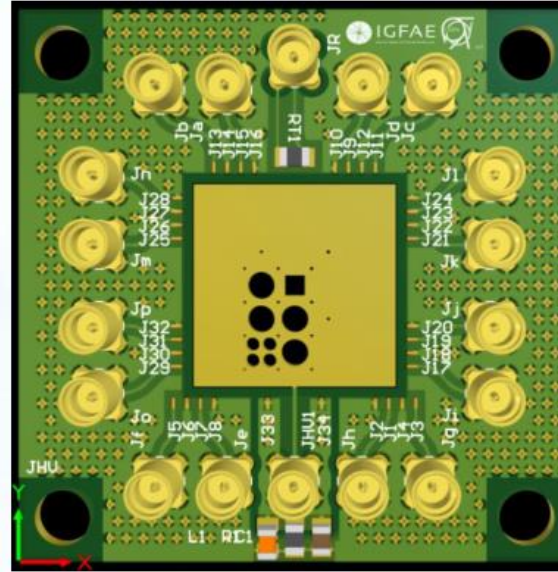
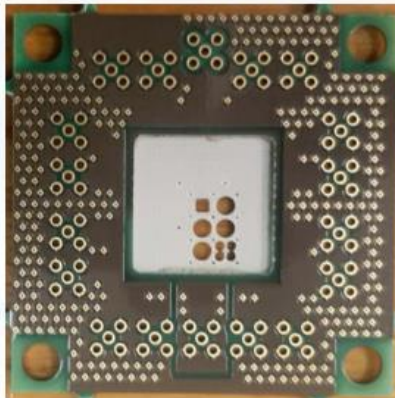
Dataset Type	Statistic Categorization	Bin Selection Criteria		
		Lower 3 bin number variations	Optimum Bin number	Higher 3 bin number variations
Discrete Datasets	$\frac{ \lim High - \lim Low }{\sigma_{fit}} < \sqrt{N_{elements}} < N_{bins_max}$	$\left\lceil \left[\sqrt{N_{elements}} - n \times \left \frac{\sqrt{N_{elements}} - \frac{ \lim High - \lim Low }{\sigma_{fit}}}{3} \right \right] \right\rceil$ with $1 < n < 3$	$\lfloor \sqrt{N_{elements}} \rfloor$	$\left\lceil \left[\sqrt{N_{elements}} + n \times \left \frac{N_{bins_max} - \sqrt{N_{elements}}}{3} \right \right] \right\rceil$ with $1 < n < 3^{**}$
	$\sqrt{N_{elements}} \leq \frac{ \lim High - \lim Low }{\sigma_{fit}} < N_{bins_max}$	Lowest bin number	Rest of the bin number array	
	$\sqrt{N_{elements}} \leq N_{bins_max} < \frac{ \lim High - \lim Low }{\sigma_{fit}}$	$\lfloor \sqrt{N_{elements}} \rfloor$	$\lfloor \sqrt{N_{elements}} \rfloor + n \times \left\lfloor \frac{N_{bins_max} - \sqrt{N_{elements}}}{6} \right\rfloor$ with $1 < n < 6^{**}$	
	$N_{bins_max} \leq \sqrt{N_{elements}}$	$n \times \lfloor N_{bins_max} / 7 \rfloor$ with $1 < n < 7$		
Continuous Datasets	$\frac{ \lim High - \lim Low }{\sigma_{fit}} < \sqrt{N_{elements}}$	$\left\lceil \left[\sqrt{N_{elements}} - n \times \left \frac{\sqrt{N_{elements}} - \frac{ \lim High - \lim Low }{\sigma_{fit}}}{3} \right \right] \right\rceil$ with $1 < n < 3$	$\lfloor \sqrt{N_{elements}} \rfloor$	$\left\lceil \left[\sqrt{N_{elements}} + n \times \left \frac{ \lim High - \lim Low }{\sigma_{fit}} \right \right] \right\rceil$ with $1 < n < 3$
	$\sqrt{N_{elements}} \leq \frac{ \lim High - \lim Low }{\sigma_{fit}}$	$\left\lceil \left[\frac{ \lim High - \lim Low }{\sigma_{fit}} - n \times \left \frac{ \lim High - \lim Low }{\sigma_{fit}} - \sqrt{N_{elements}} \right \right] \right\rceil$ with $1 < n < 3$	$\left\lceil \frac{ \lim High - \lim Low }{\sigma_{fit}} \right\rceil$	$\left\lceil \frac{ \lim High - \lim Low }{\sigma_{fit}} + n \times \left\lfloor \frac{\sqrt{N_{elements}}}{3} \right\rfloor \right\rceil$ with $1 < n < 3$

•Sensor Daughterboard

Sensor board

- Two types of designs. (15x15 mm and 5x5 mm central pad).
- 41 x 41 mm square shape.
- Rogers 4350B for the high speed signals.
- Connector area reinforce with 0.3 μm FR4.
- Under sensor pad thickness of 100 μm .
- Multiple drills design on the central pad to place different types and sensors sizes.
- 140 boards produced at [Gacem](https://www.gacem.com).

Back side



•H/W and S/W developed

Category	Function	Description	Github Project Link
Electronics	TiB Board	Interface and synchronize oscilloscope with AIDA TLU	Trigger Interface Board - TiB
	FEi4 HitOr Converter	Convert CMOS level output to TTL level necessary for ROI trigger	HitOr Converter
	Environmental Expander (EnviE)	Monitor Temperature / Humidity at DUT level	Environmental Monitoring Expander - EnviE
	Front-End readout board	12 GHz fast transimpedance amplifier with integrated faraday cage	Single Channel Board
Mechanics	Cold-Box and DUT Support	XPS foam enclosure for -20C operation and individual DUT alignment	Test beam Mechanics
Software	Oscilloscope Fast DAQ	SCPI layer DAQ program for oscilloscope readout	Oscilloscope DAQ
	Power/ Temp Control Software	Labview based Low Voltage and HV control software with integrated single event burnout protection	TiCAS - Timing Control Automation Software
	Trimming analysis Software	LGADUtils timing analysis framework	LGADUtils

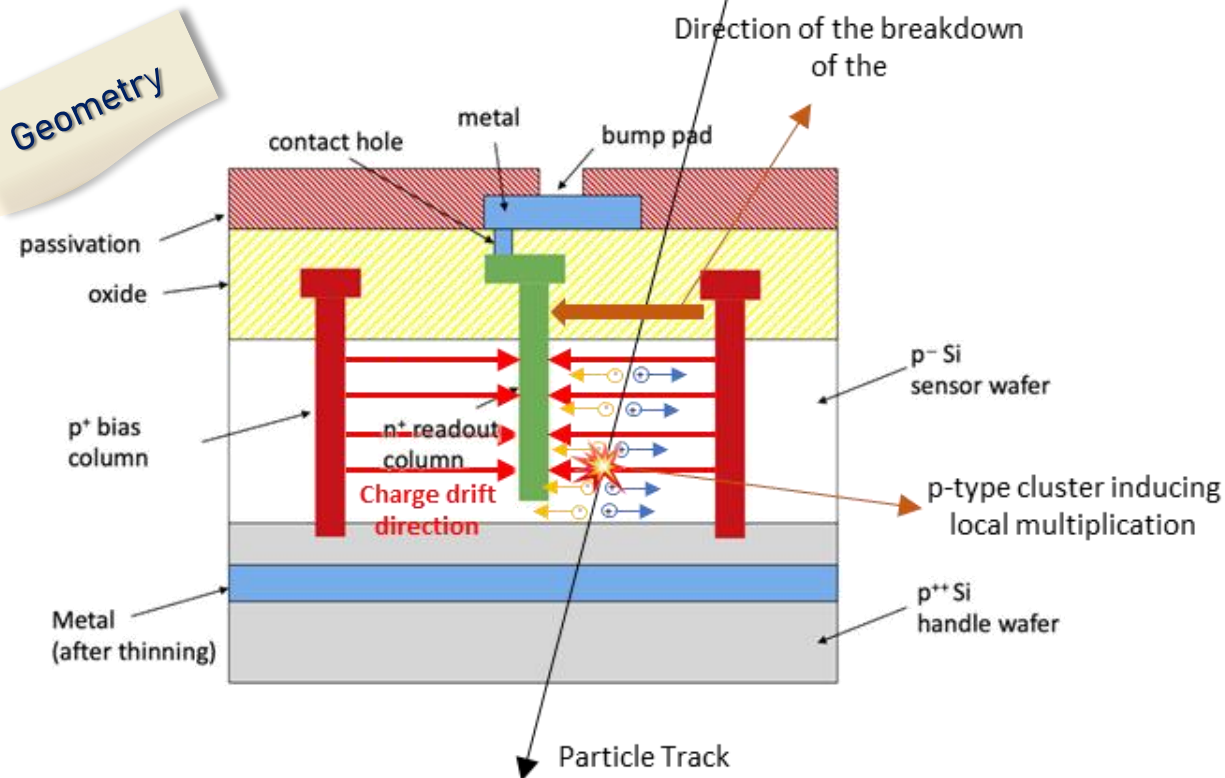
•Single Event Burn-out



Single Event Burn-Out was also observed in 3D sensors but in much Higher fields than LGADs ($\sim 30 \text{ V} / \mu\text{m}$) but $1\text{e}16$ (not $1\text{e}17$)

BUT

Sensors don't need to operate at that region, efficient at lower fields



3D Single Pixel structures

Geometry	Irradiation		Operating Conditions	
	Species	Flu. (n_{eq}/cm^2)		
CNM 13680-6 Single 3D 25 x 100	Unirradiated		-50 V	1.4 V/ μm
	Fast Neutrons @ JSI	1×10^{15}	-180 V	5.1 V/ μm
		8×10^{15}	-310 V	8.7 V/ μm
		6×10^{16}	-360 V	10.1 V/ μm
		1×10^{17}	-410 V	11.5 V/ μm
PS protons (24 GeV/c)		1×10^{15}	-210 V	5.9 V/ μm
		8×10^{15}	-180 V	5.1 V/ μm
CNM 5936-11 Double 3D 55 x 55	Unirradiated		60 V	3.2 V/ μm
	Fast Neutrons @ JSI	1×10^{15}	-160 V	8.5 V/ μm
		8×10^{15}	-170 V	9.0 V/ μm
		6×10^{16}	-120 V	6.3 V/ μm
		1×10^{17}	-200 V	10.6 V/ μm
CNM 13680-6 Single 3D 50 x 50	Unirradiated		-50 V	2.6 V/ μm
	Fast Neutrons @ JSI	1×10^{15}	-200 V	10.3 V/ μm
		6×10^{16}	-320 V	16.5 V/ μm
		1×10^{17}	-340 V	17.6 V/ μm
	PS protons		1×10^{15}	-180 V